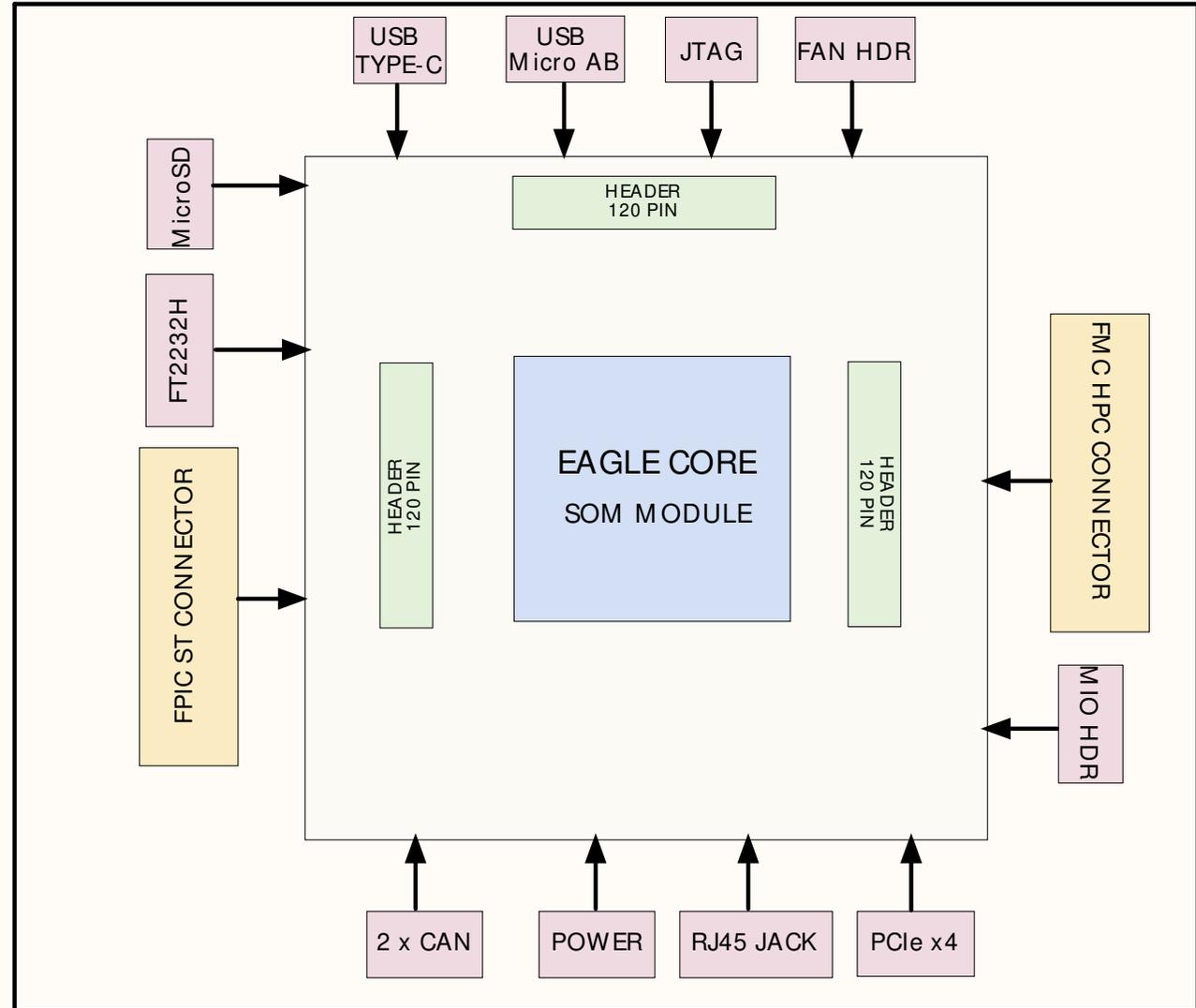
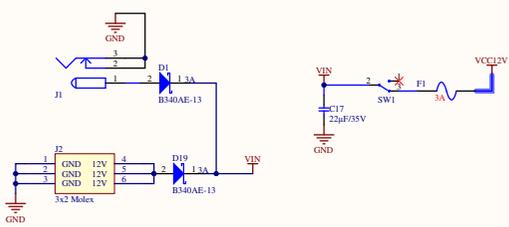


BLOCK DIAGRAM

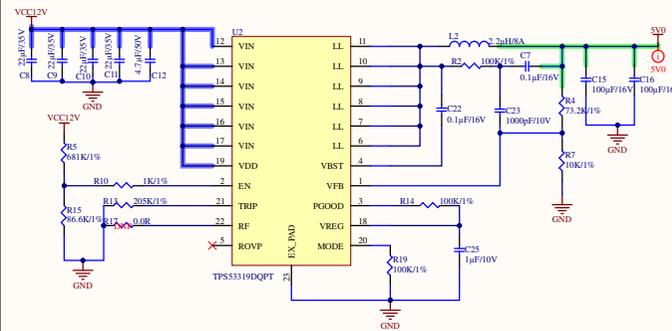
Sheet No	Contents	Sheet Name
1	Block diagram	Block diagram
2	Power Supply, Fan HDR	Power Supply
3	RJ45,CAN, SD CARD Mode sel, FPIC, VBAT RESET SW	Ethernet
4	FTDI, USB C USB MICRO AB	USB
5	IO CON, MMCX, LED	CONN
6	FM C HPC CONN	FM C CONN
7	PCIe x4, JTAG, MIO HDR	PCIe JTAG



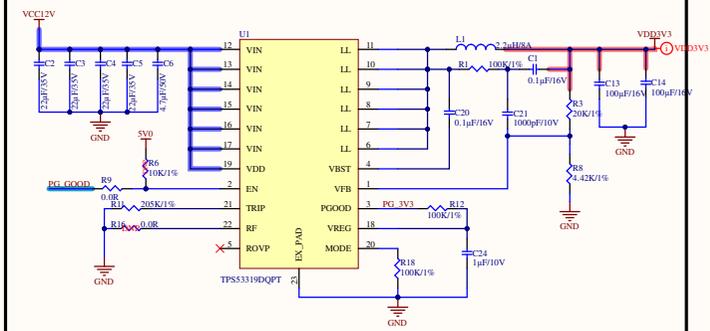
INPUT POWER 12V0



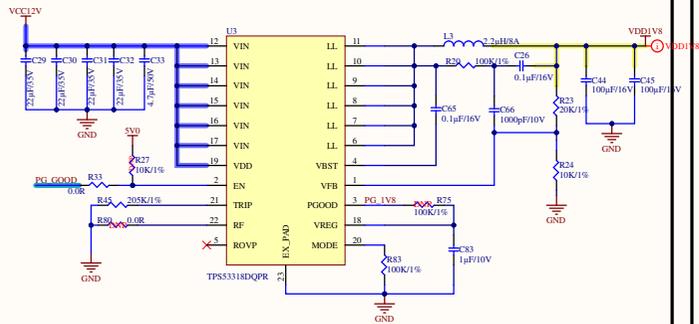
POWER 5V0



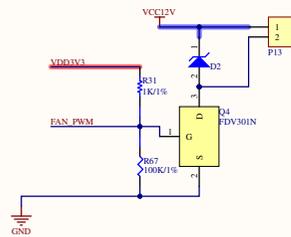
POWER 3V3



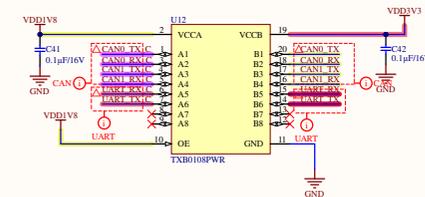
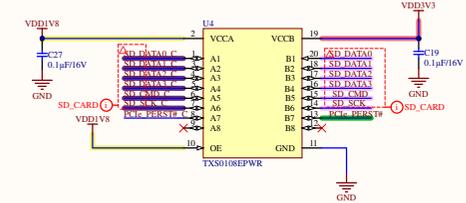
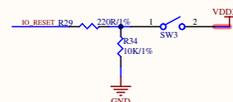
POWER VDD1V8



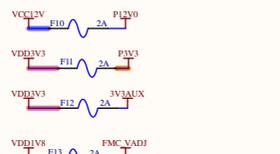
FAN HDR



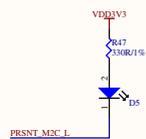
IO RESET



FMC INPUT FUSE



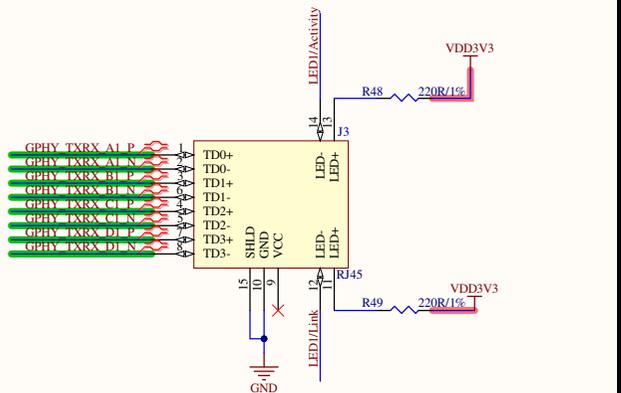
FMC PRSNT SIG



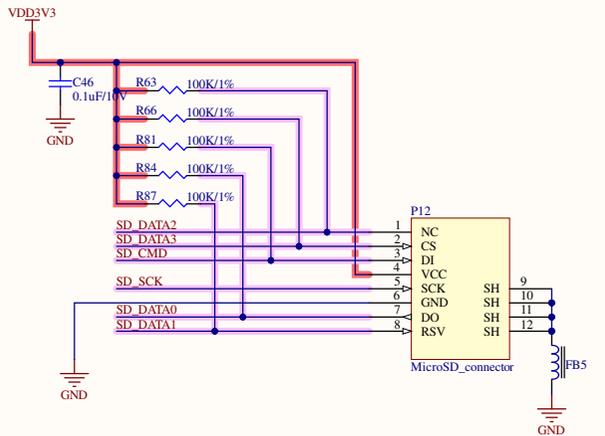
POWER OK



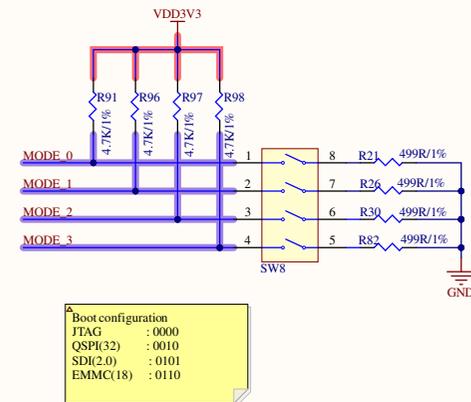
ETHERNET



MicroSD Slot

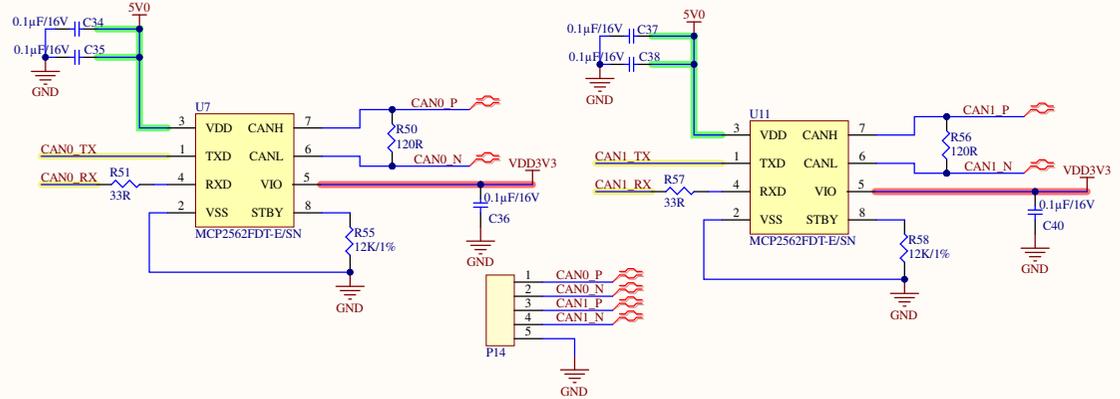


MODE SEL SW

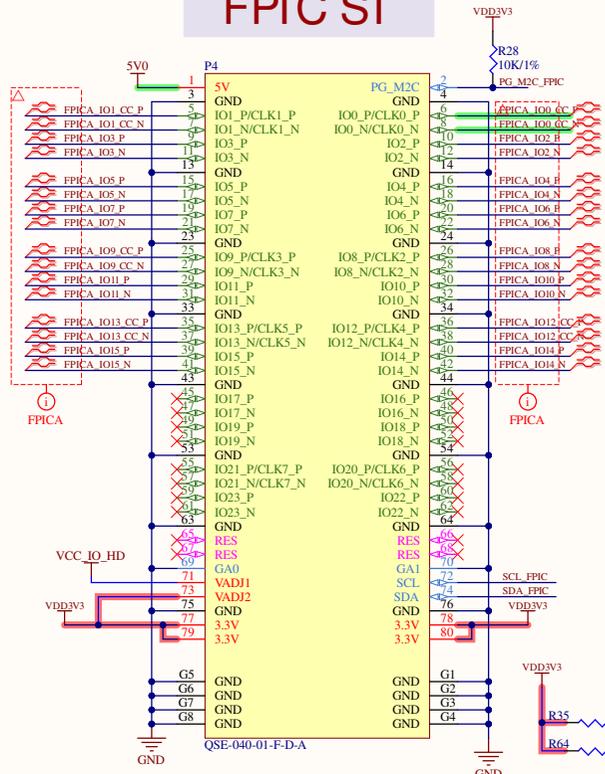


Boot configuration
 JTAG : 0000
 QSPI(32) : 0010
 SDI(2.0) : 0101
 EMMC(18) : 0110

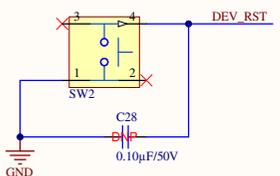
2x CAN



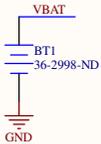
FPIC ST



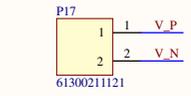
DEVICE RESET



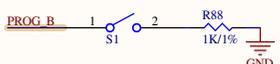
VBAT



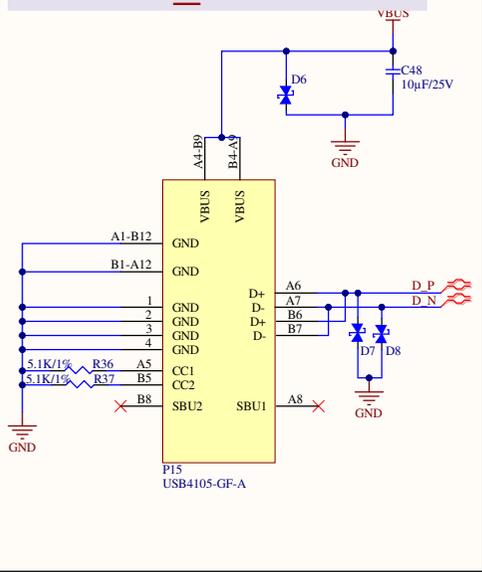
XADC



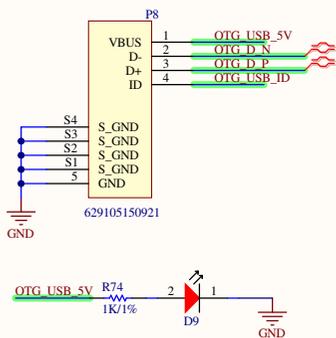
PROGB



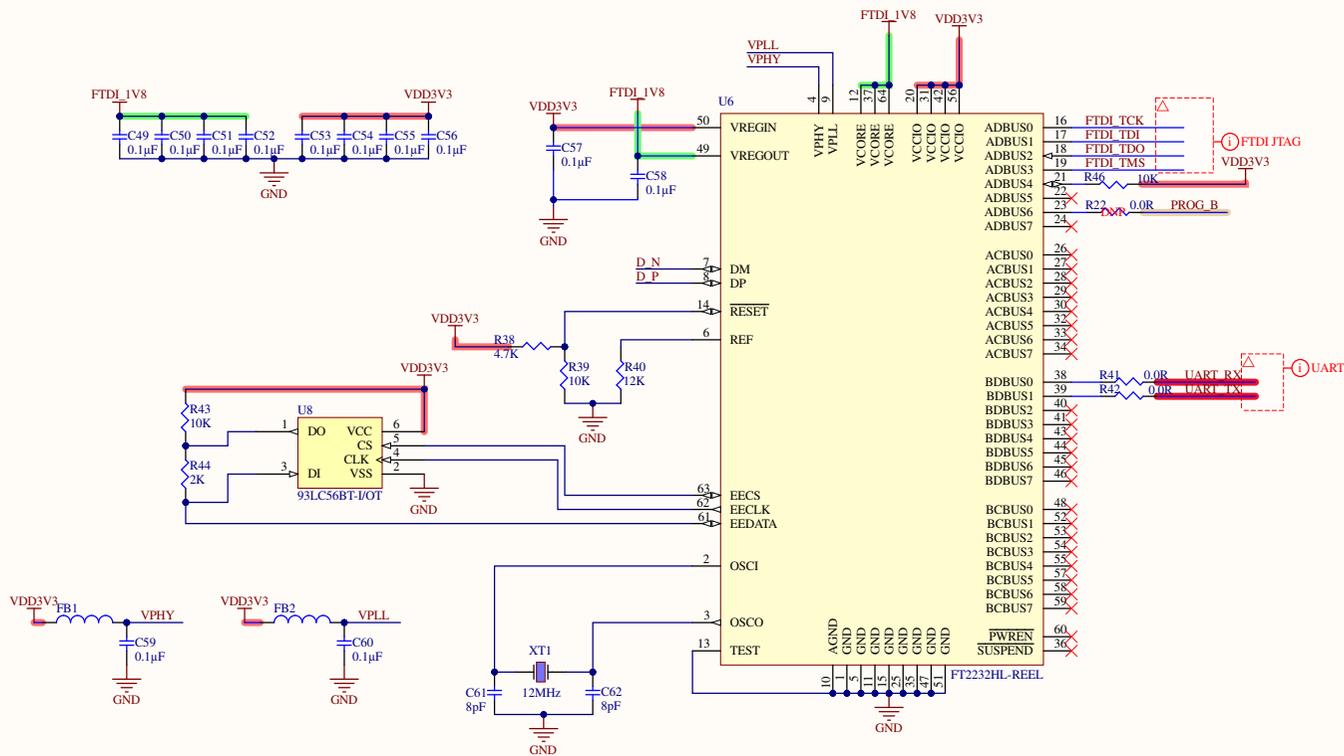
USB_TYPEC



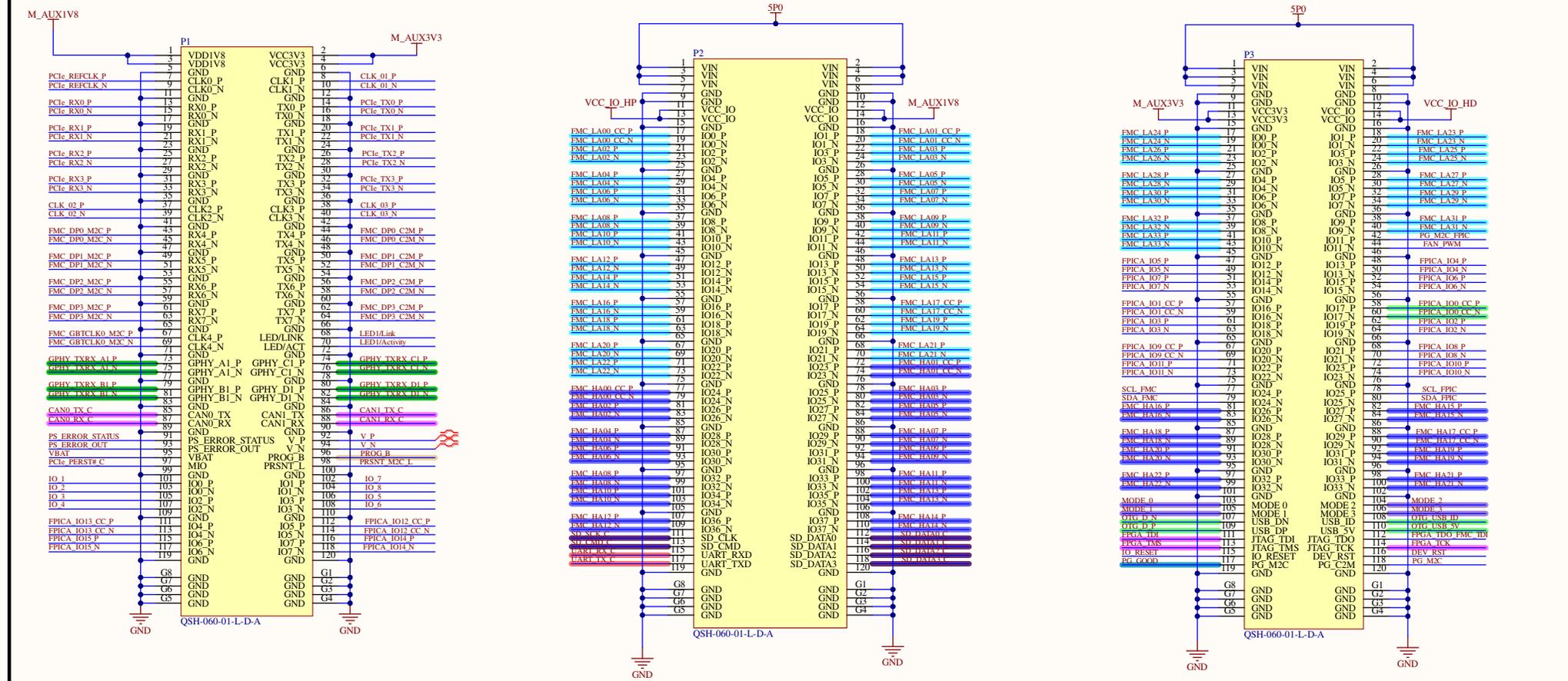
USB MICROAB



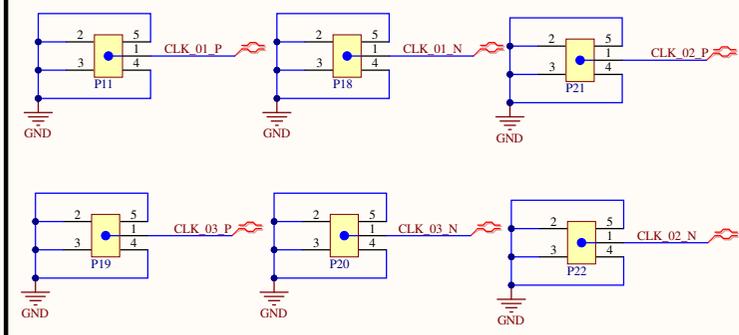
FTDI



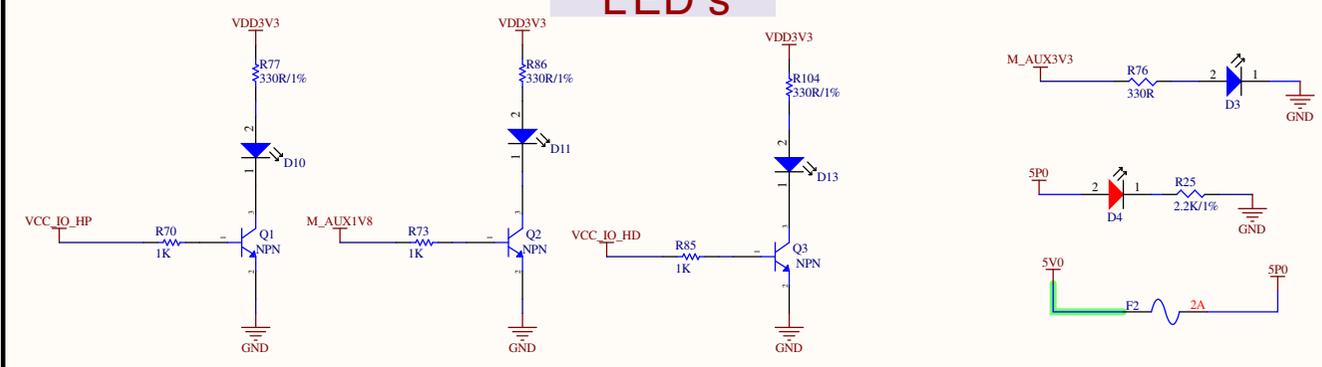
CONNECTOR



MMCX CON



LED'S



Title: CONNECTOR	Revision: A1		
Size: A3	Project: EagleCore_C02.PrjPcb		
Date: 25.02.2026	Time: 09:48:58		Sheet 5 of 7
File: CONN.SchDoc			

