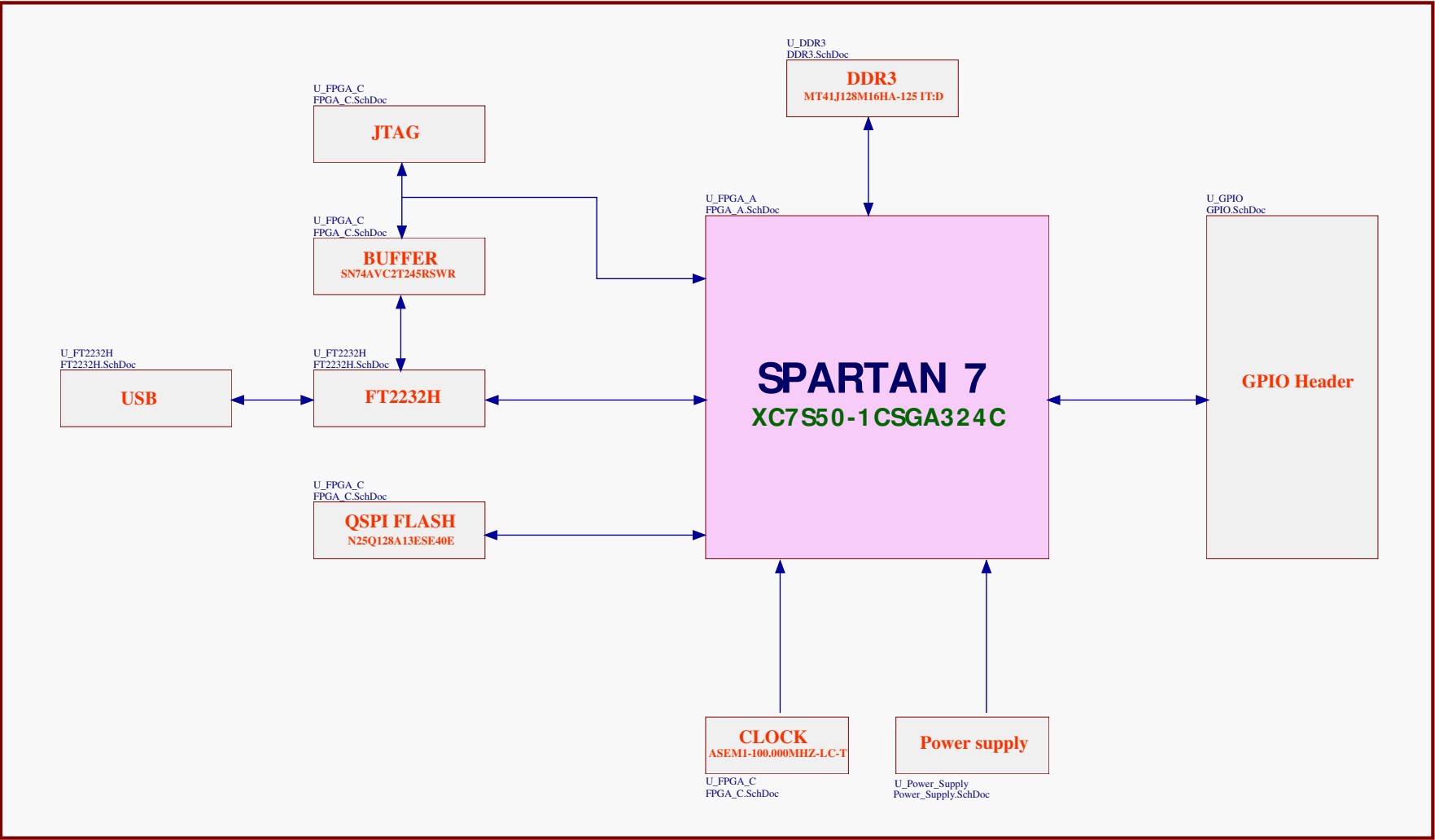
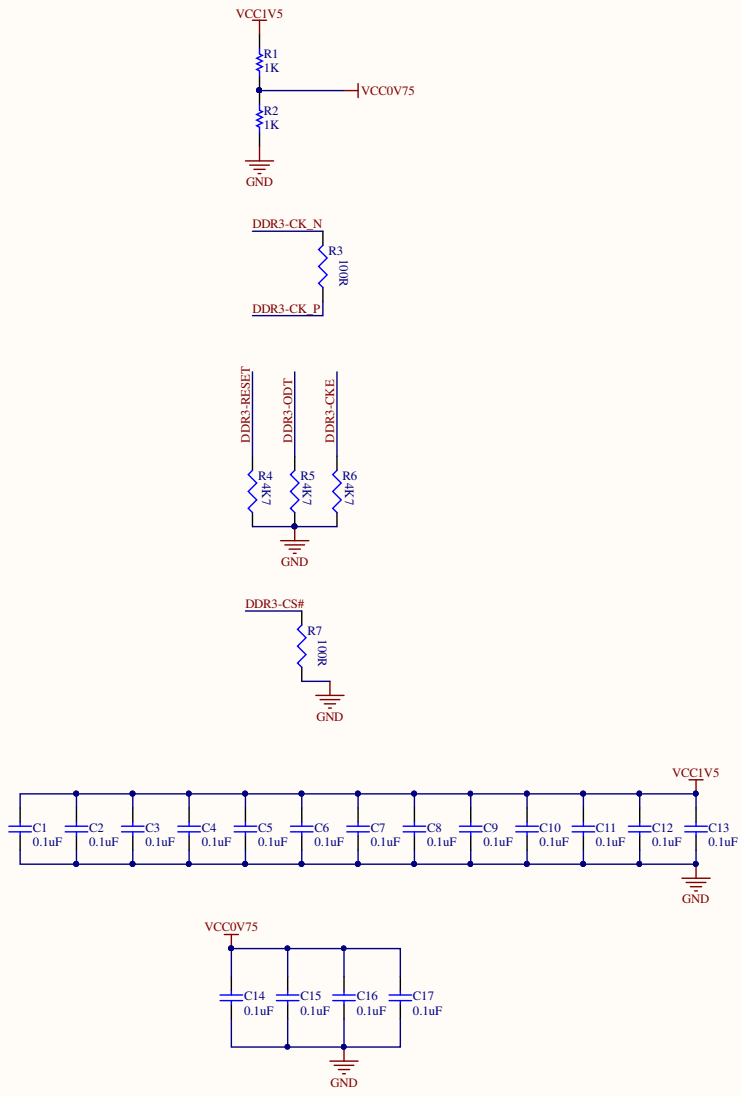
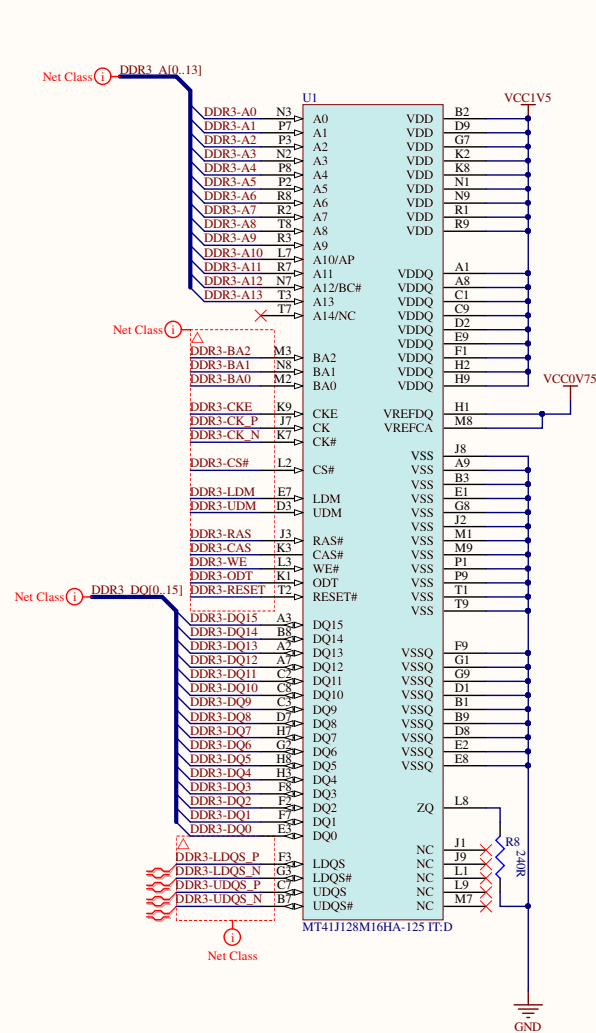
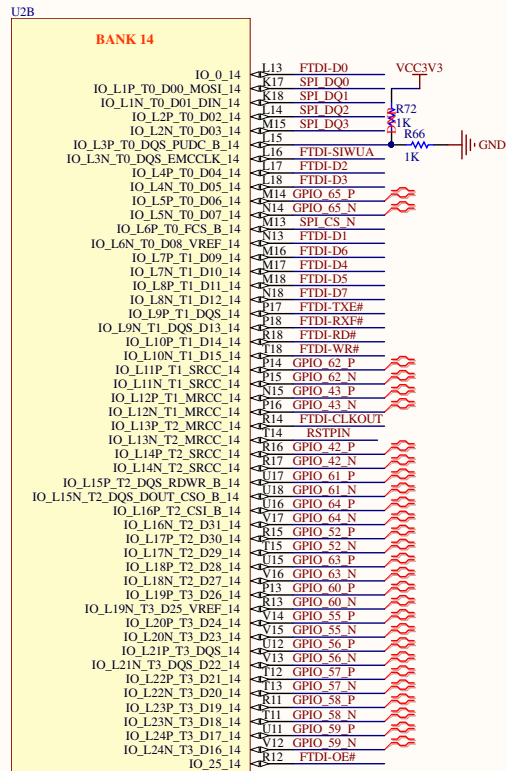




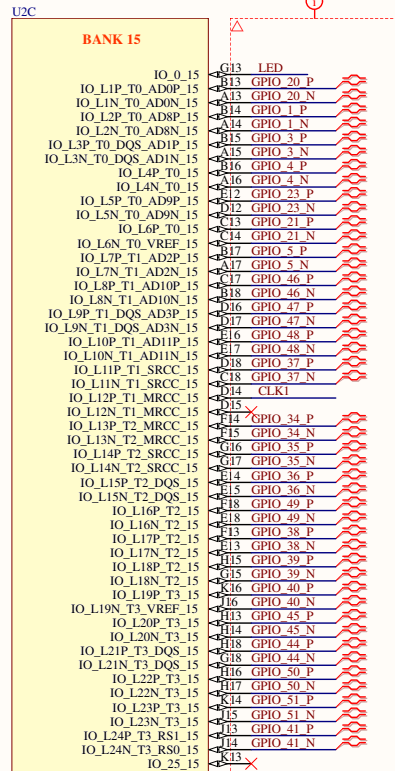
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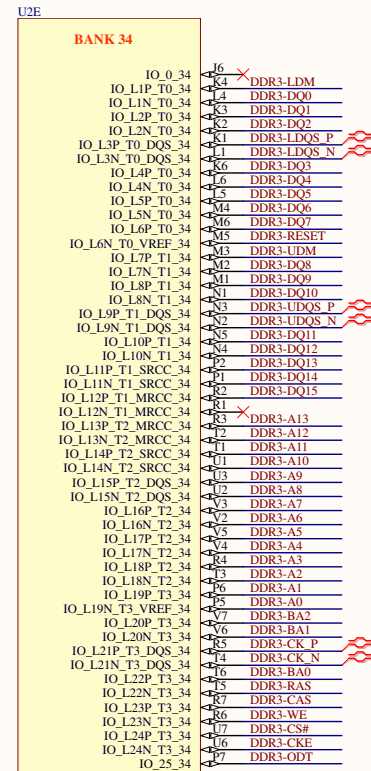
FPGA Banks



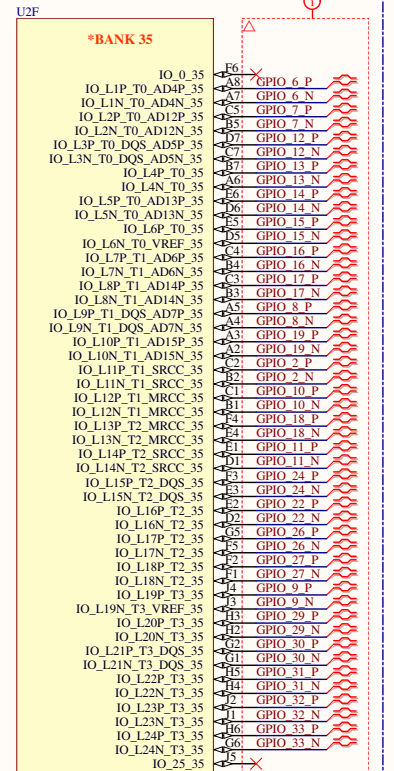
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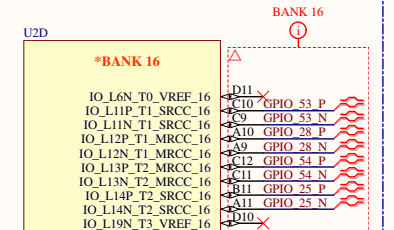
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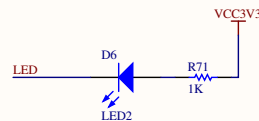
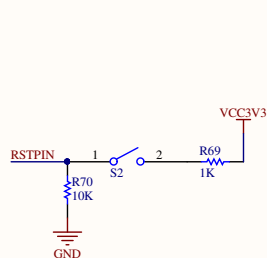
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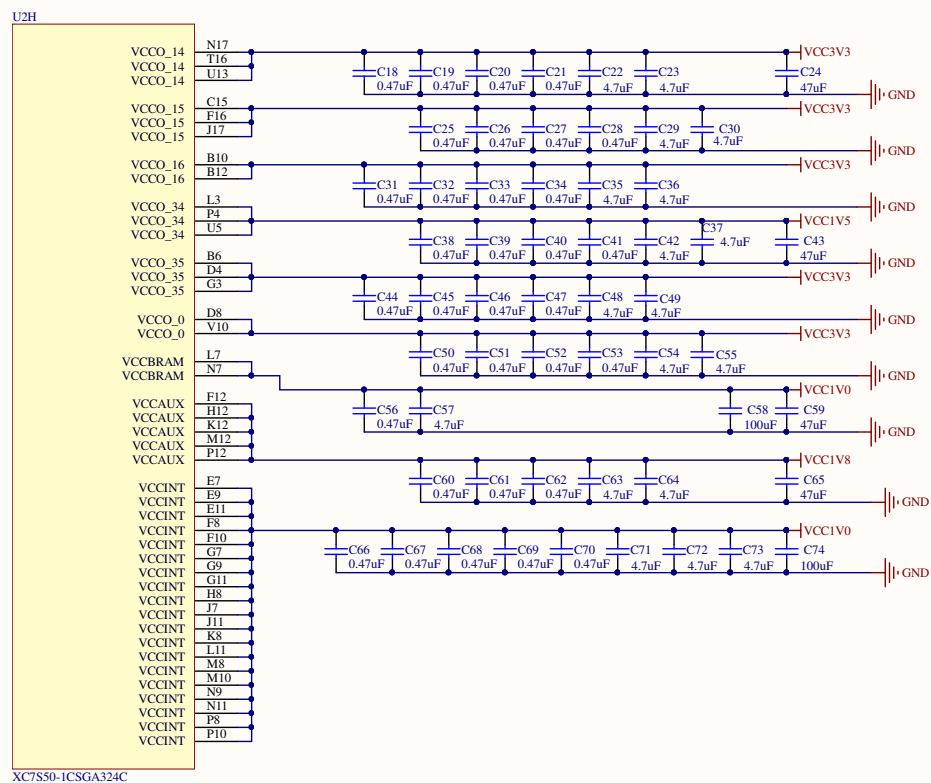
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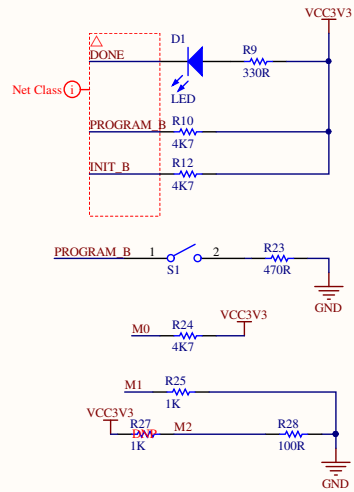
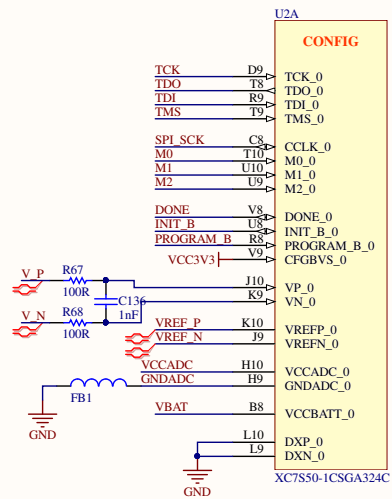
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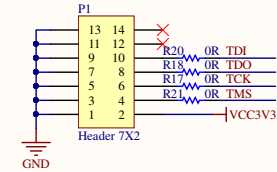
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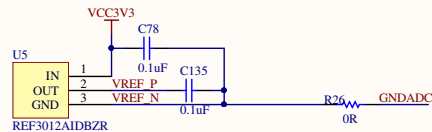
FPGA Configuration



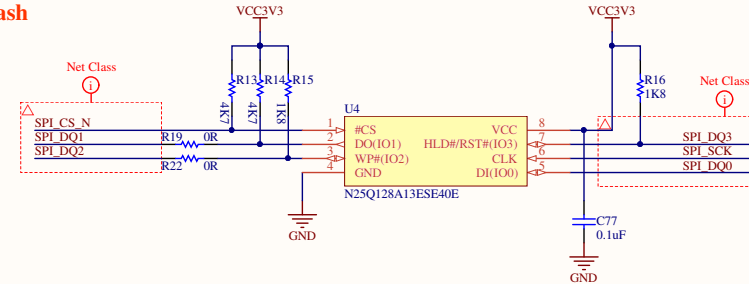
JTAG



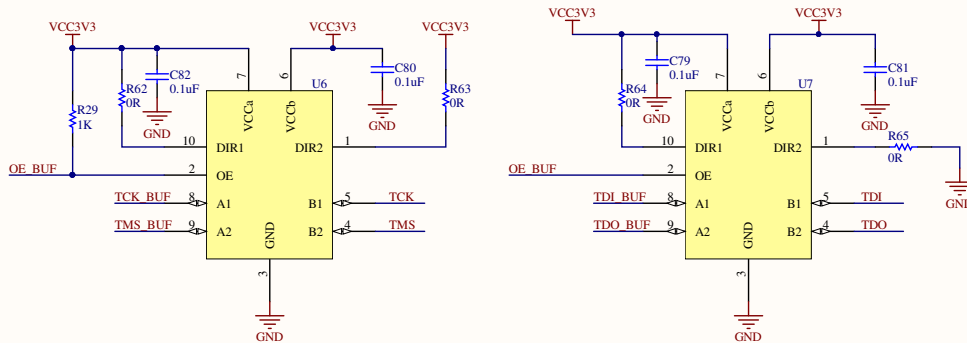
Backup Power and ADC Reference



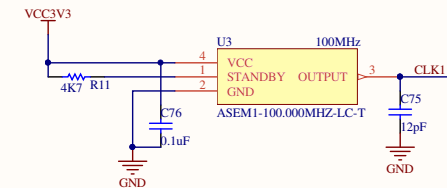
QSPI Flash



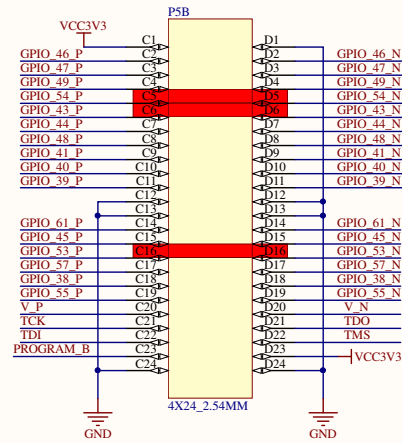
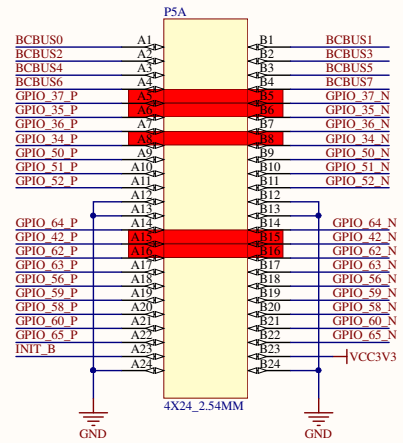
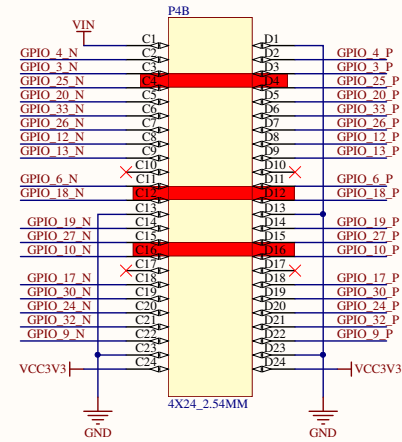
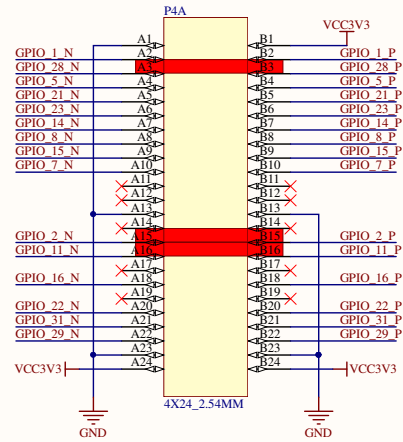
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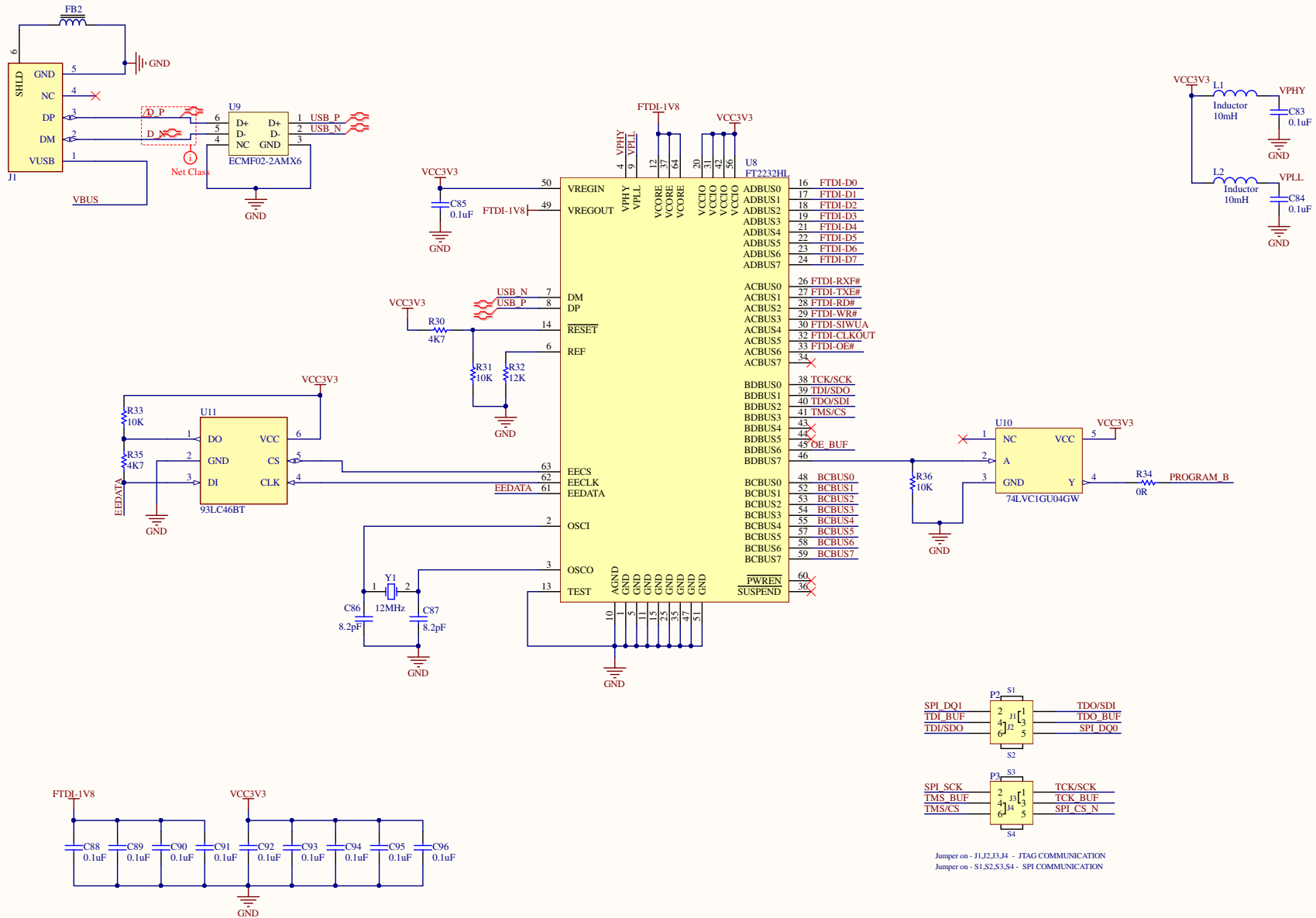
Clock



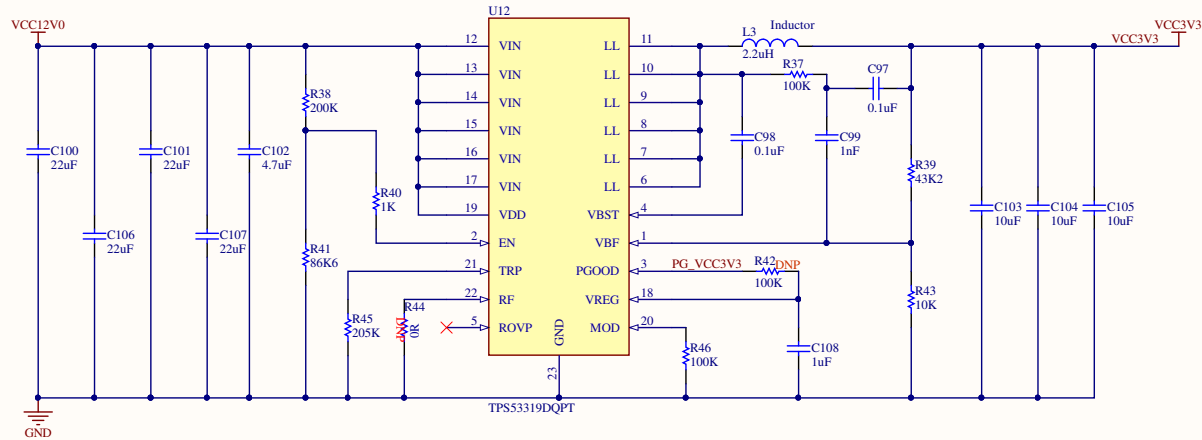
GPIO Header



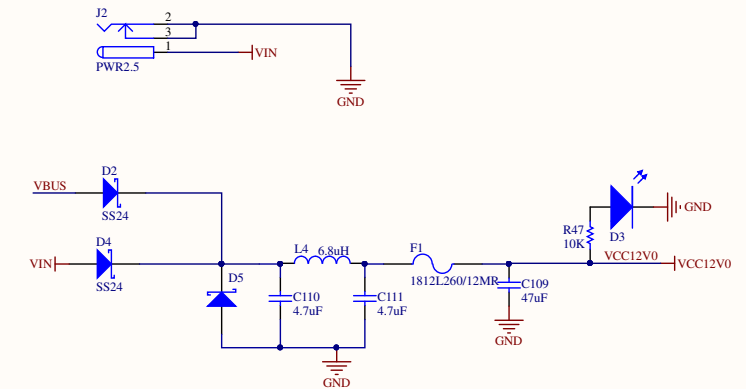
FT2232H



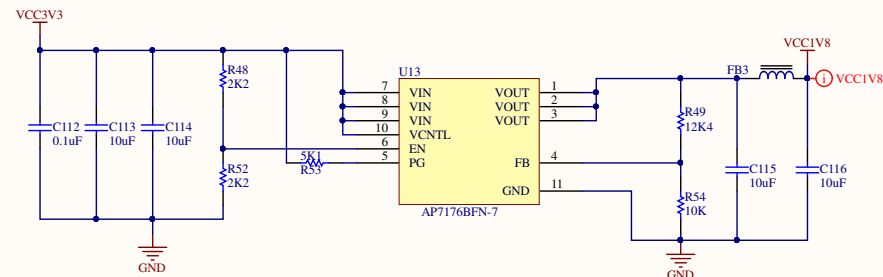
VCC3V3



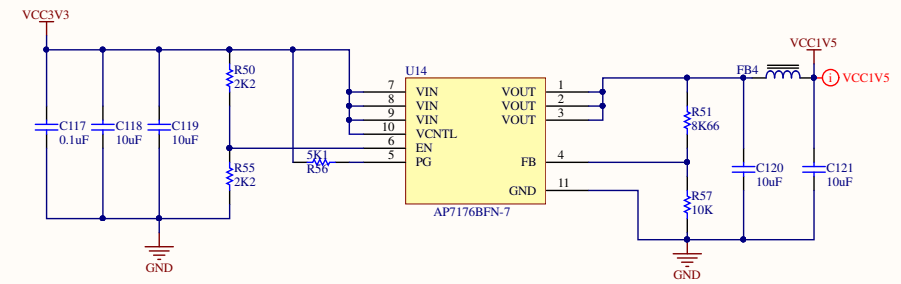
DC Jack



VCC1V8



VCC1V5



VCC1V0

