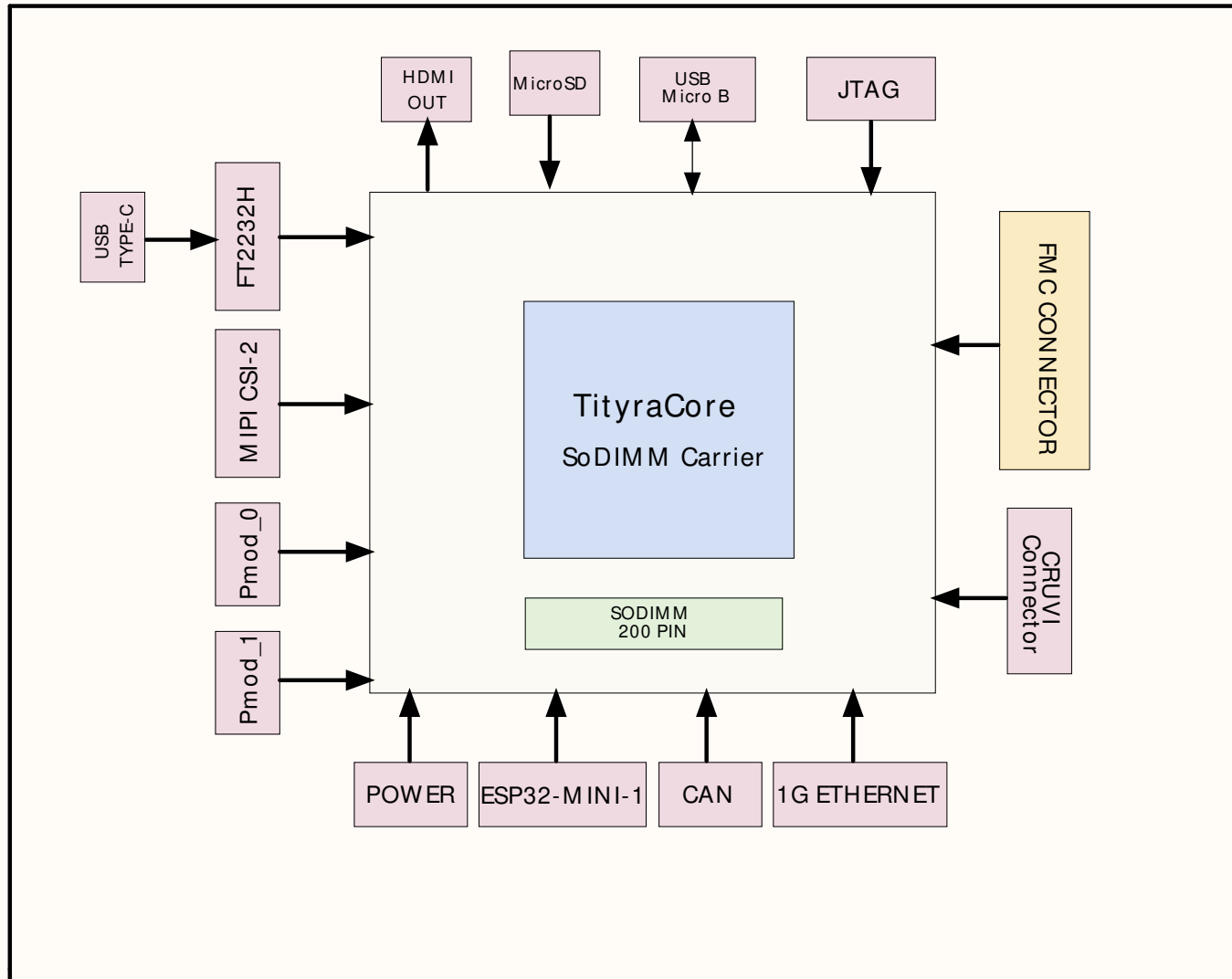
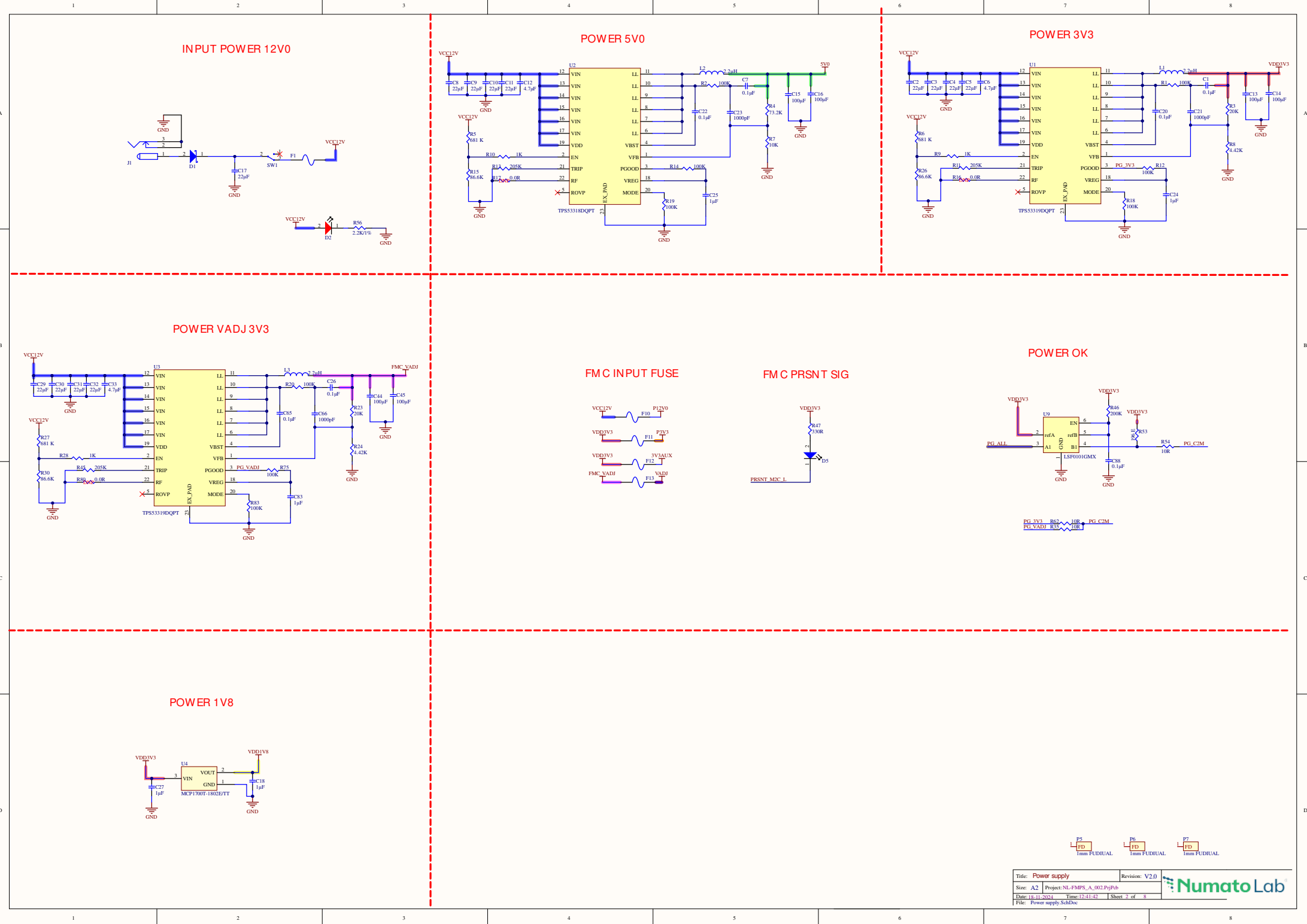
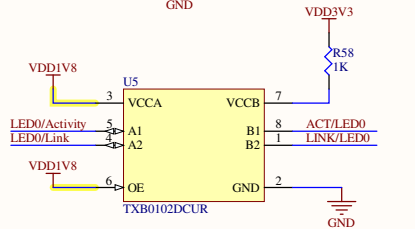
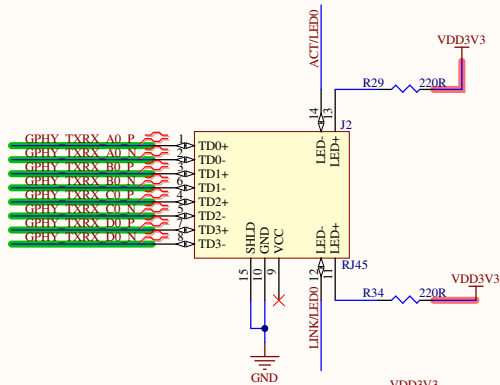


BLOCK DIAGRAM



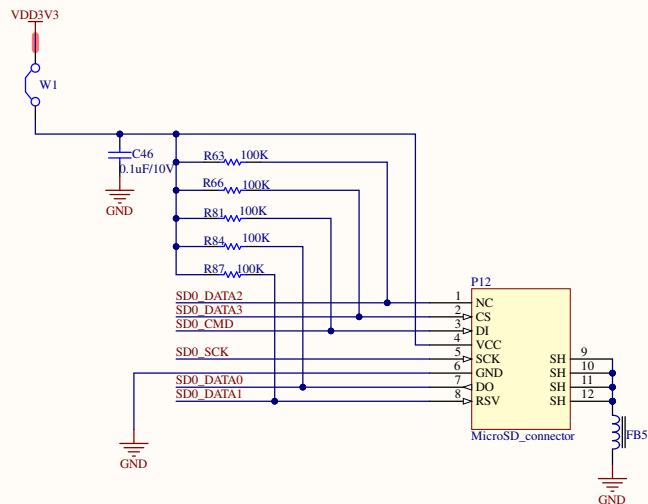


ETHERNET

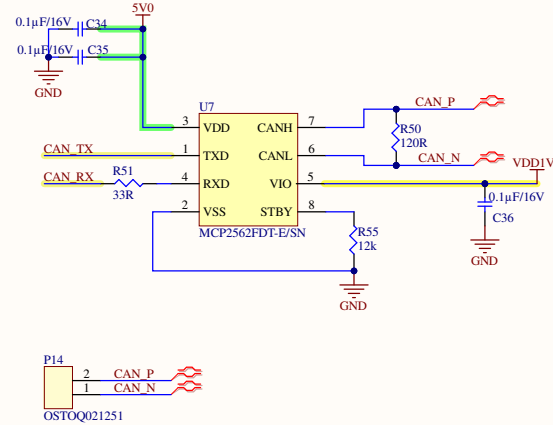


If using RJ45 & 1.8 VDDIO, a level shifting is required from 3.3V led to 1.8V

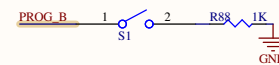
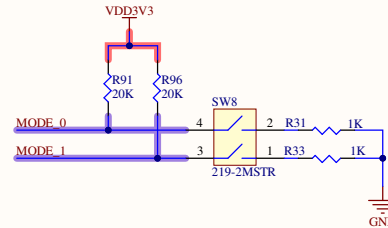
MicroSD



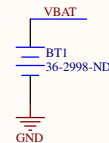
CAN 0



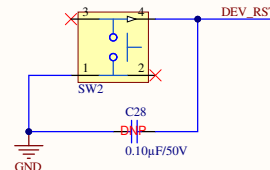
MODE SELECTION



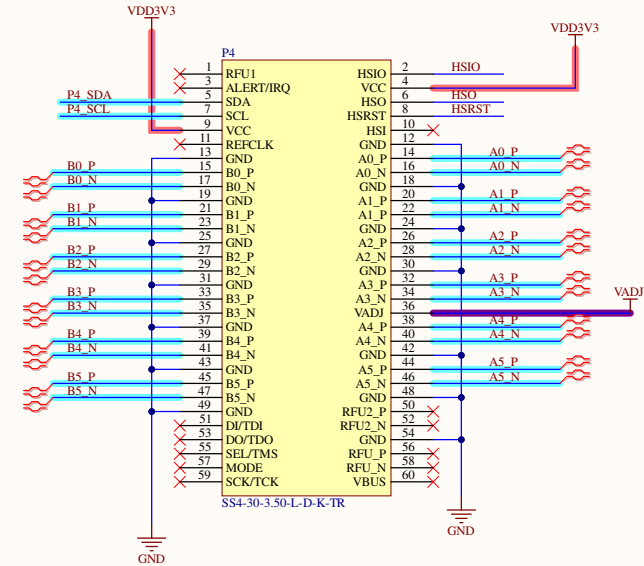
VBAT



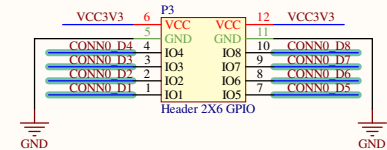
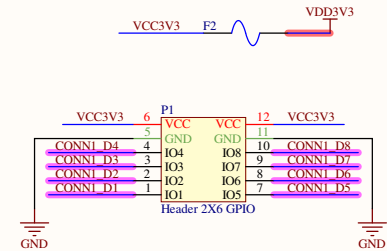
RESET CFG



CRUVI HS Connector

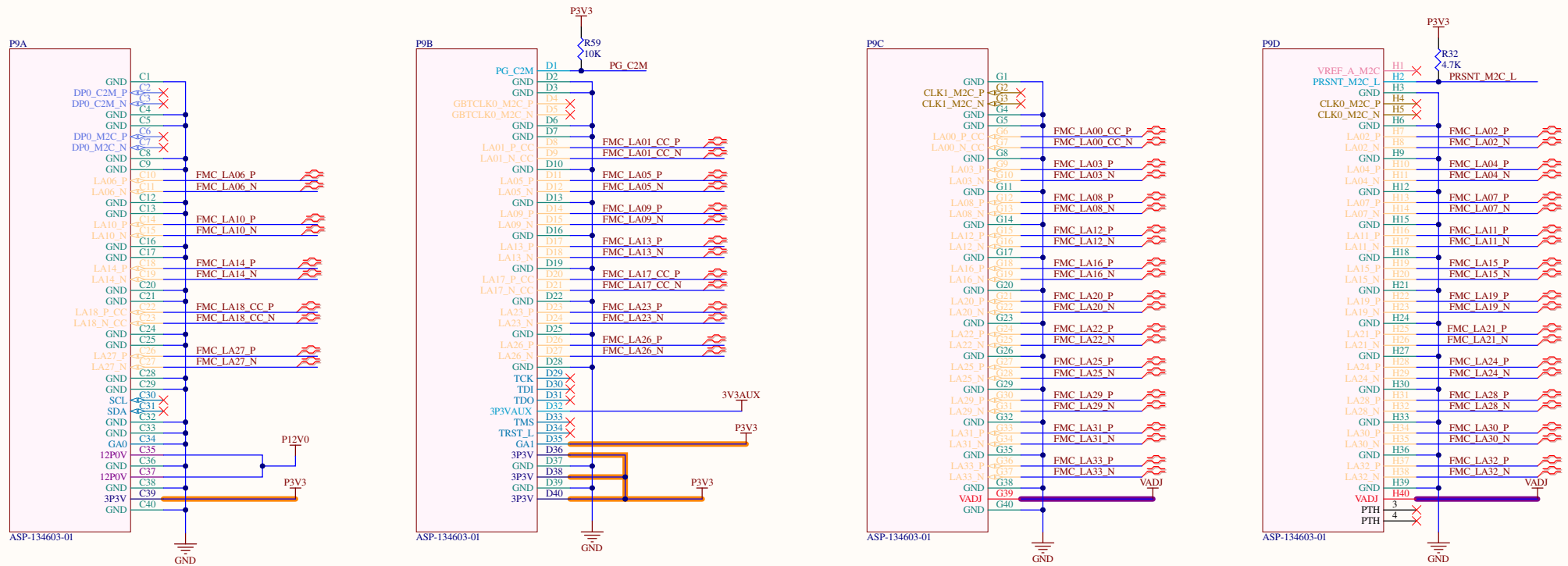


Pmod



The schematic diagram illustrates the HDMI interface circuit. It features an HDMI source chip (TPD12S520DBTR) connected to an HDMI sink chip (HDMI RECPT). The source side includes a 5V supply, LV supply, and various signal pins (TMDS, DDC, Hotplug) connected to the sink's corresponding pins. Power and ground connections are shown, including decoupling capacitors (C159, C164, C37) and resistors (R48, R25, R21, R52, R49, R92, R90, R85, R108, R60). Signal traces for P6 D2, P6 D1, P6 D0, P6 CLK, P6 CEC, P6 SCL, and P6 SDA are shown with their respective pin numbers and labels.

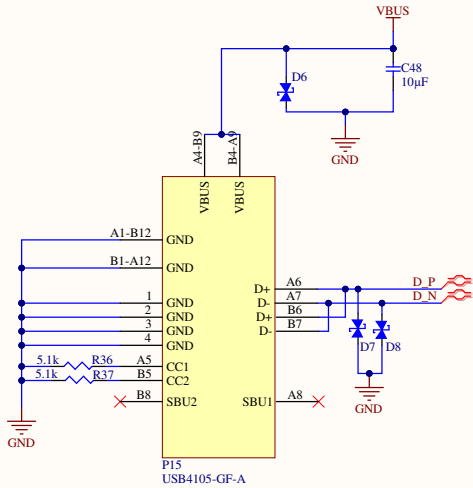
FMC LPC



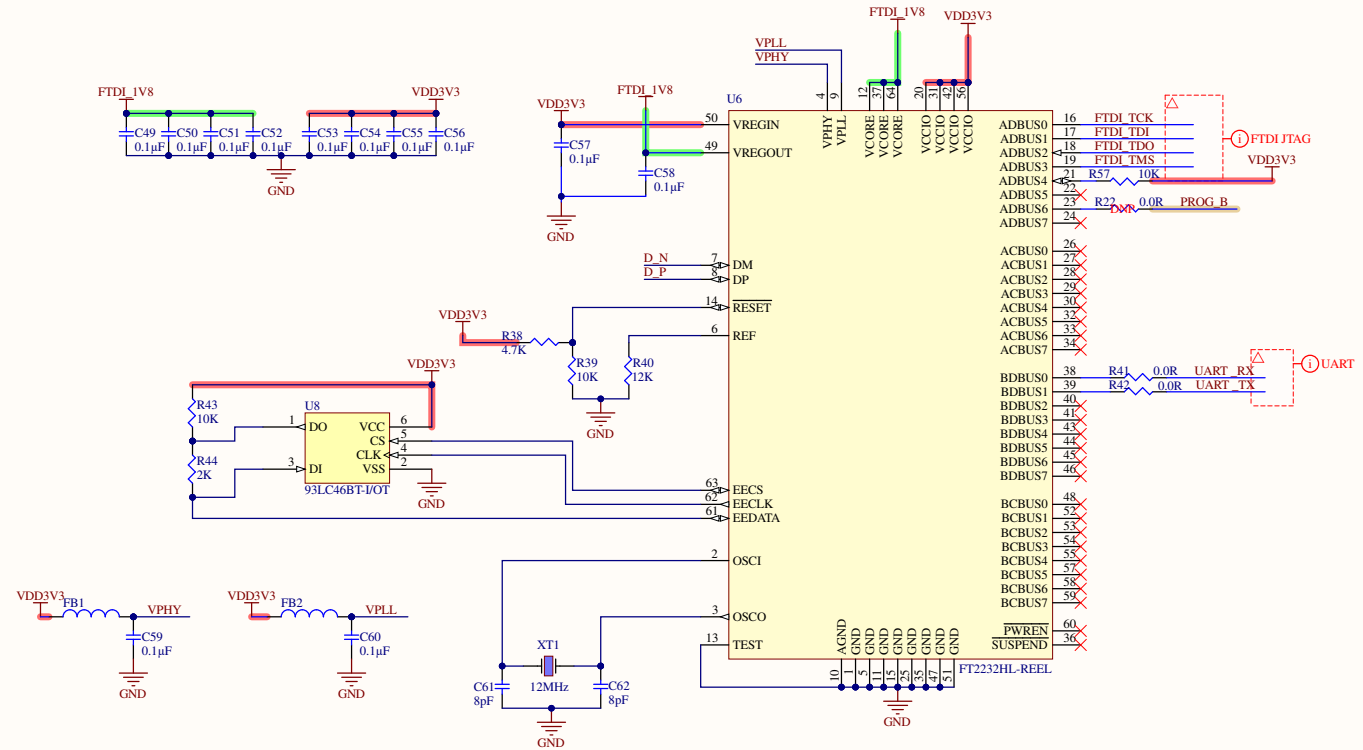
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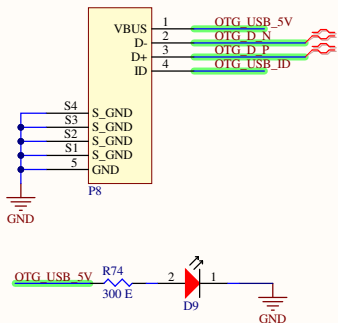
USB_TYPEC



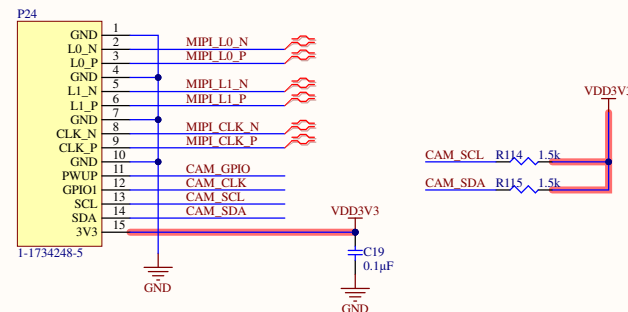
FTDI USB



USB_OTG



MIPI CSI-2



SODIMM CONNECTOR

