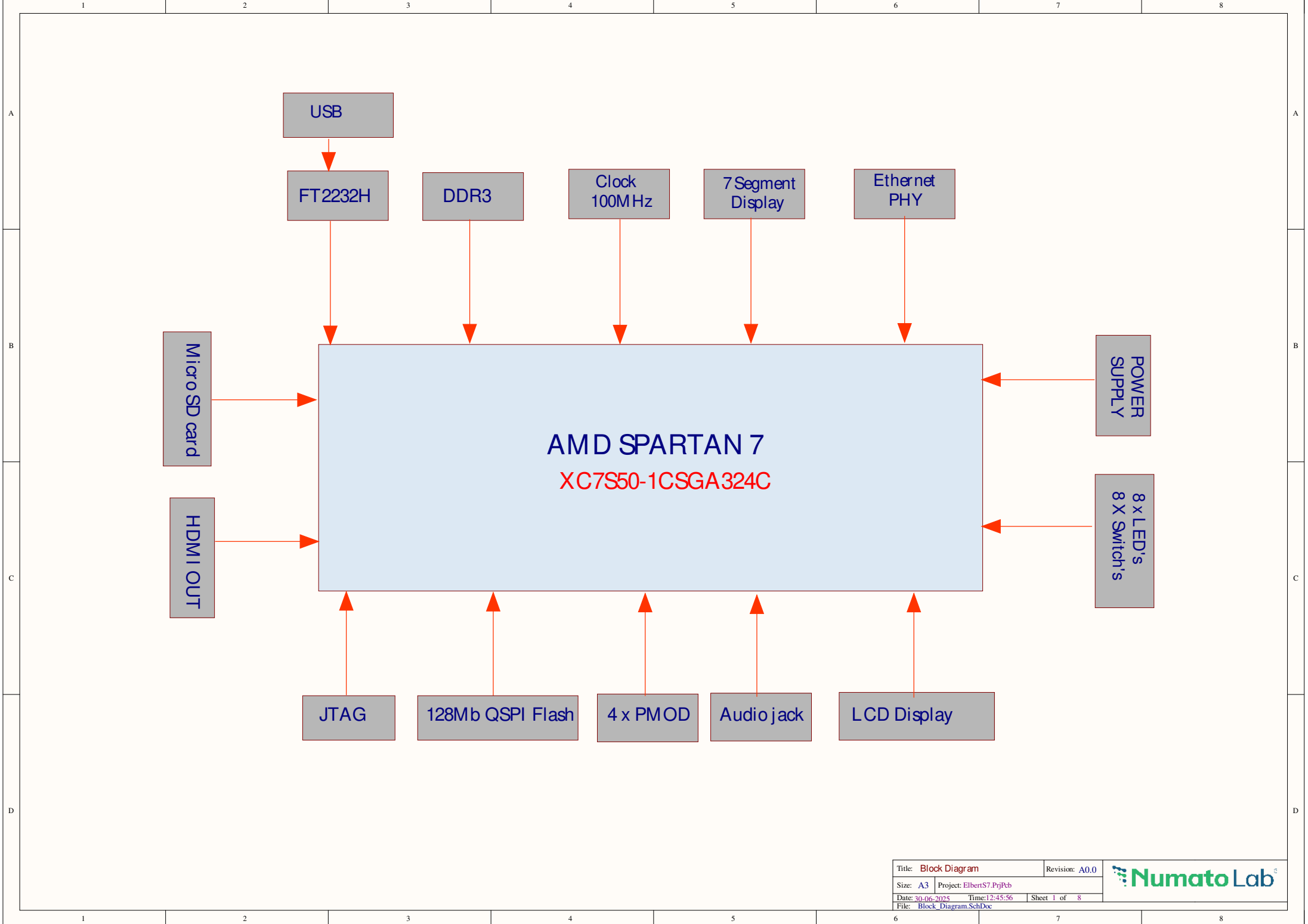
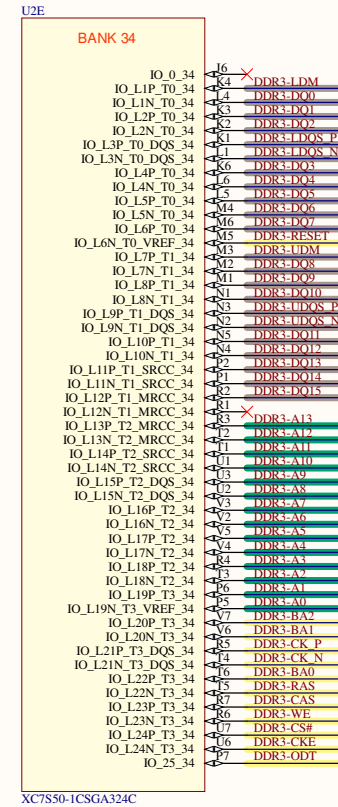
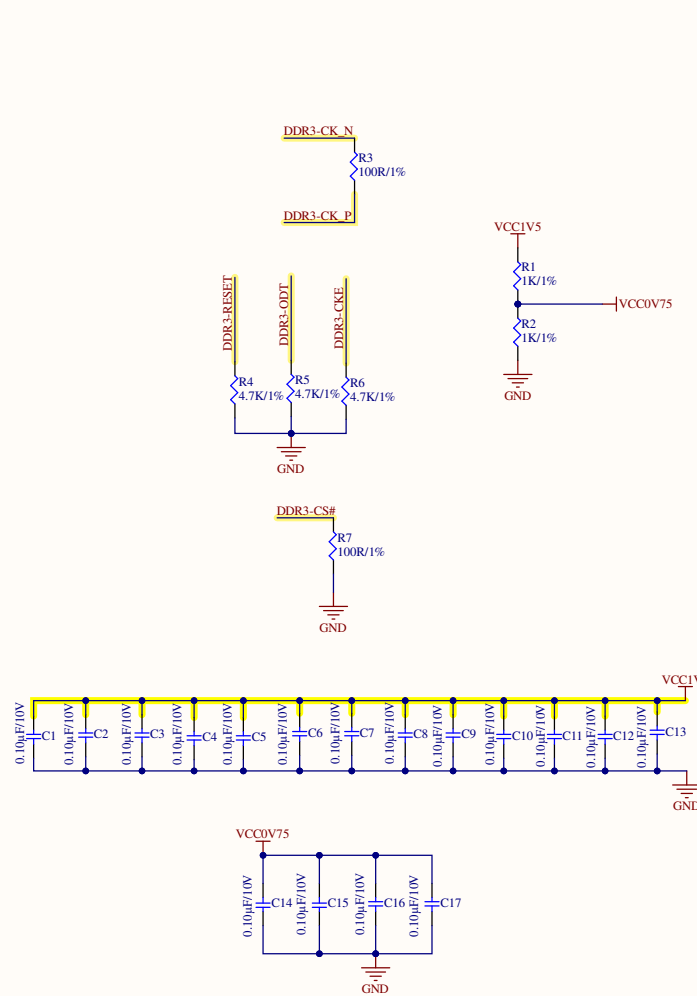
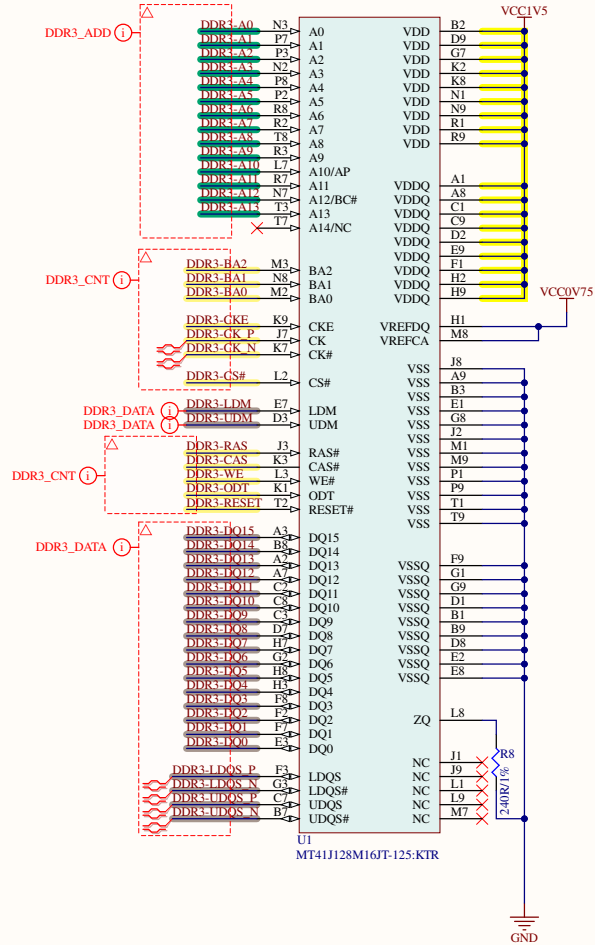
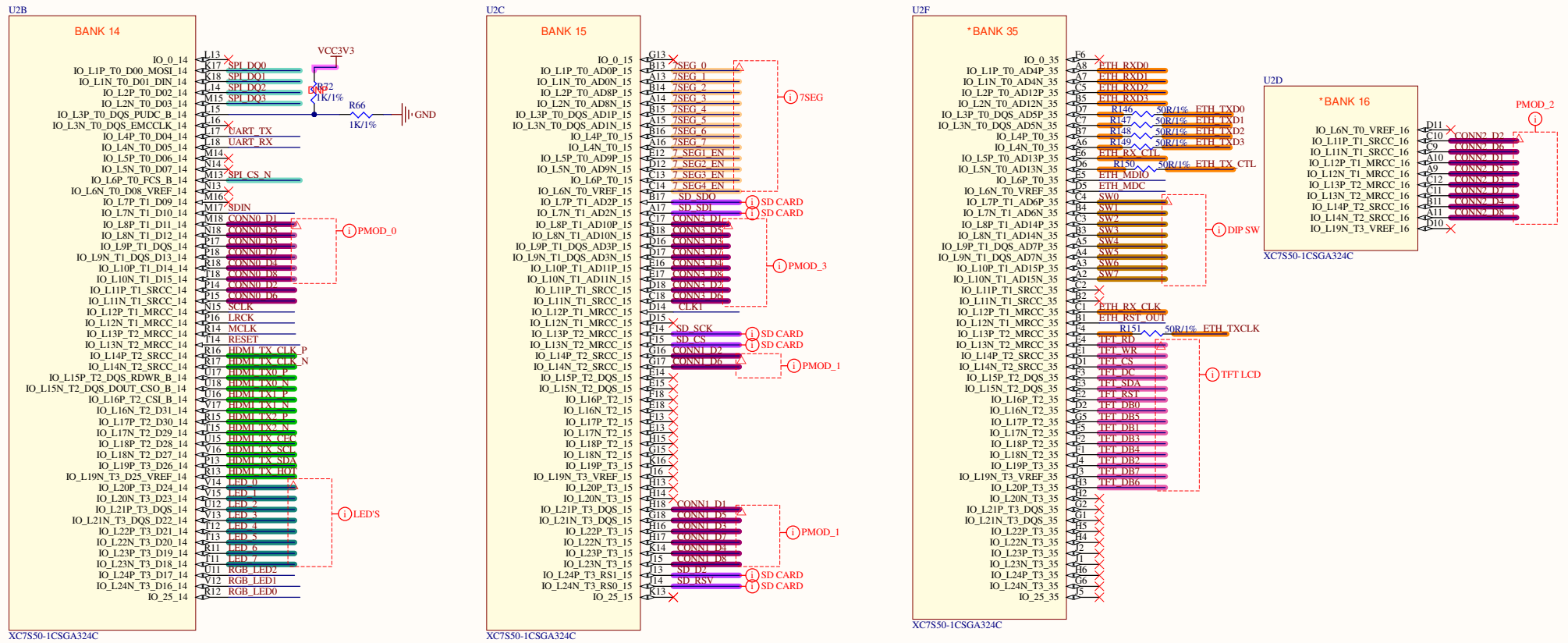




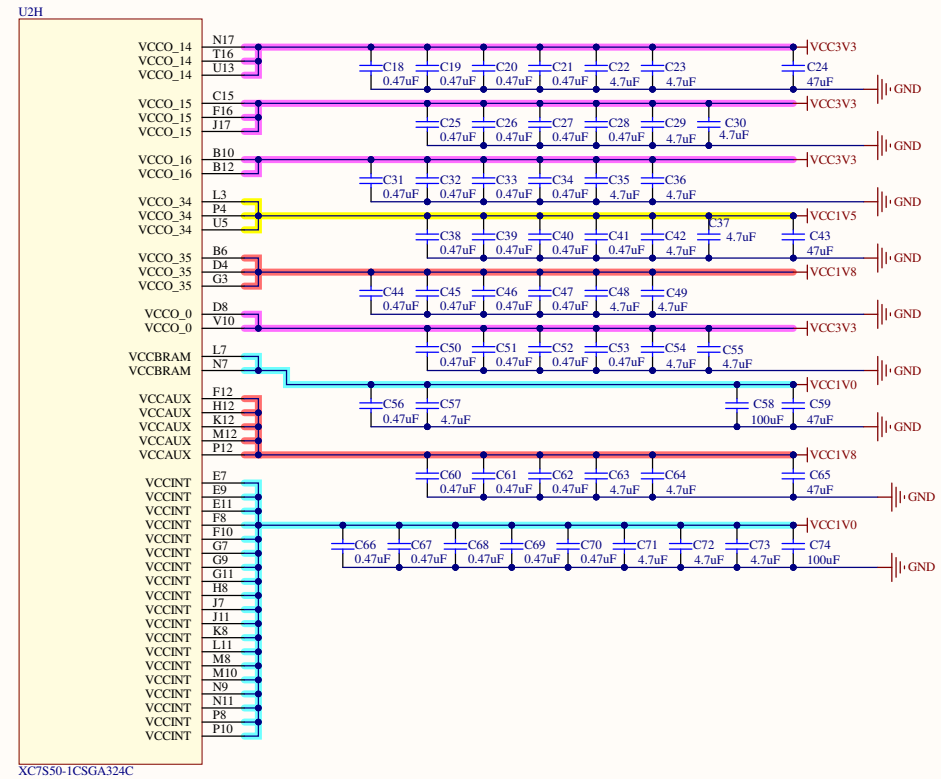
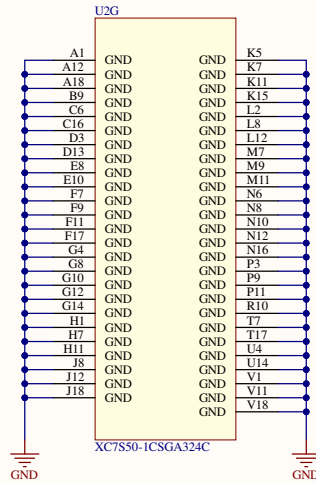
DDR3



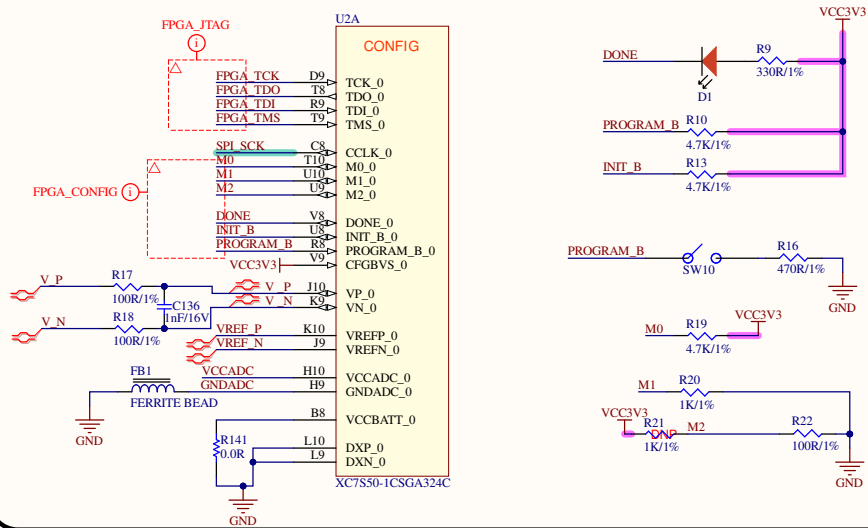
FPGA Banks



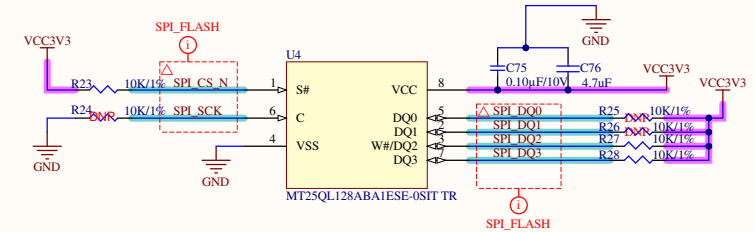
FPGA Power



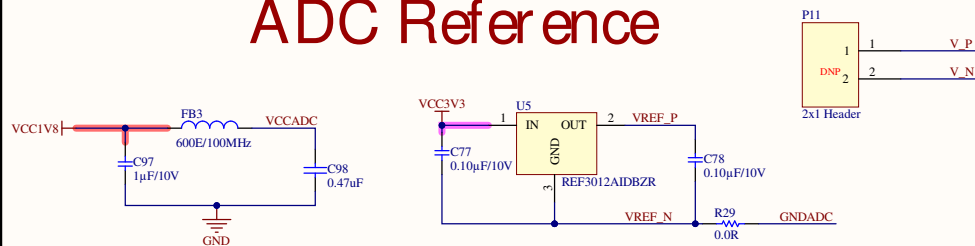
FPGA Configuration



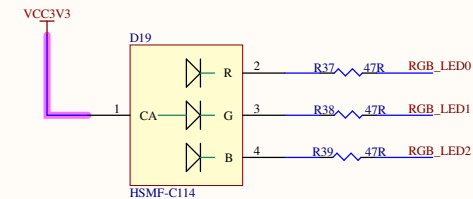
SPI FLASH



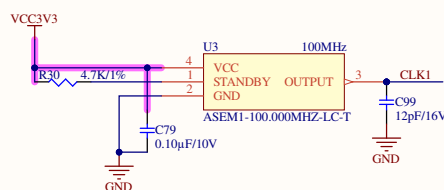
ADC Reference



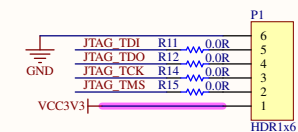
RGB



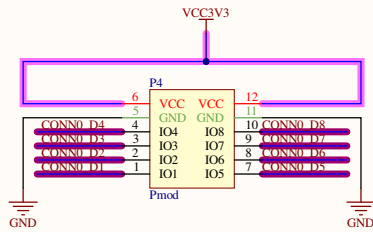
CLOCK



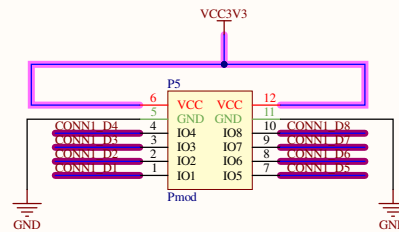
JTAG



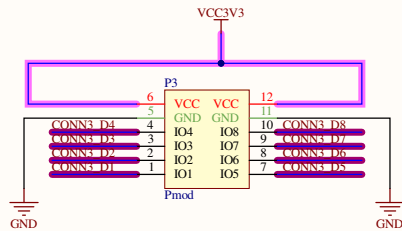
PMOD_0



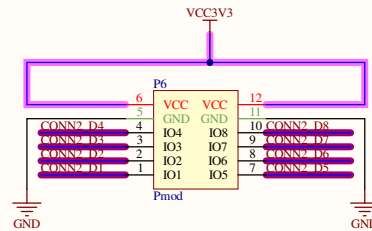
PMOD_1



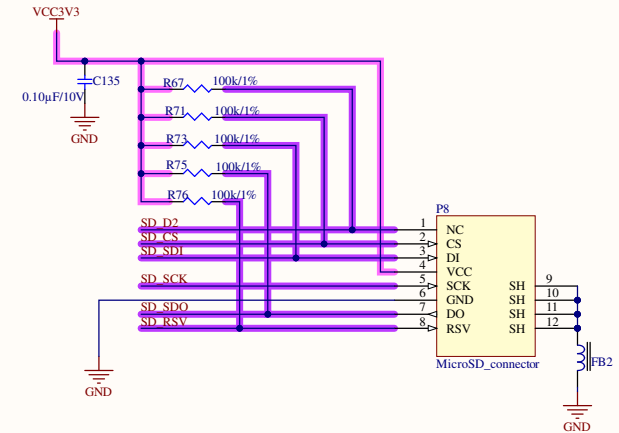
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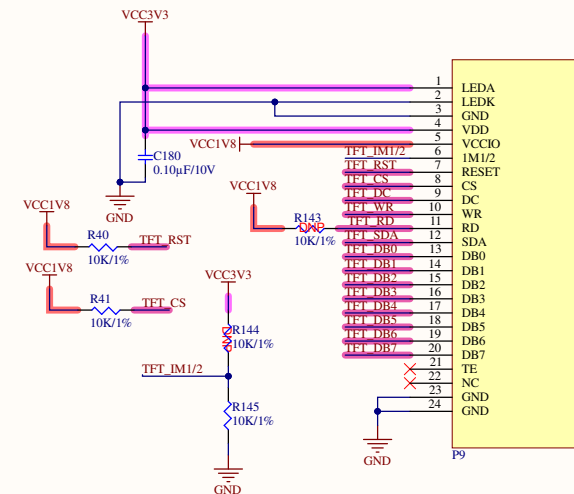
PMOD_2



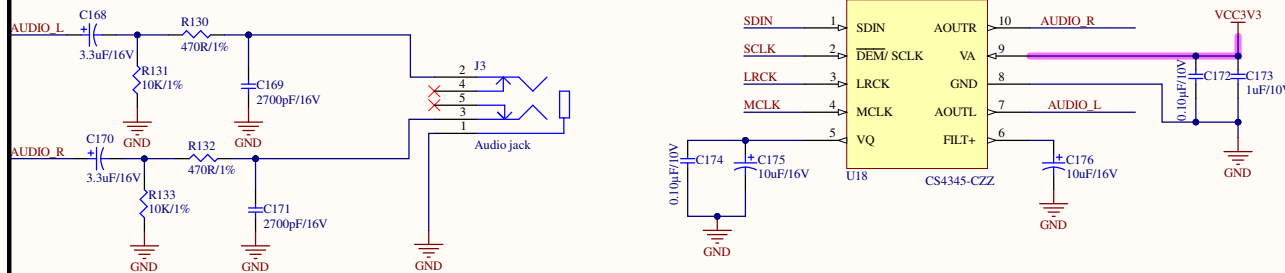
MicroSD



LCD DISPLAY

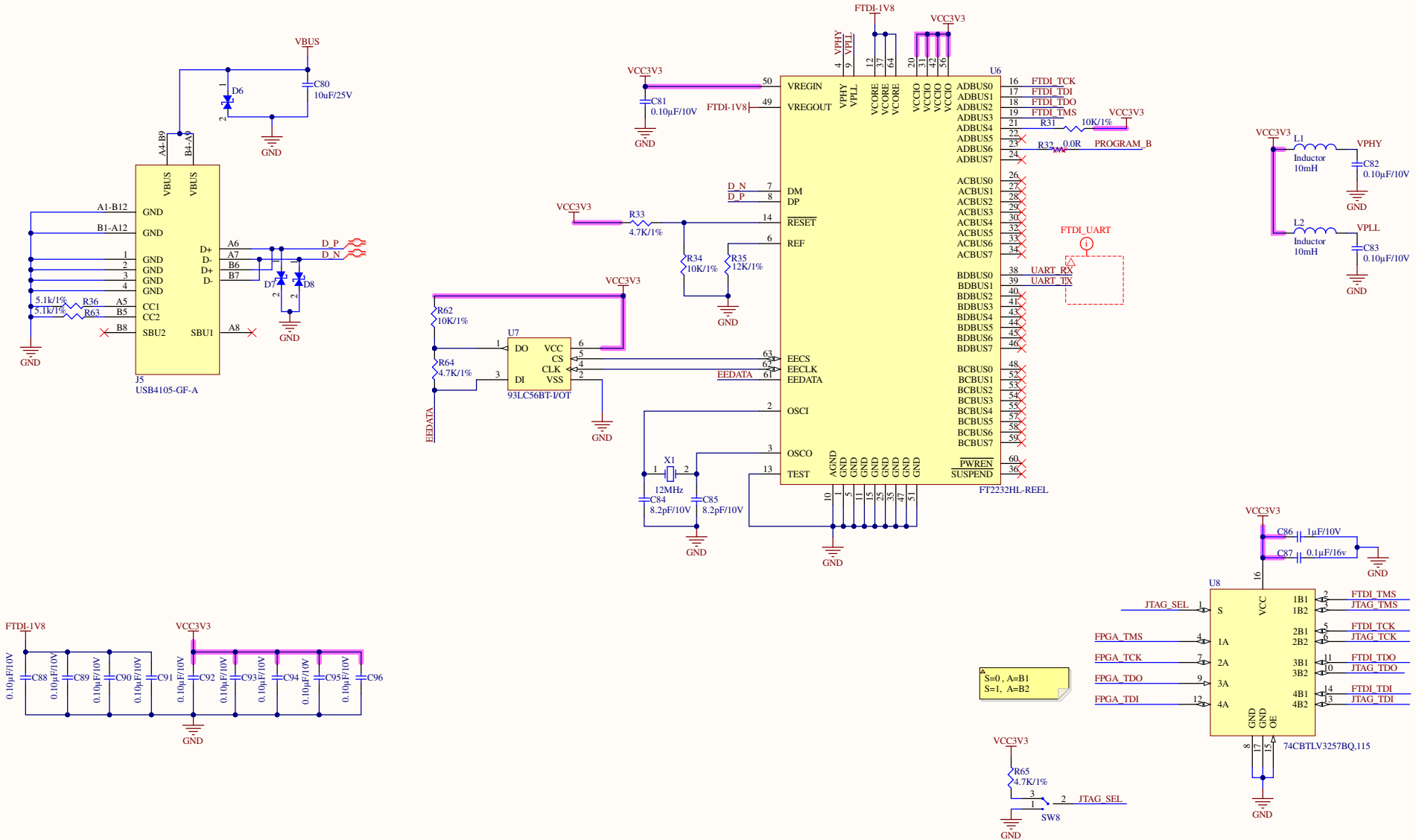


AUDIO JACK

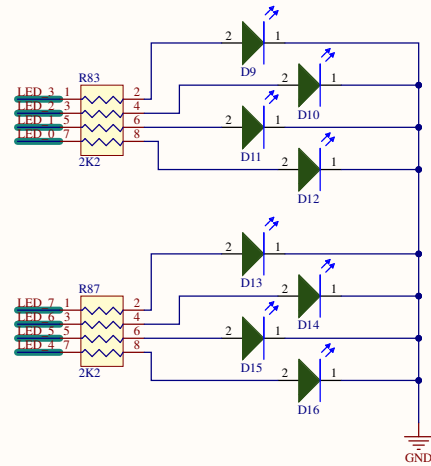


USB-C

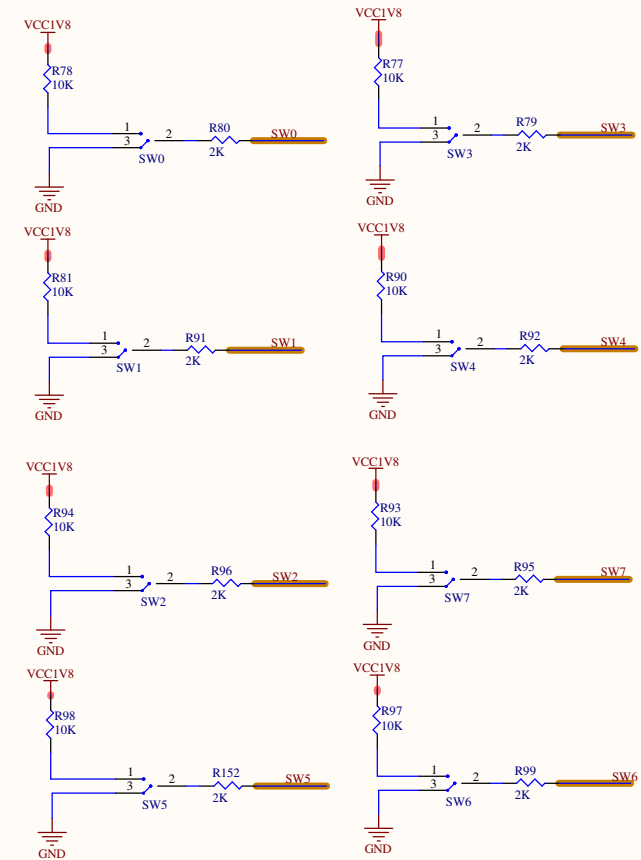
FT 2232H



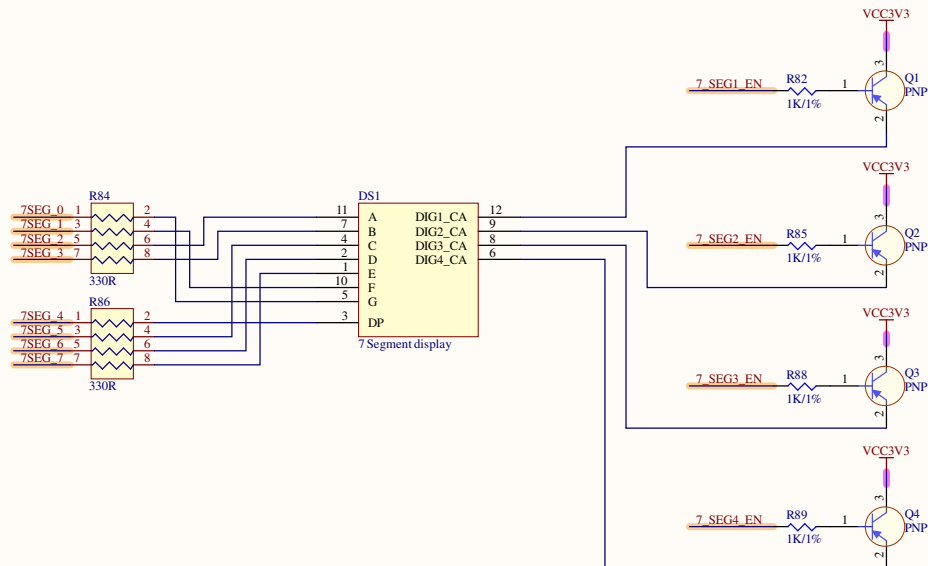
LEDs



Switch



7 Segment Display



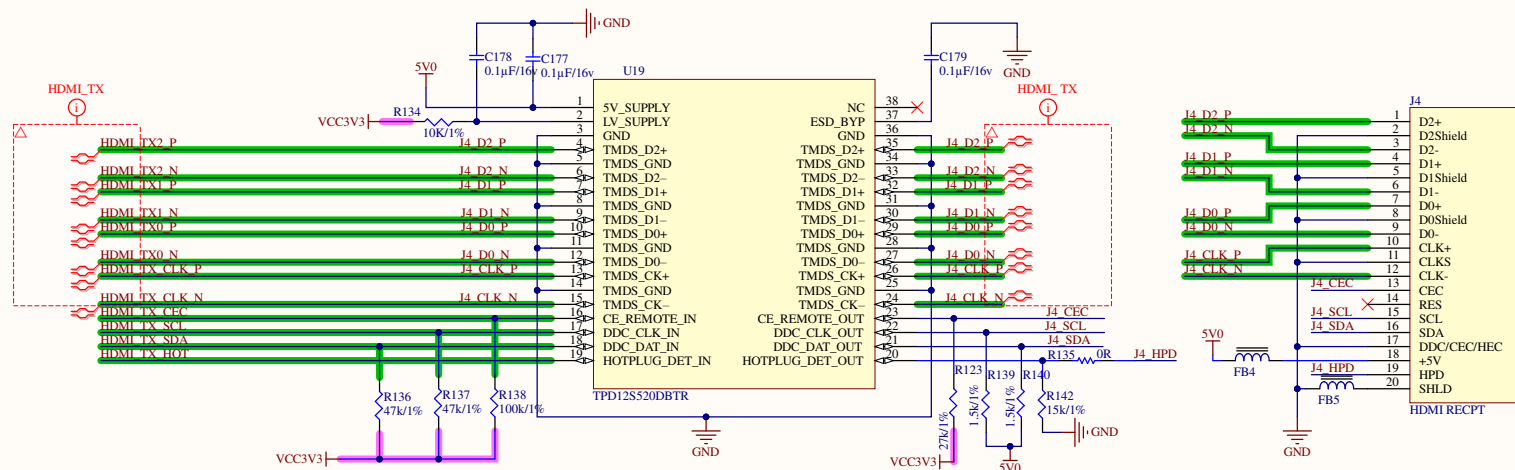
[illegible]

ETH POWER

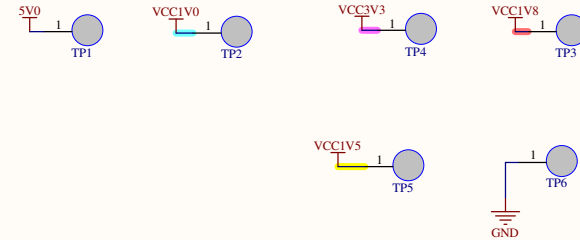
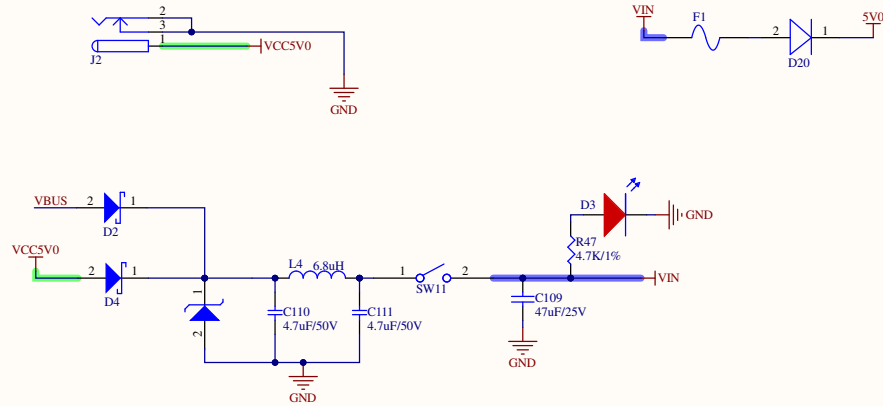
A Pull down disables the 125 Mhz clock output from KSZ

High: Single led mode
Low: Dual led mode

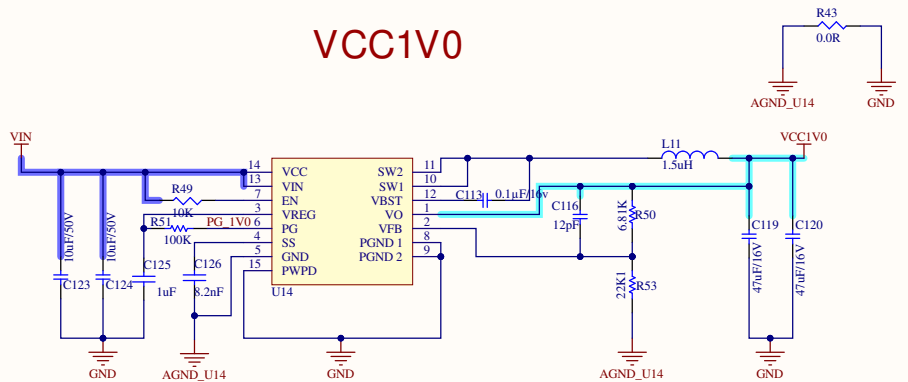
HDMI OUT



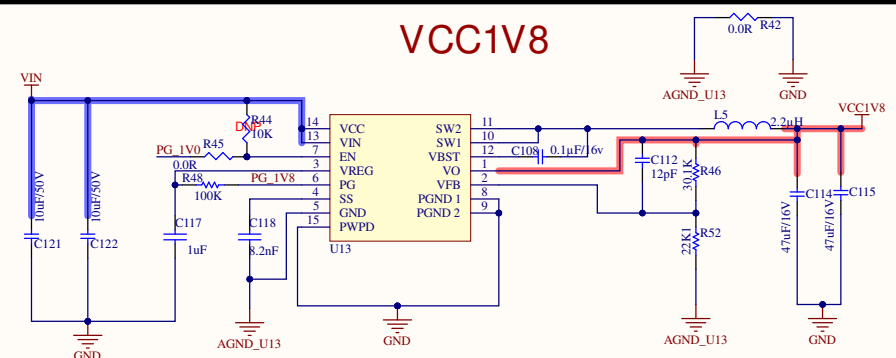
DC Jack



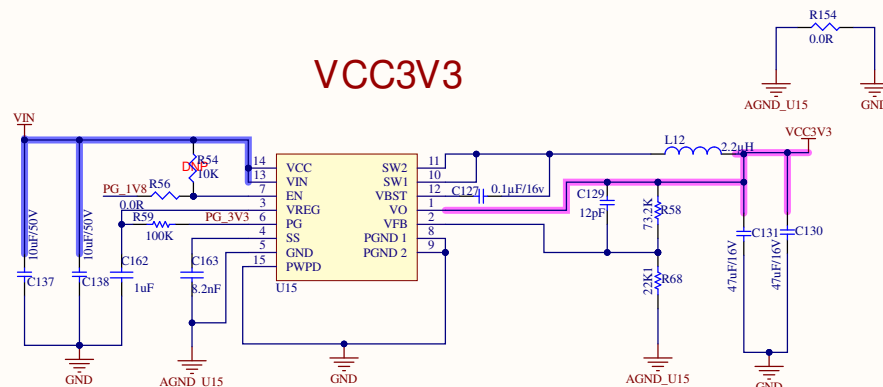
VCC1V0



VCC1V8



VCC3V3



VCC1V5

