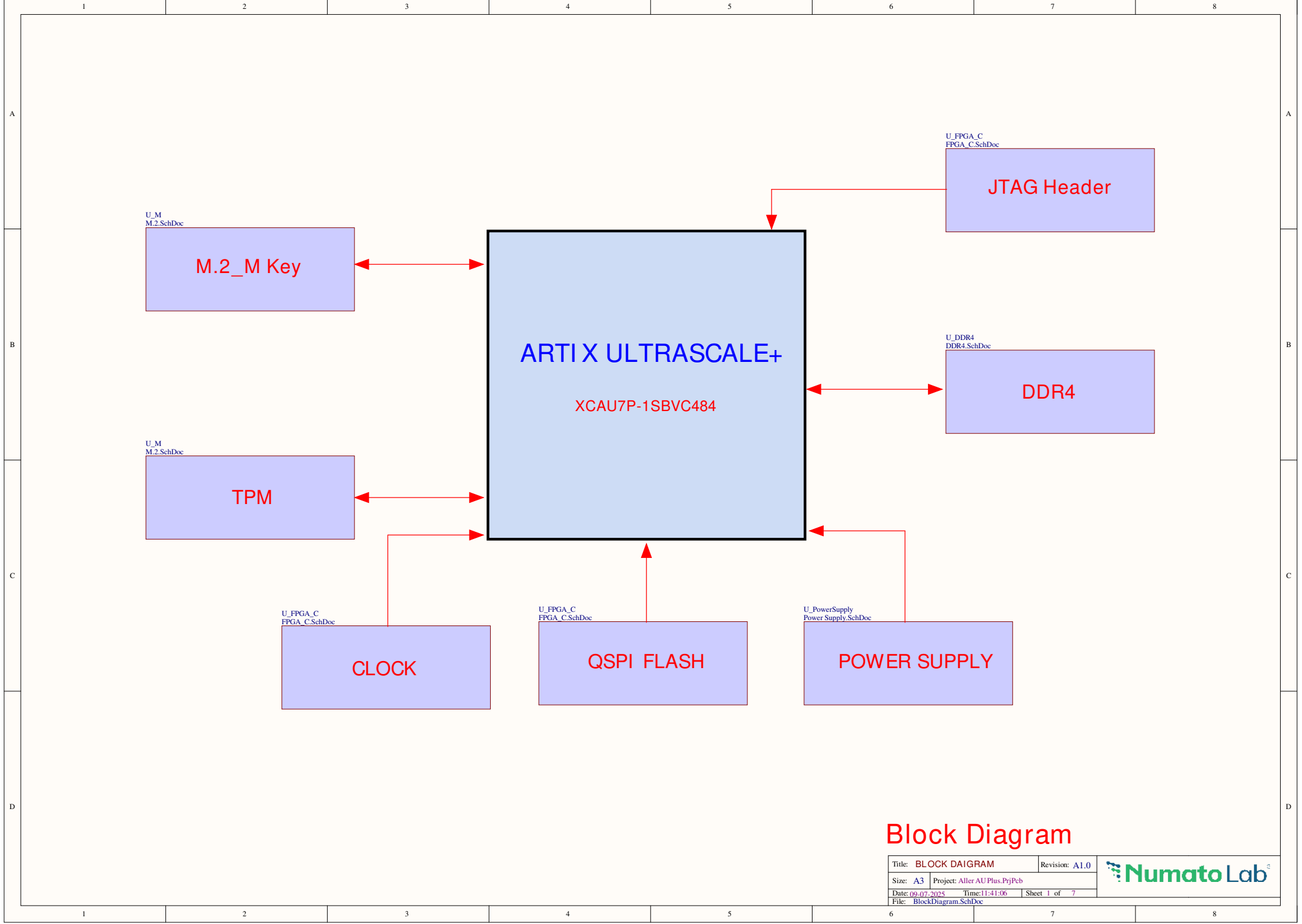
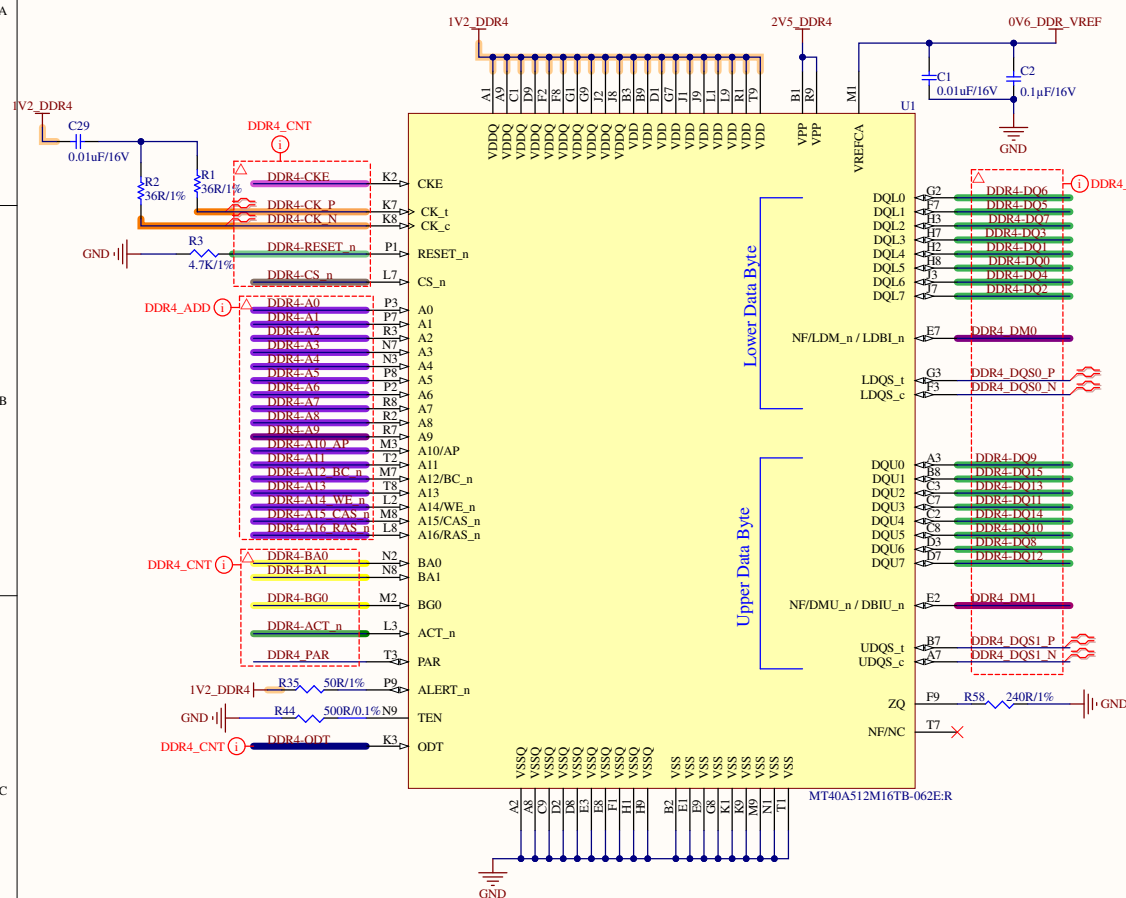
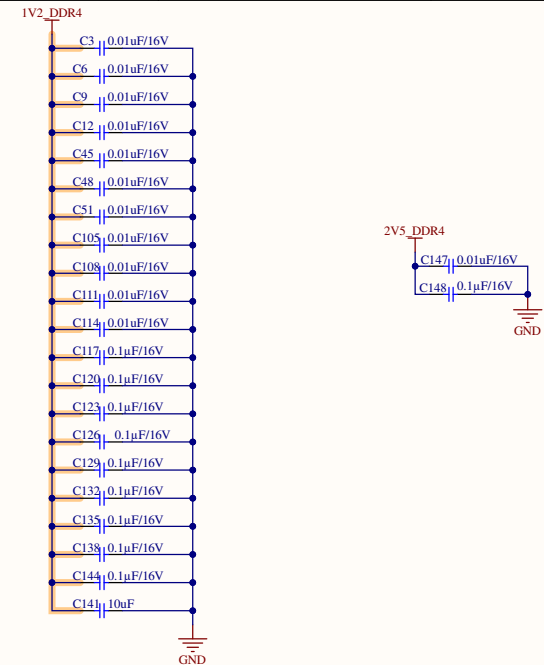
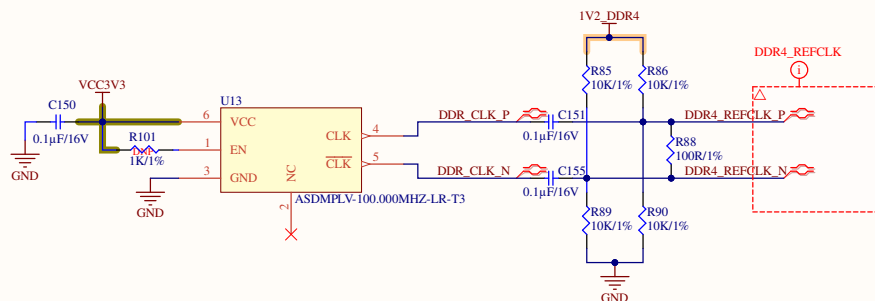




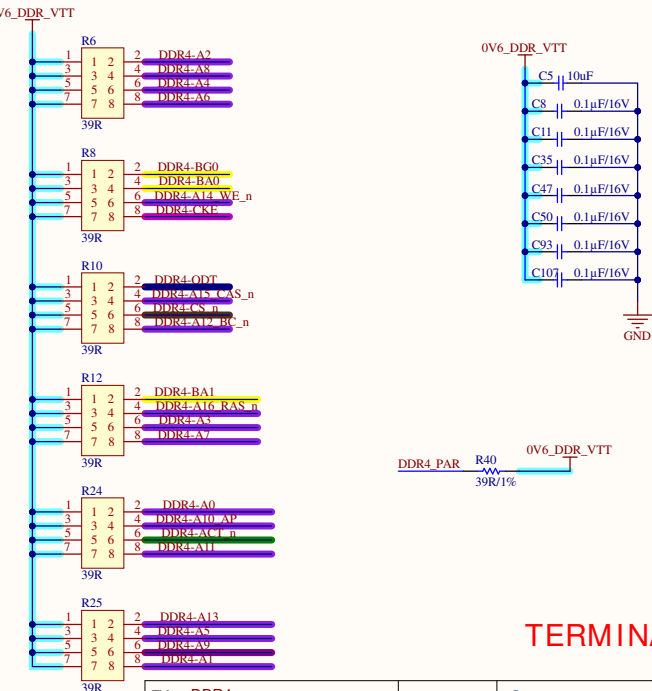
DDR4



DDR REFERENCE CLOCK 100MHz

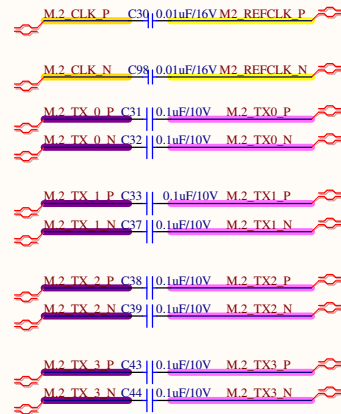
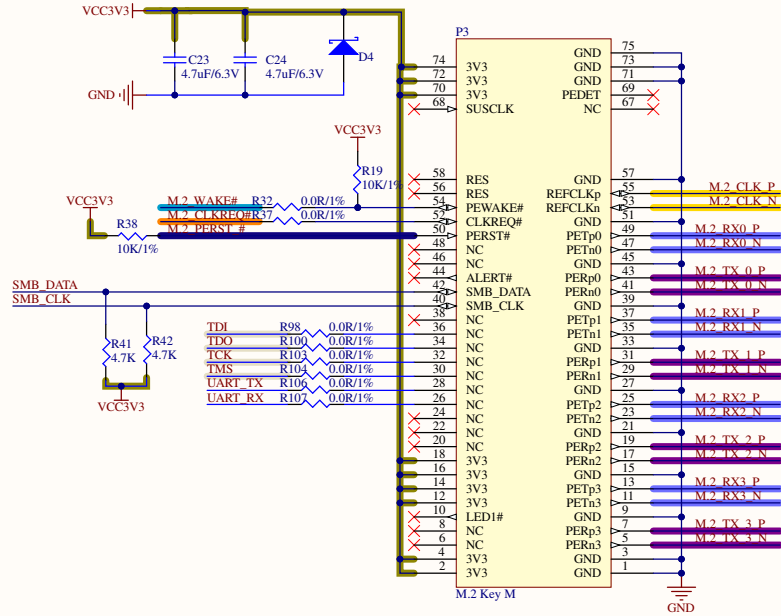


DECAP

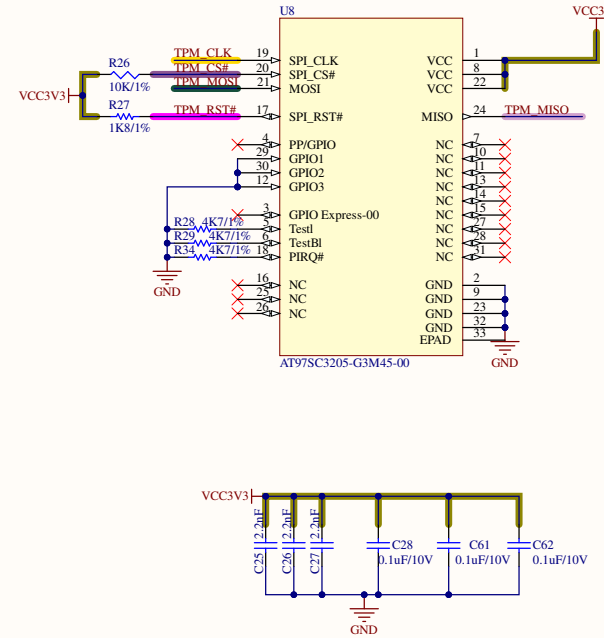


TERMINATION

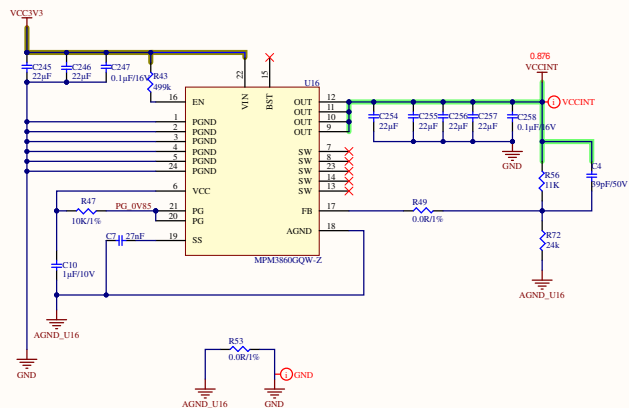
M.2



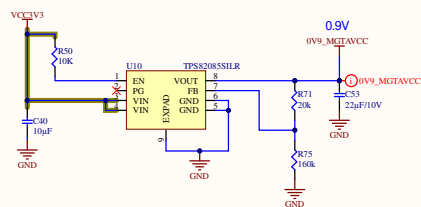
TPM



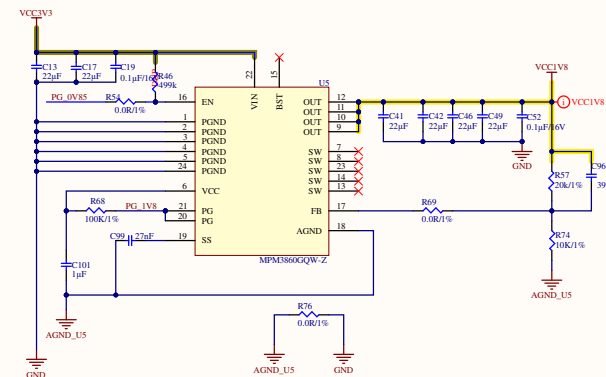
POWER VCC0V875 (VCCINT)



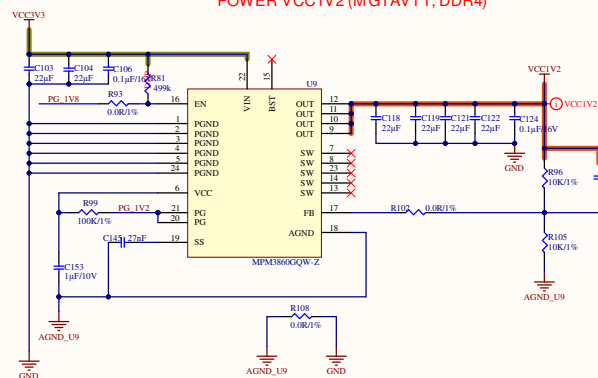
POWER 0V9 (MGTAVCC)



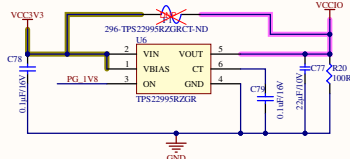
POWER VCC1V8 (VCCAUX, VCCO)



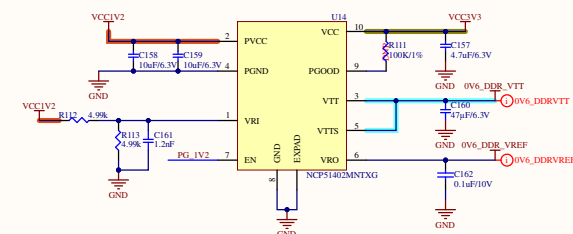
POWER VCC1V2 (MGTAVTT, DDR4)



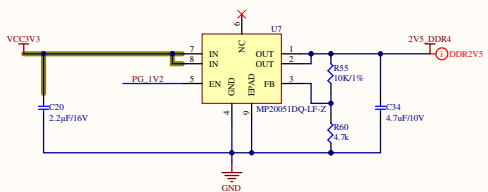
POWER VCCIO 3V3



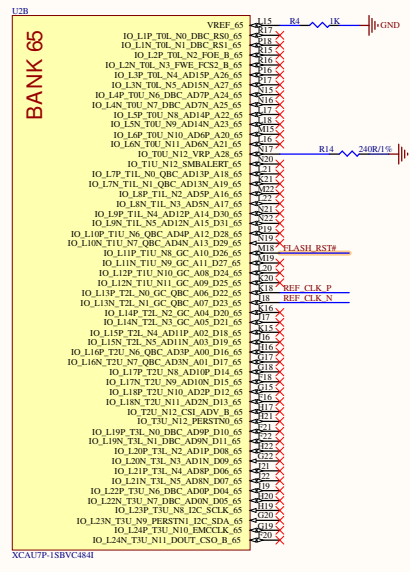
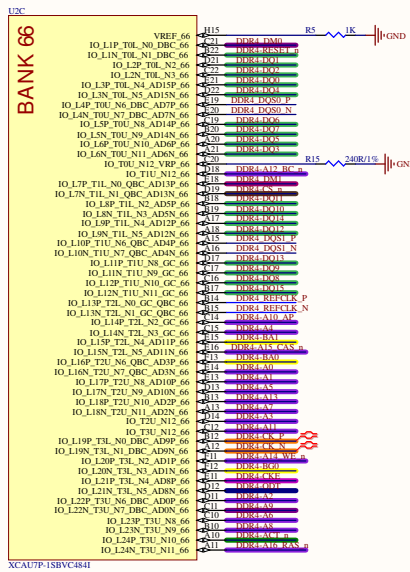
DDR4 0V6 (VTT/VREF)



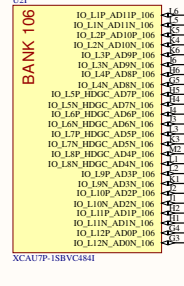
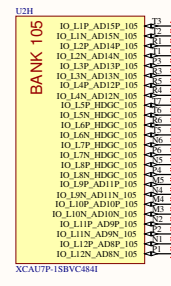
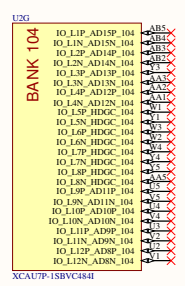
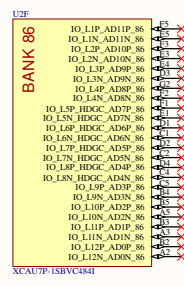
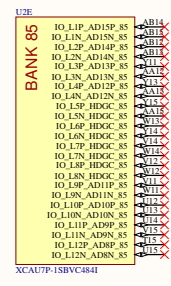
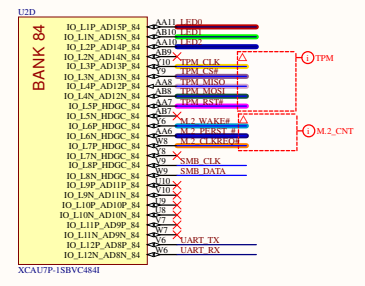
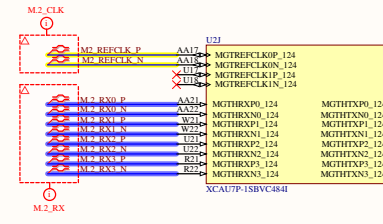
POWER 2V5 (DDR4 VPP)



HP BANKS



GTH BANK 124



FPGA POWER

