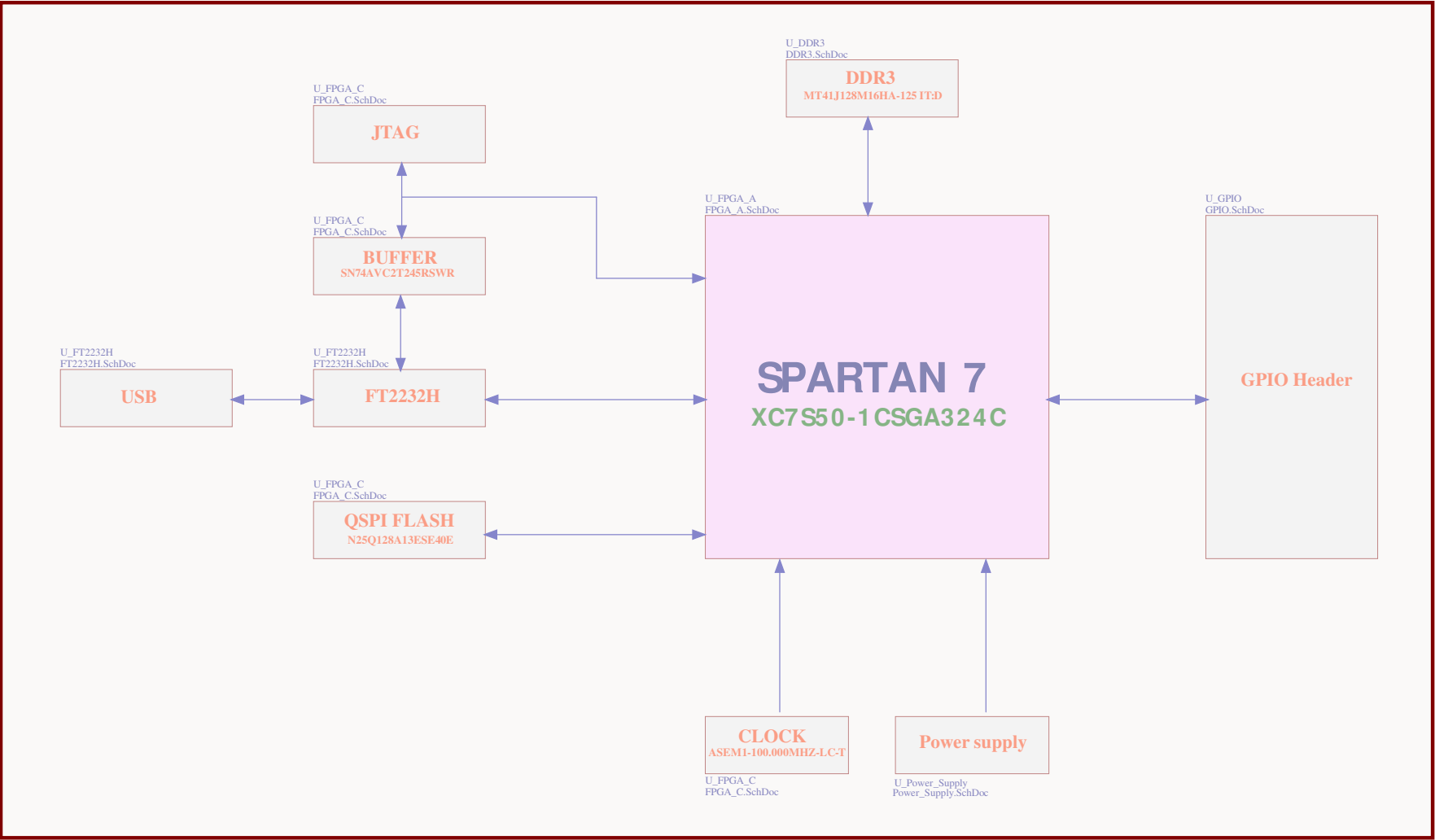
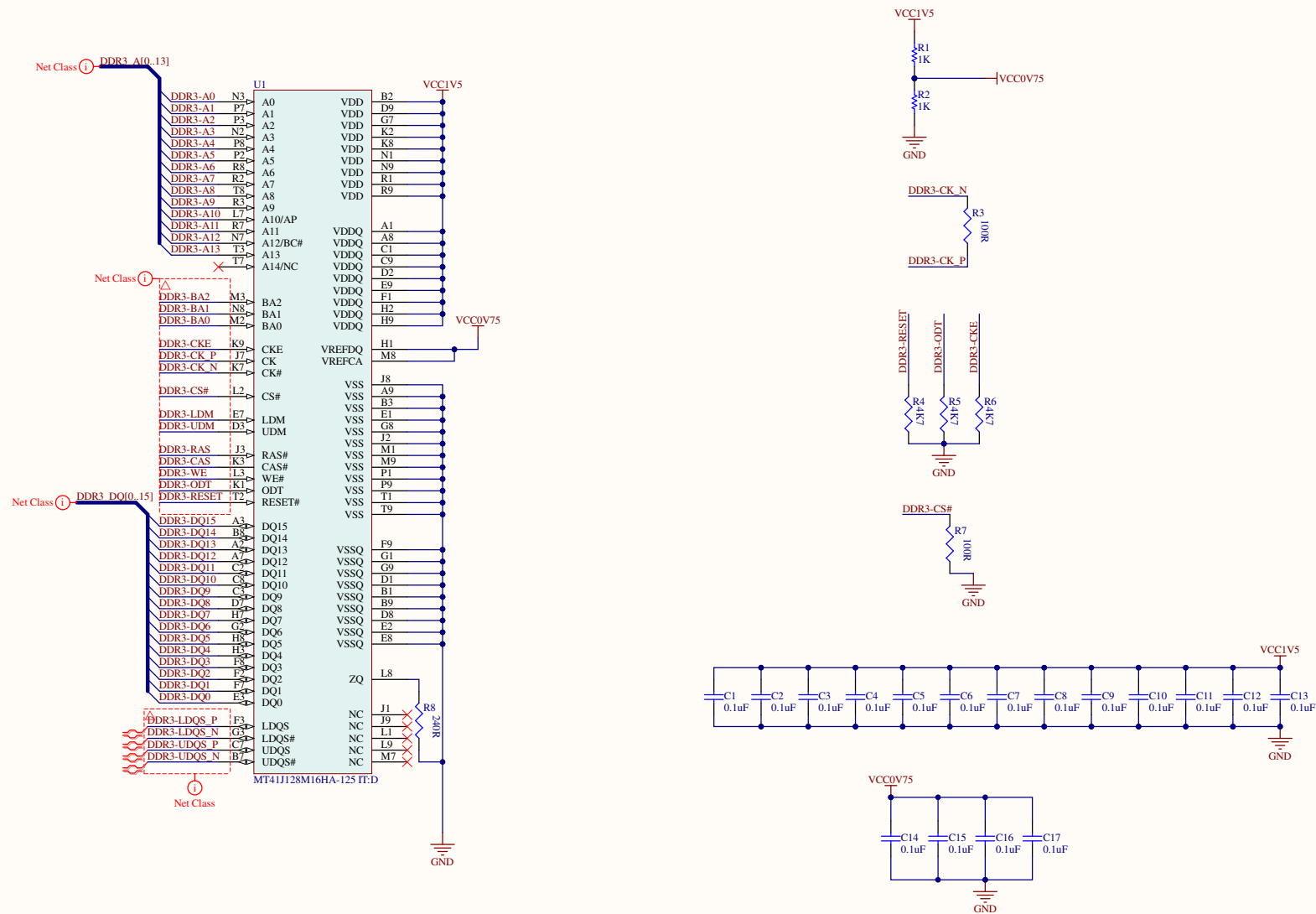
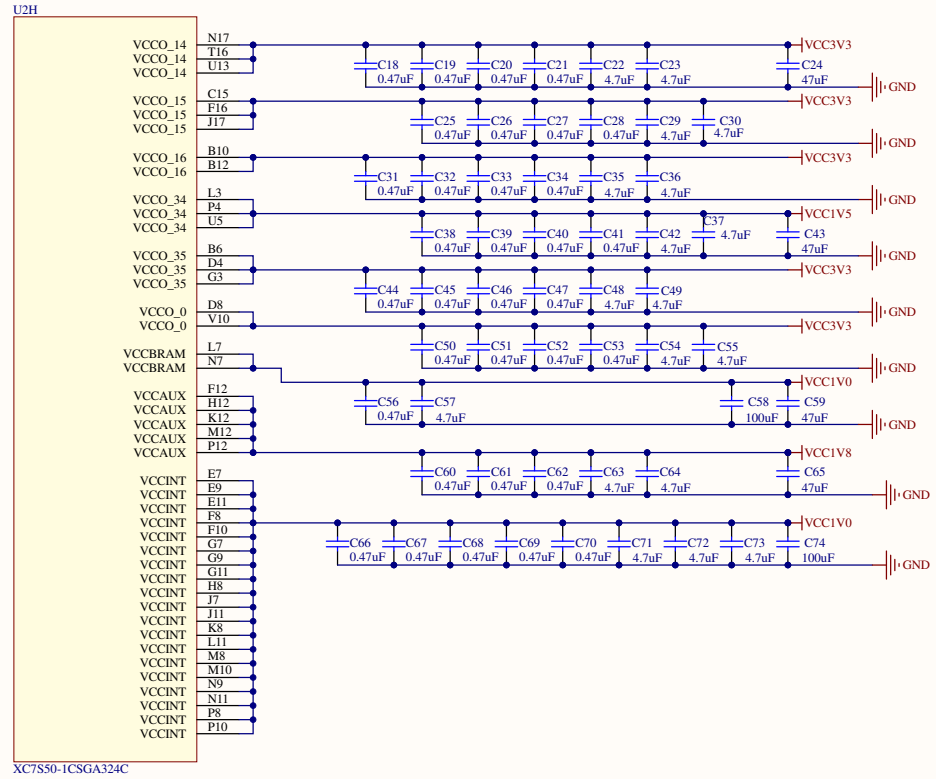
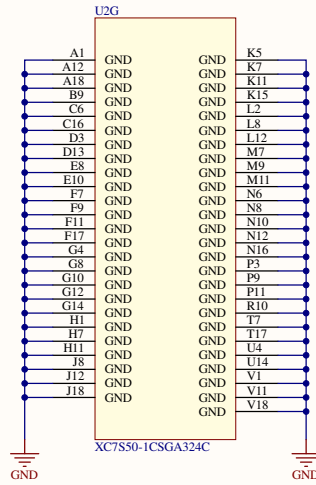




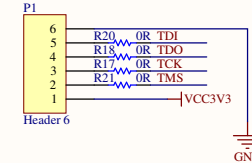
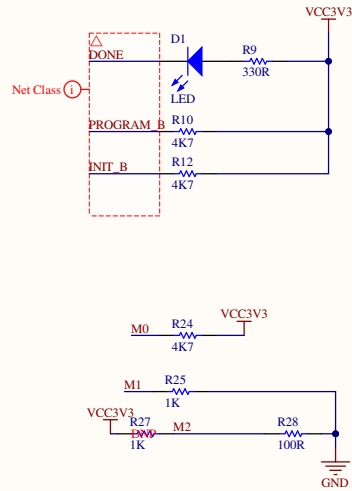
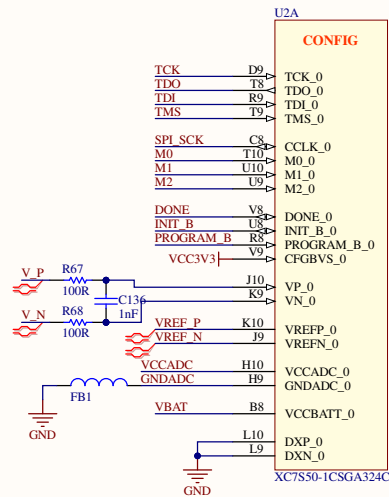
DDR3



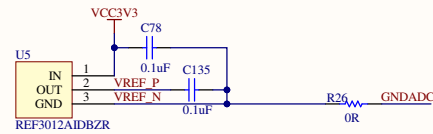
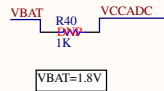
FPGA Power



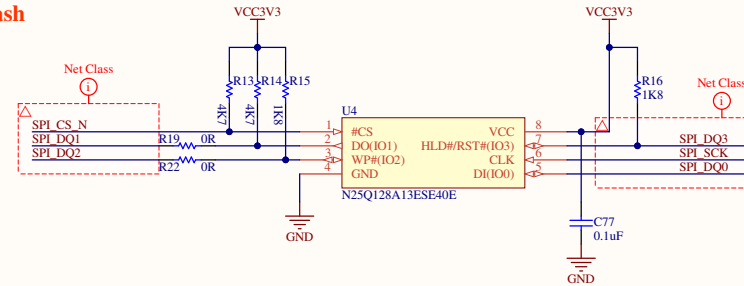
FPGA Configuration



Backup Power and ADC Reference



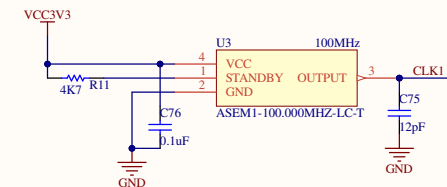
QSPI Flash



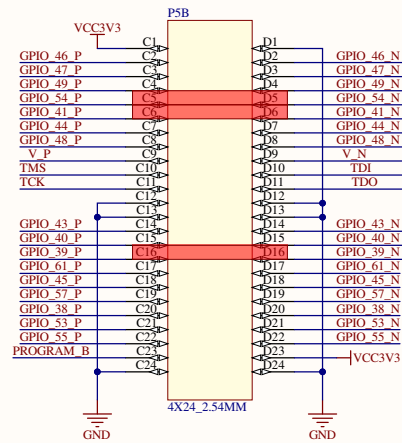
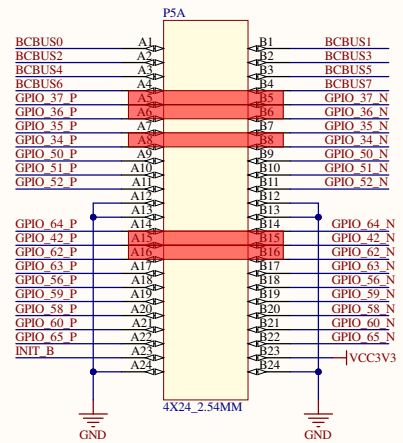
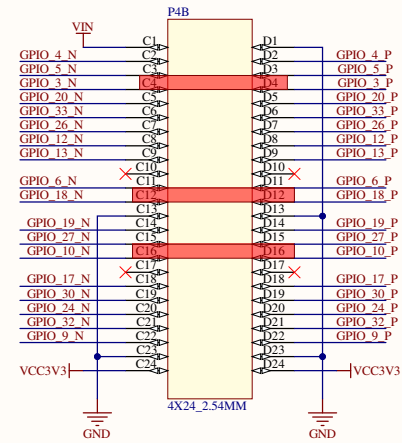
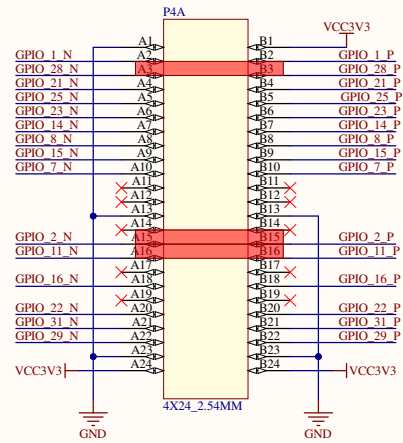
Buffers



Clock



GPIO Header



FT2232H

