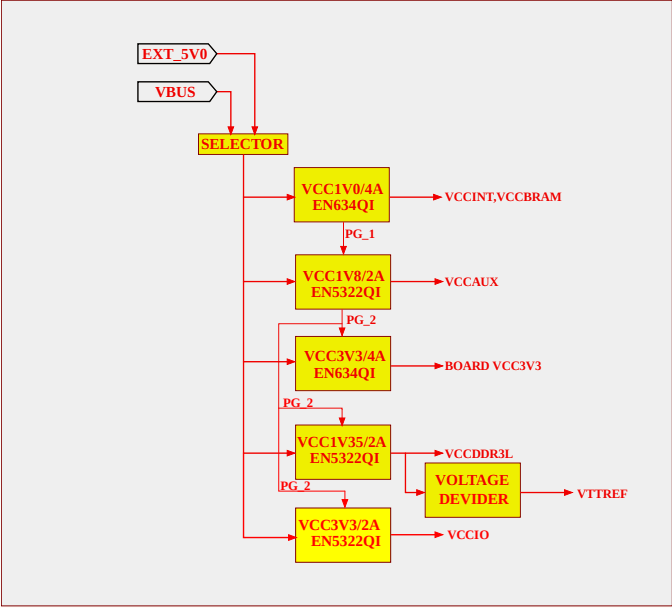
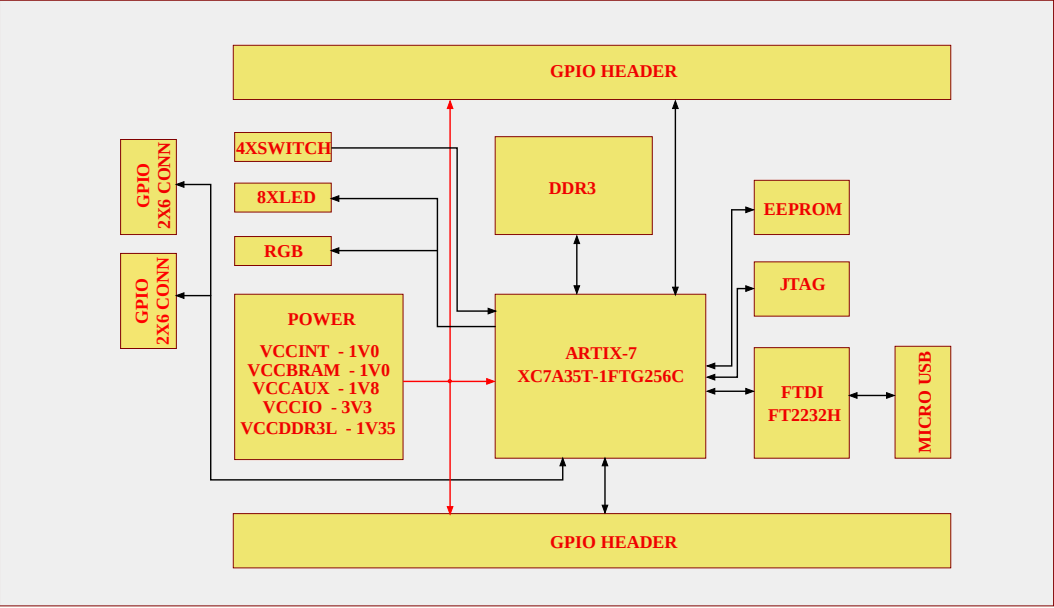
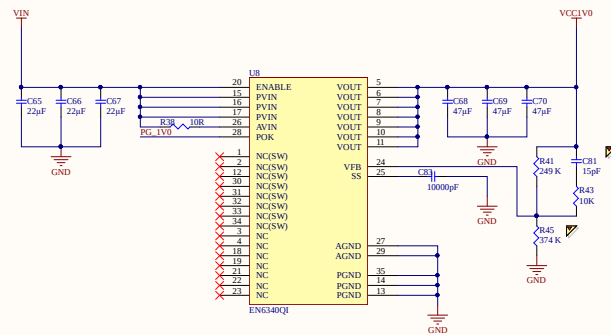


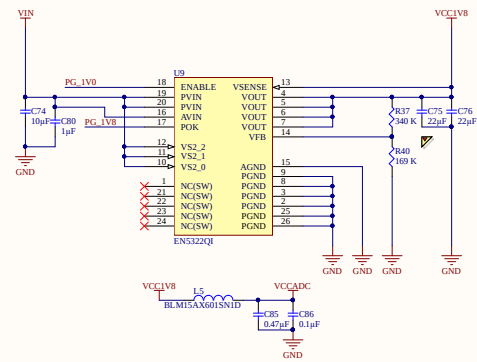
BLOCK DIAGRAM



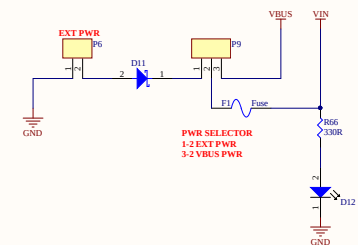
POWER SUPPLY



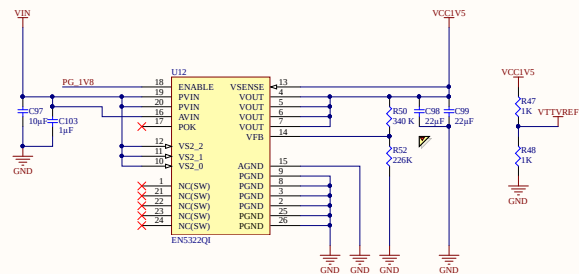
POWER VCC1V8



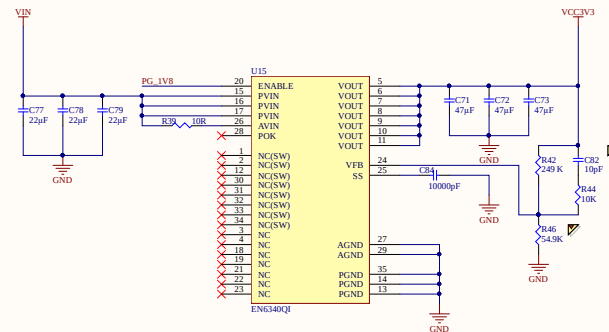
POWER SELECTOR



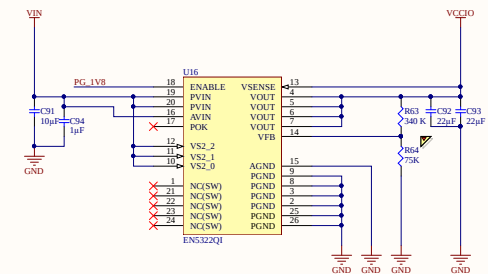
DDR3 VCC1V5, VTTREF



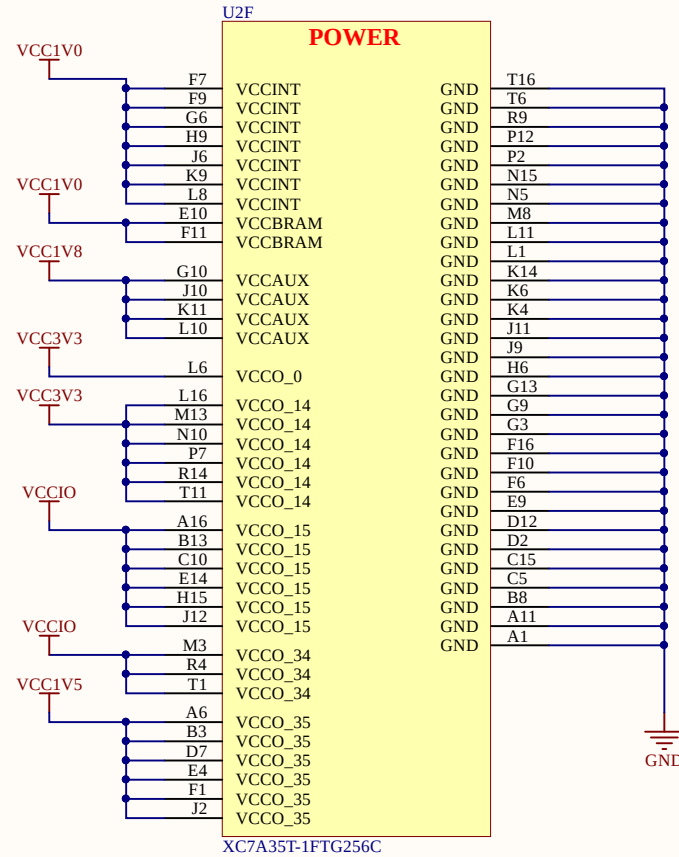
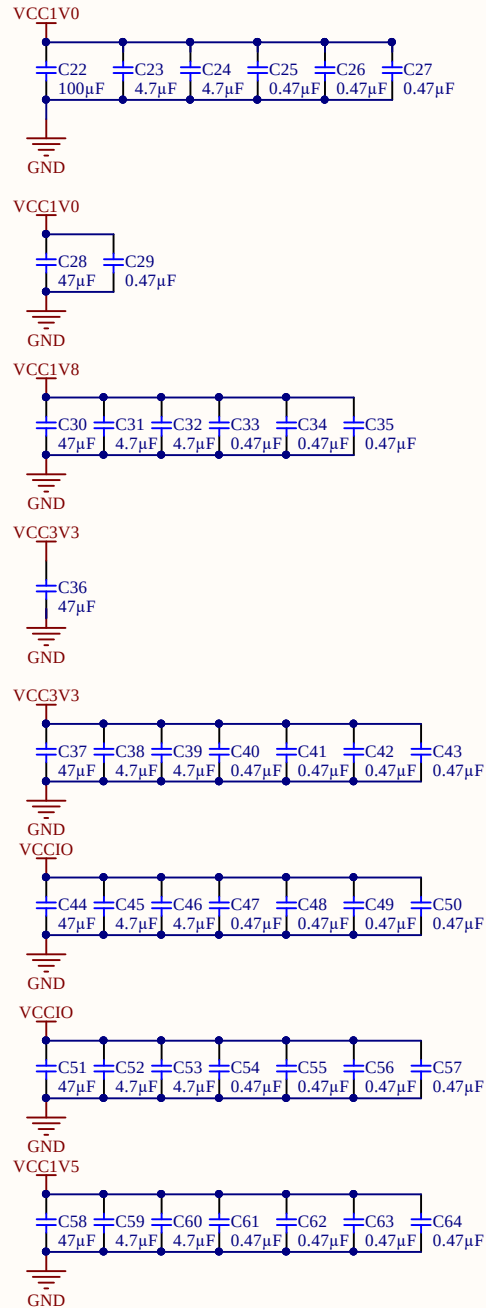
POWER VCC3V3



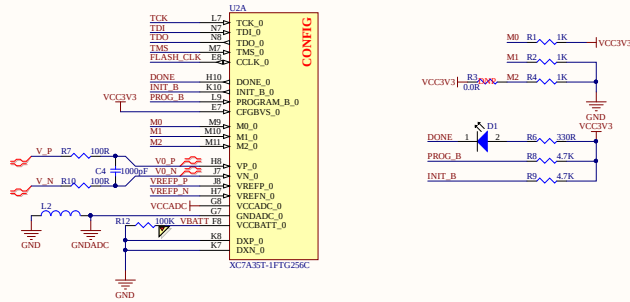
POWER VCCIO



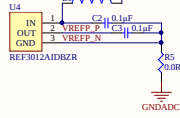
FPGA POWER



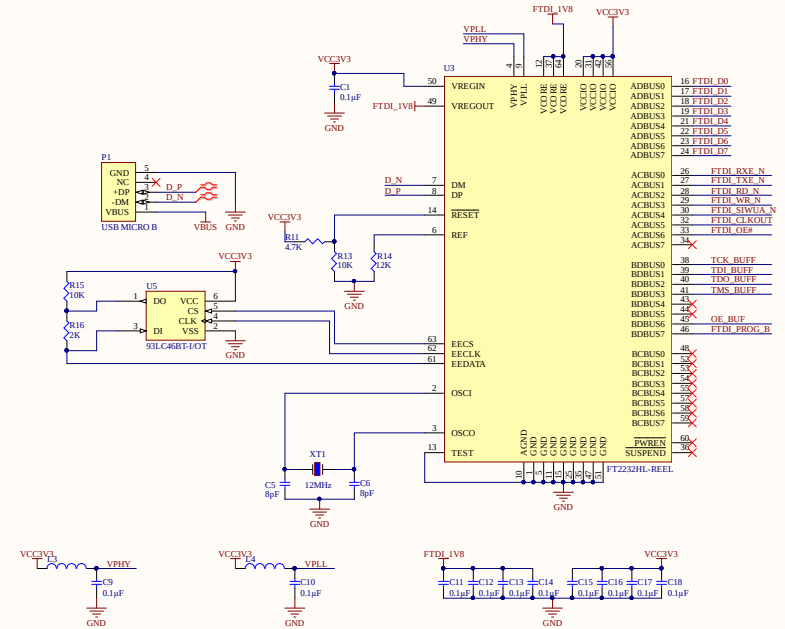
FPGA CONFIG



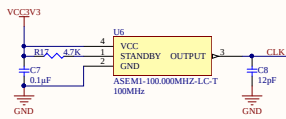
FPGA VREF



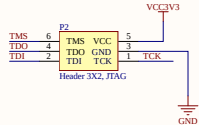
USB FTDI



FPGA CLK



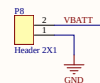
FPGA JTAG



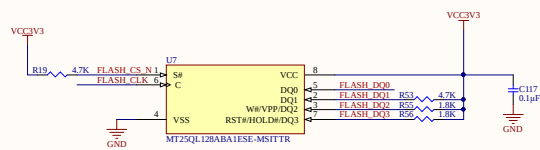
FPGA ANALOG INPUT



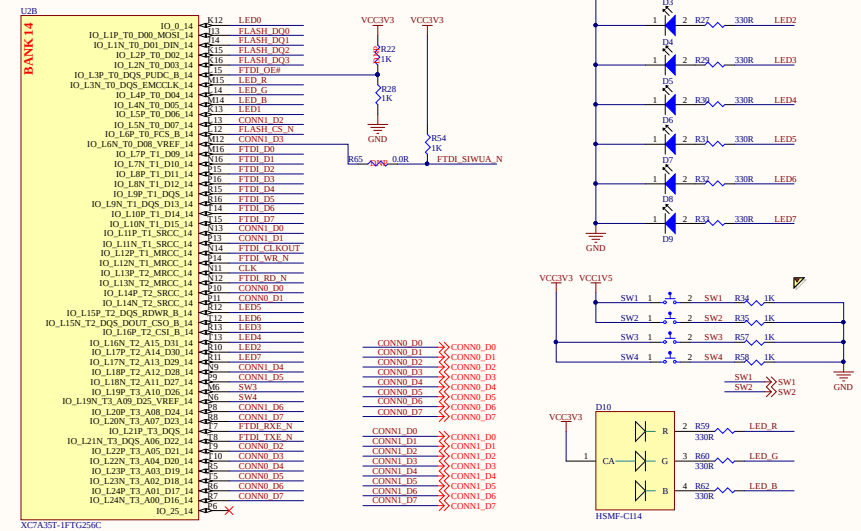
VCCBATT POWER



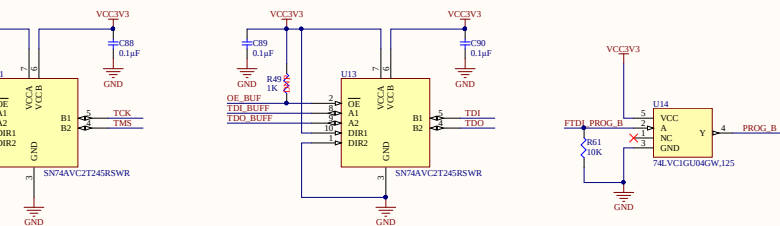
FPGA EEPROM



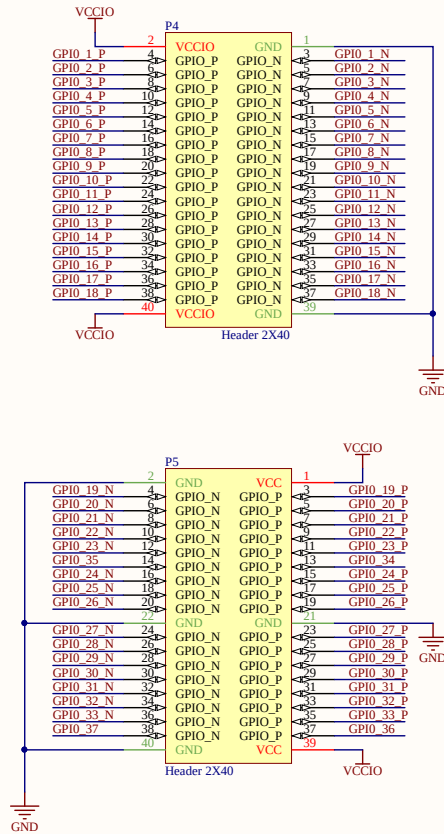
FPGA CONFIG



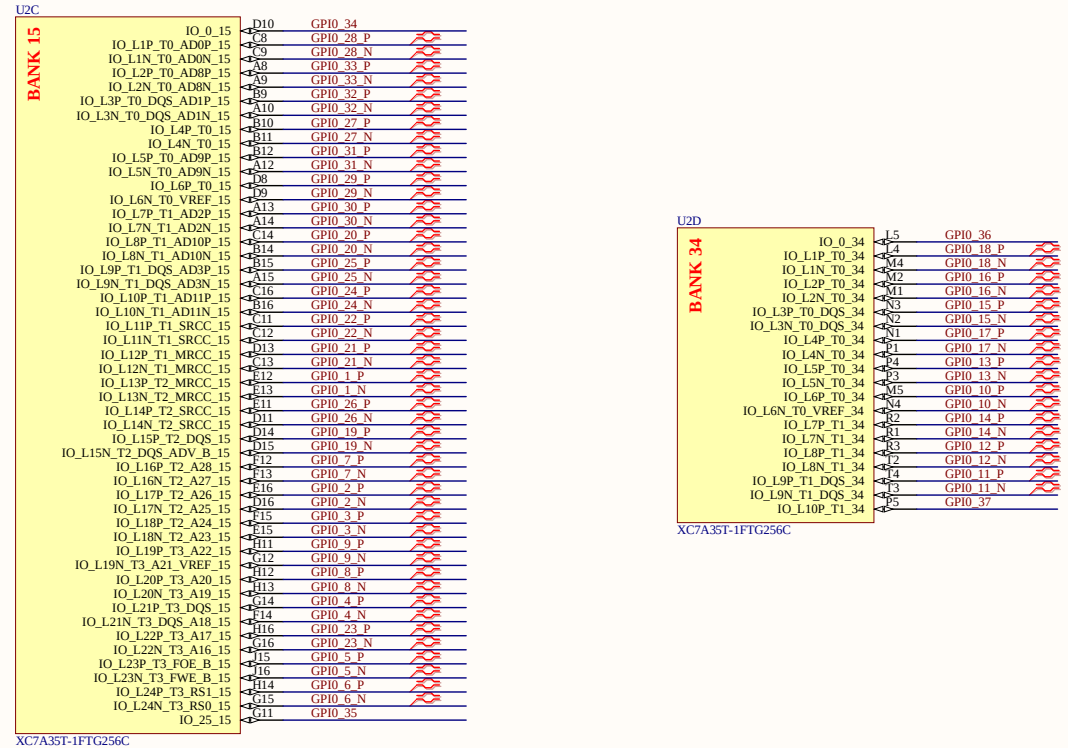
FTDI JTAG SELECTOR



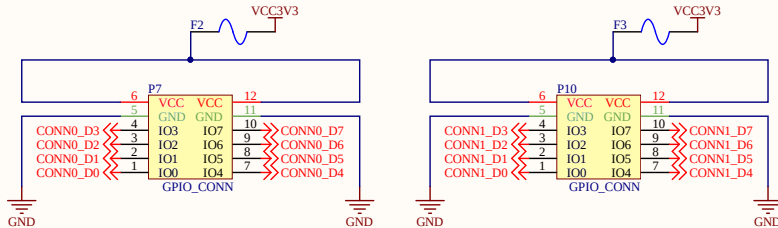
CONNECTOR GPIO



FPGA GPIO



GPIO 2X6 CONNECTOR



DDR3_FPGA

