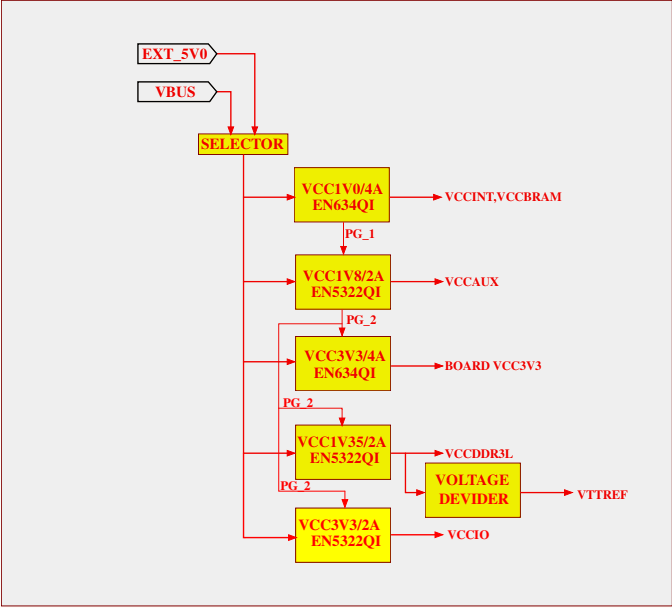
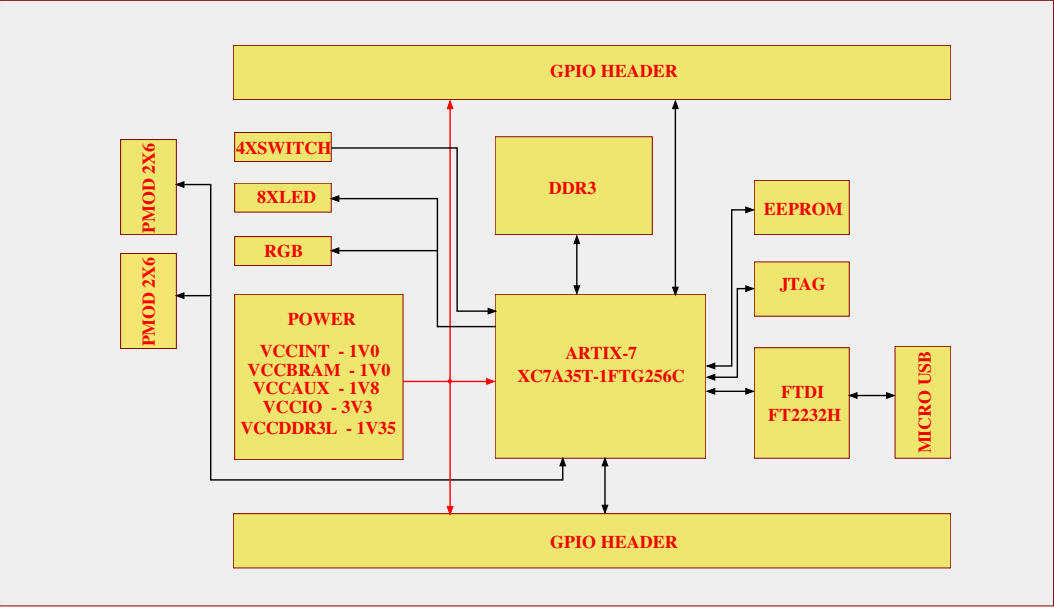
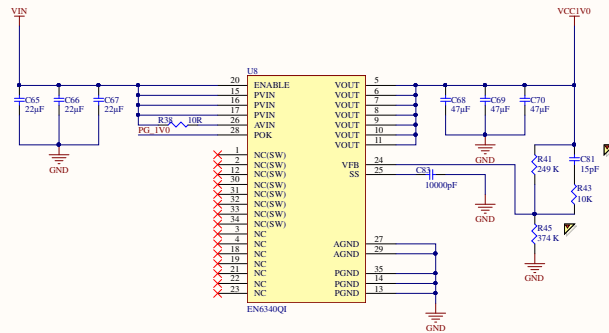


BLOCK DIAGRAM

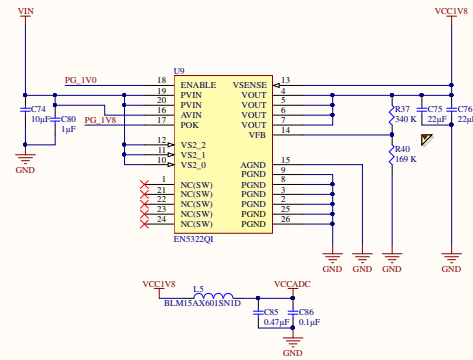


POWER SUPPLY

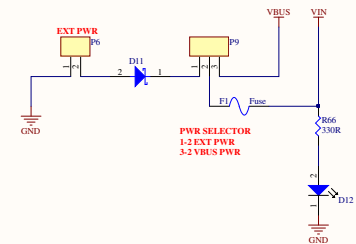
POWER VCC1V0



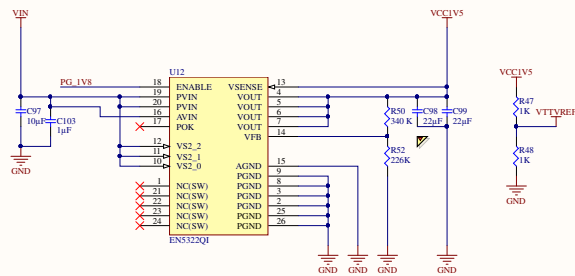
POWER VCC1V8



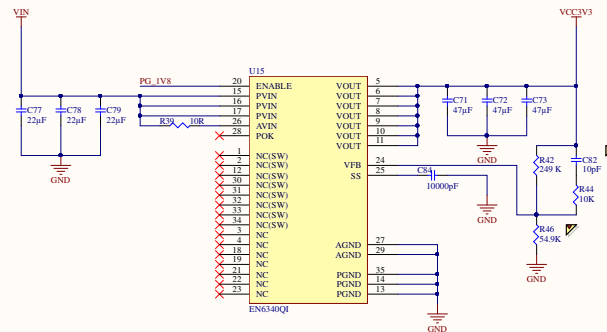
POWER SELECTOR



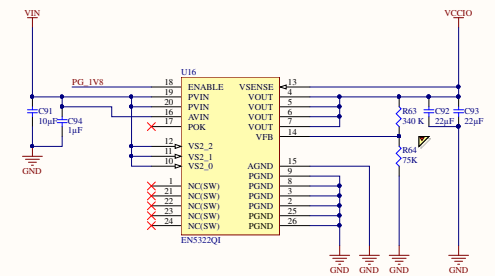
DDR3 VCC1V5, VTTREF



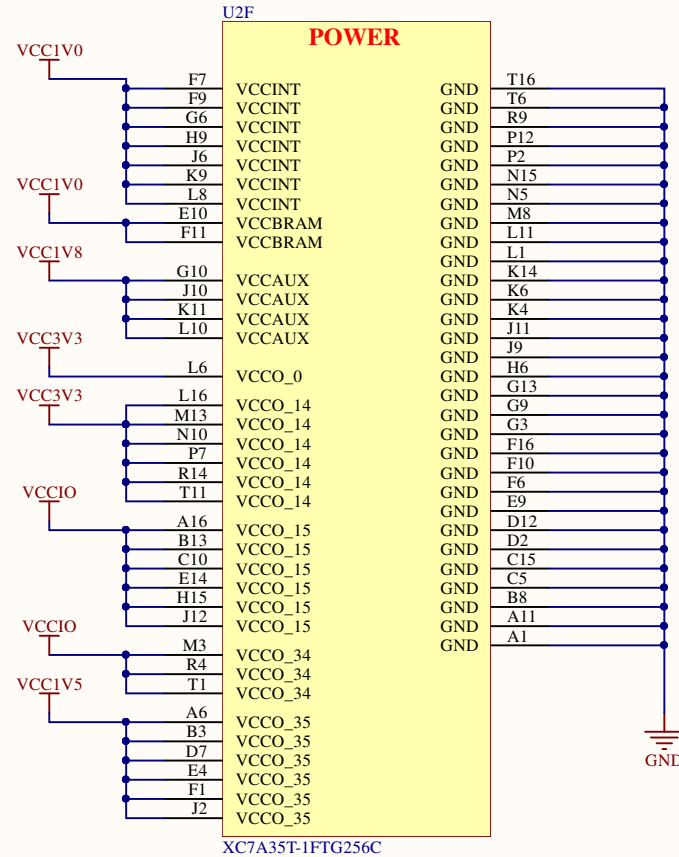
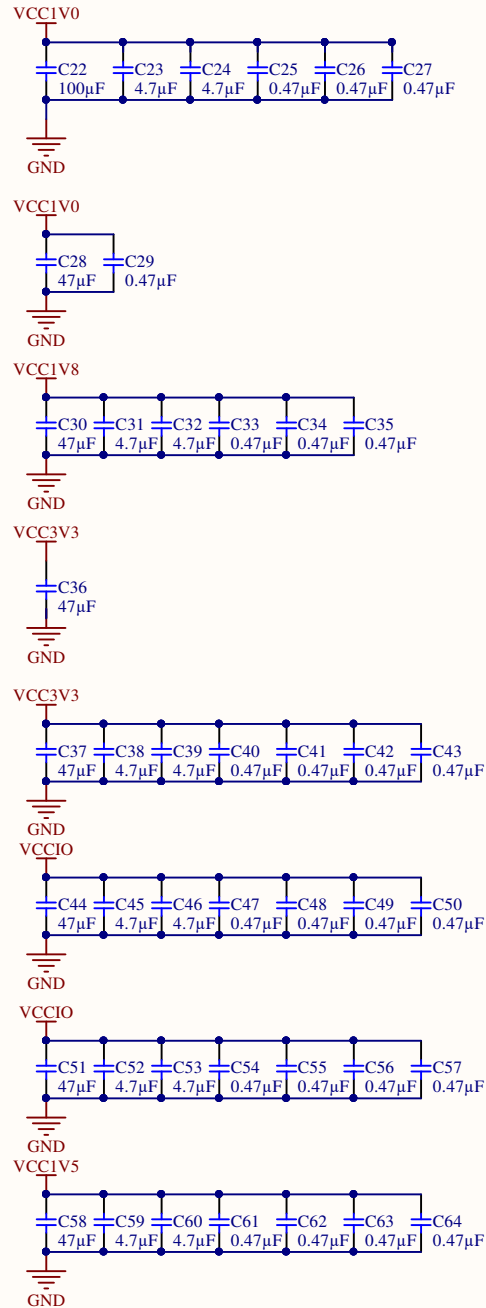
POWER VCC3V3



POWER VCCIO

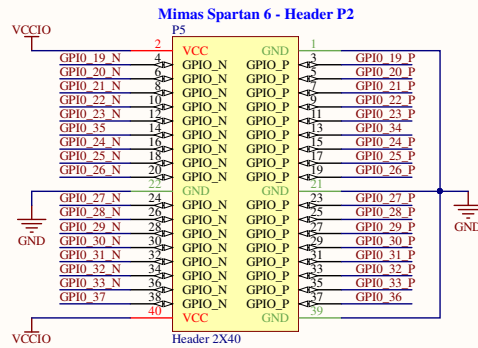
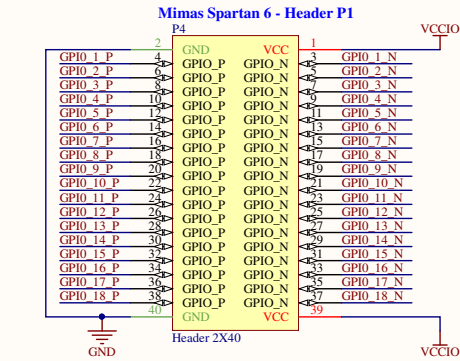


FPGA POWER

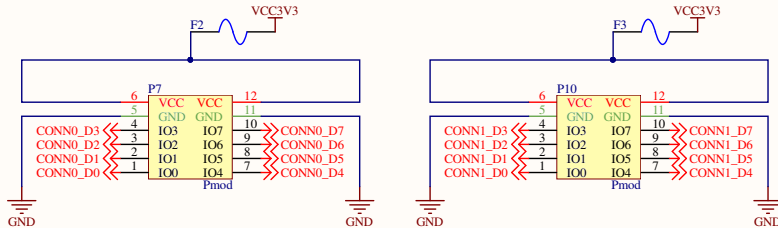


Title: FPGA_POWER		Revision: V 4.0	Numato Systems Pvt Ltd	
Size: A4	Project: mimas-a7-mini-board.PrjPCB			
Date: 22-11-2019	Time: 10:44:26	Sheet 3 of 6		
File: FPGA_POWER.SchDoc				

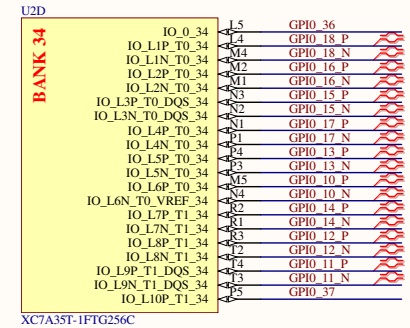
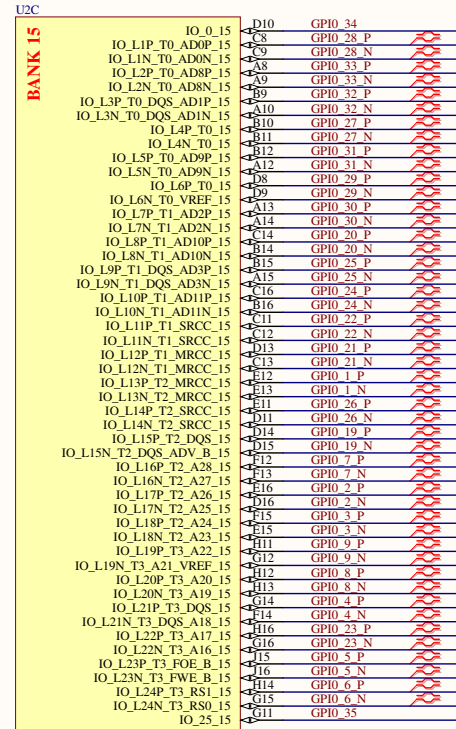
CONNECTOR GPIO



GPIO 2X6 CONNECTOR



FPGA GPIO



DDR3_FPGA

