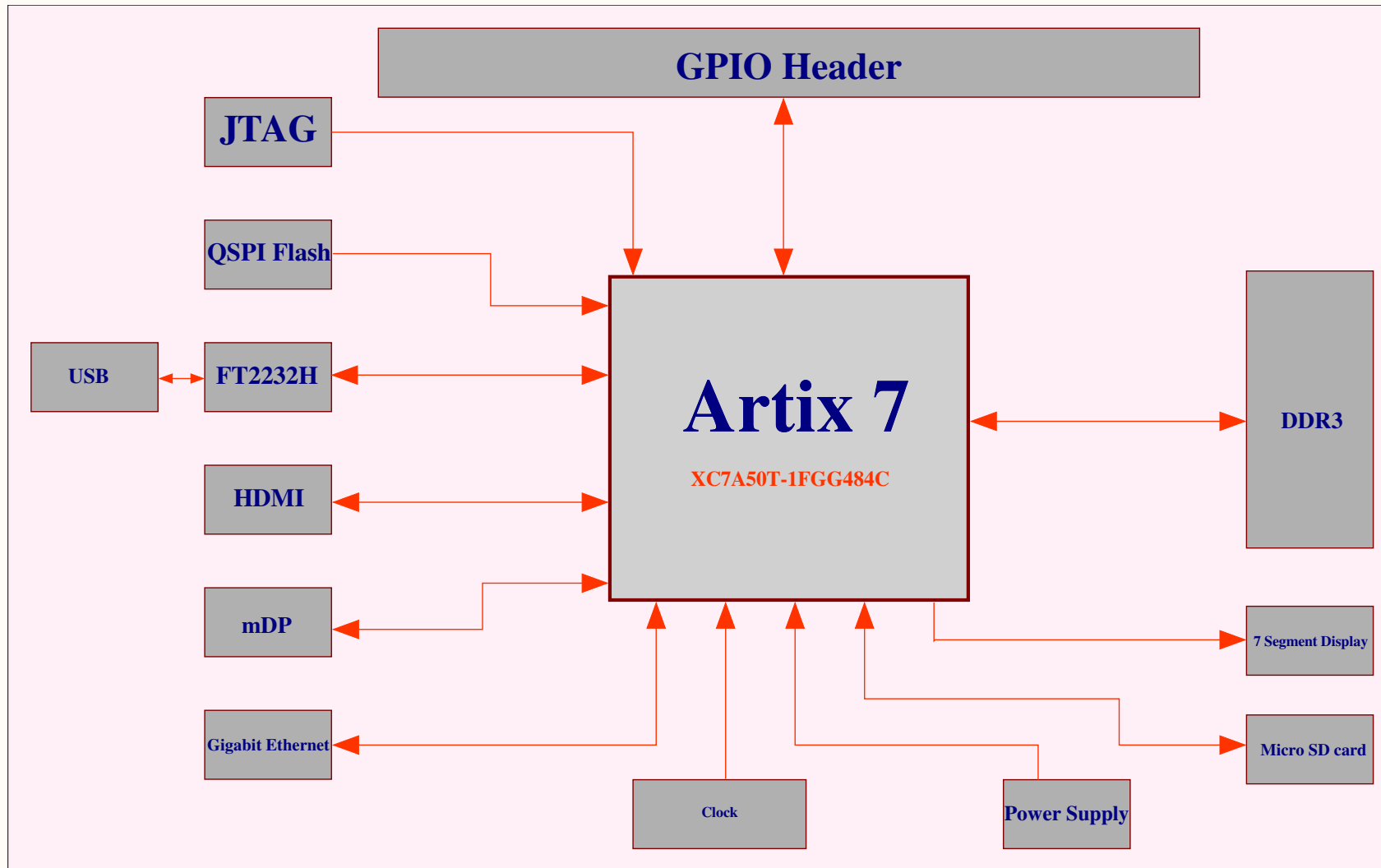
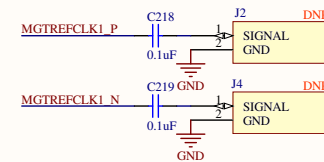


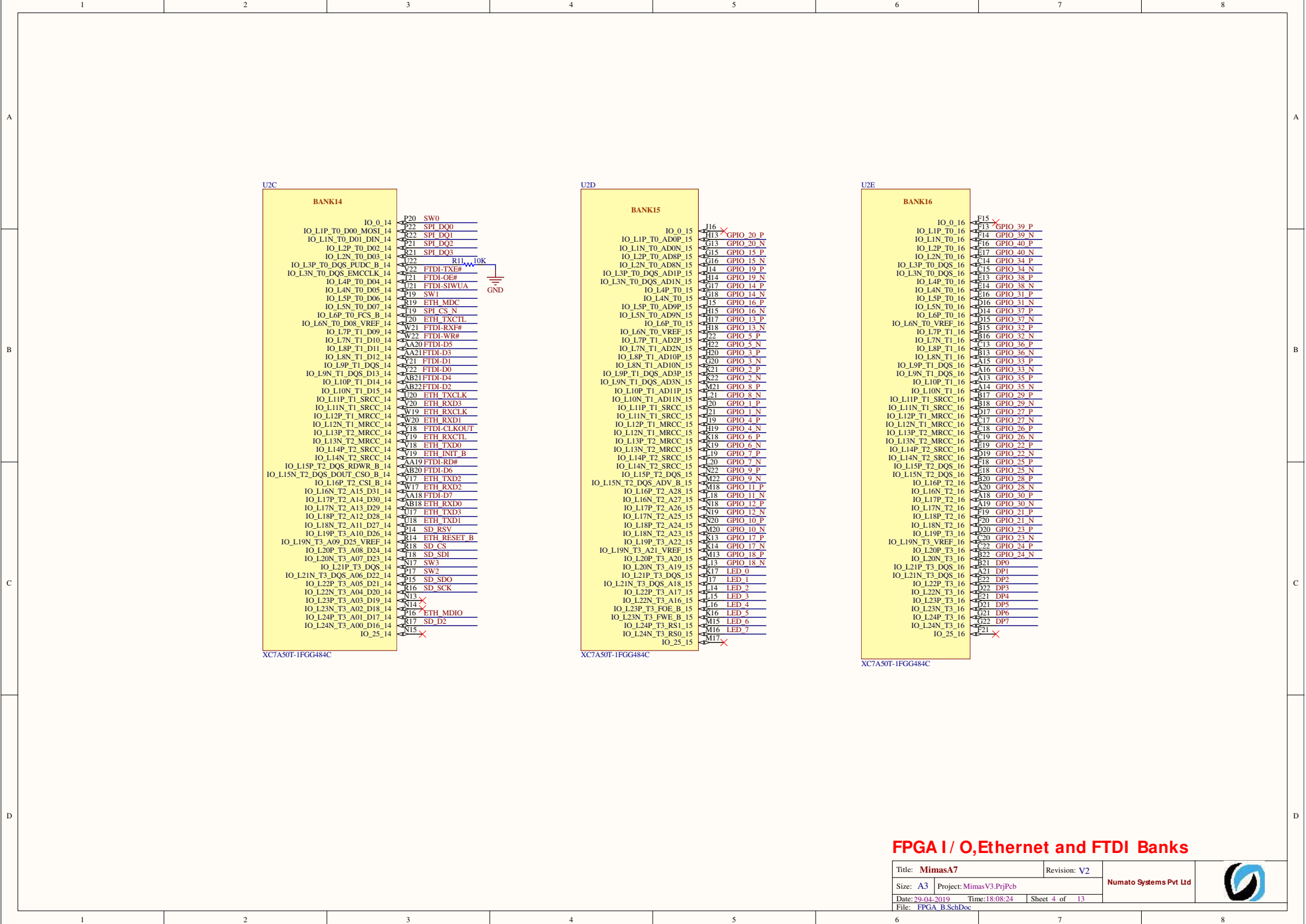


Block Diagram

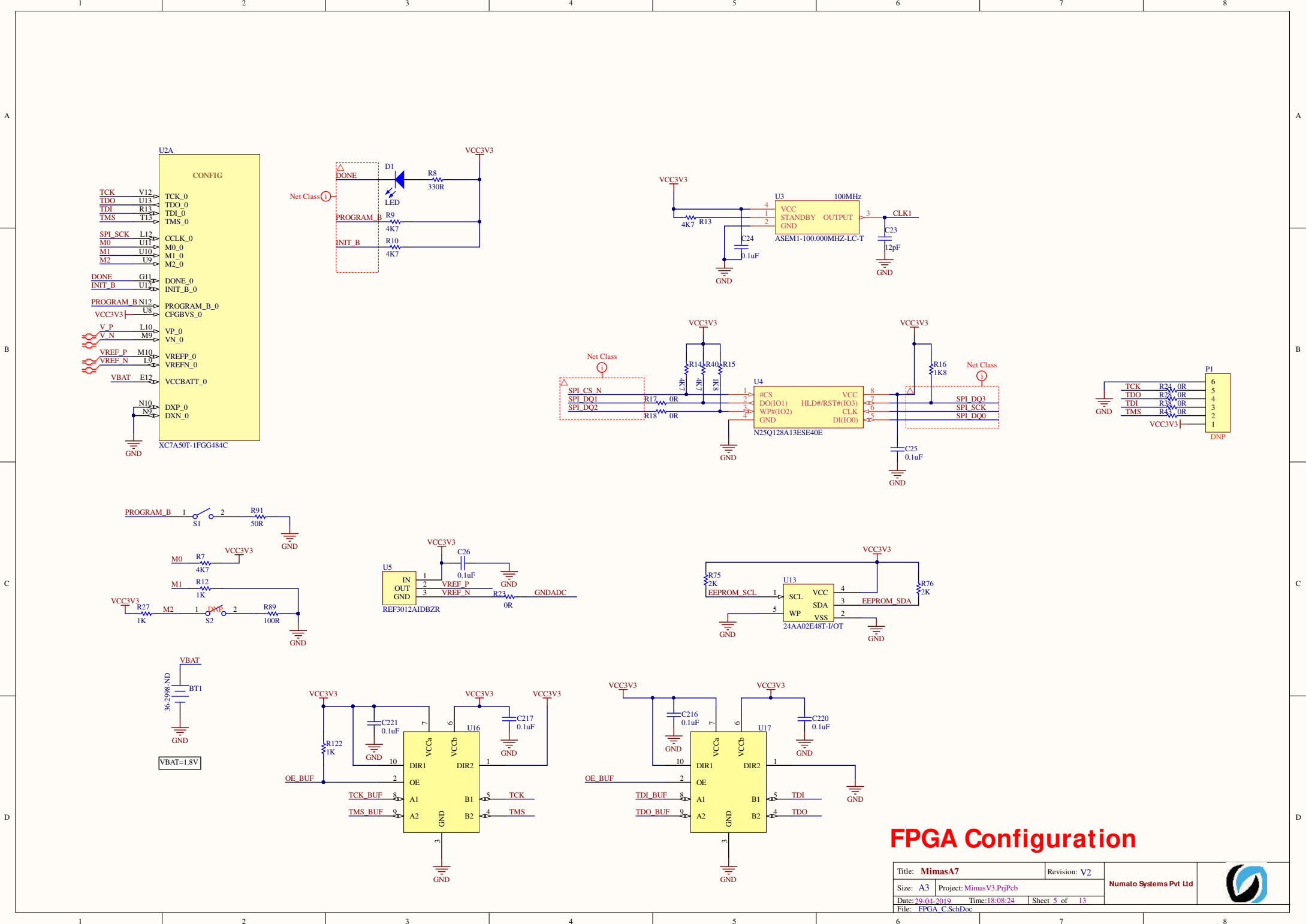
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Size: A3	Project: MimasV3.PrjPcb			
Date: 29-04-2019	Time: 18:08:23	Sheet 1 of 13		
File: Block_Diagram.SchDoc				



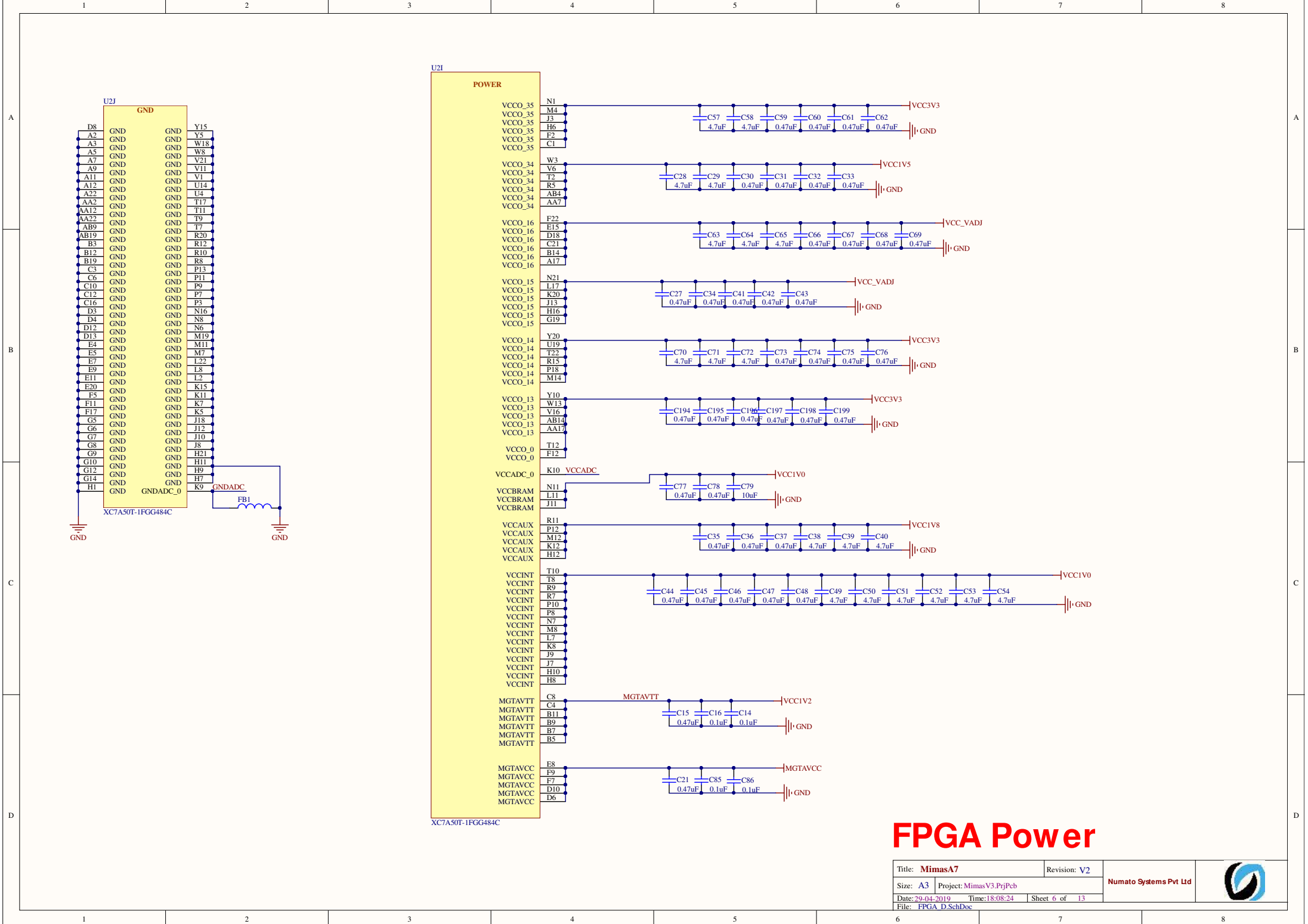
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Date: 29.04.2019	Time: 18:08:24	Sheet 3 of 13	
File: FPGA_A.SchDoc			



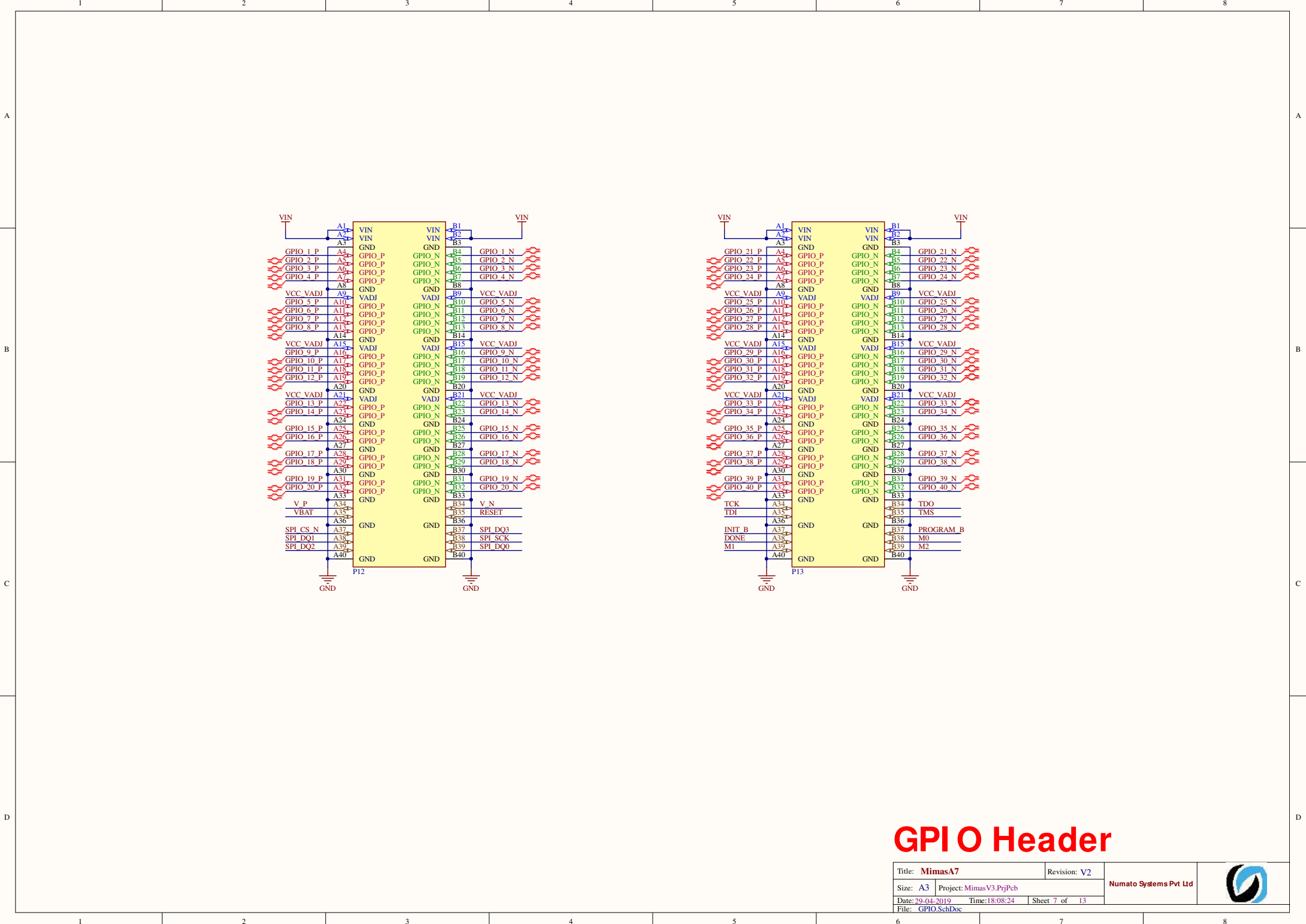
FPGA I / O,Ethernet and FTDI Banks



FPGA Configuration

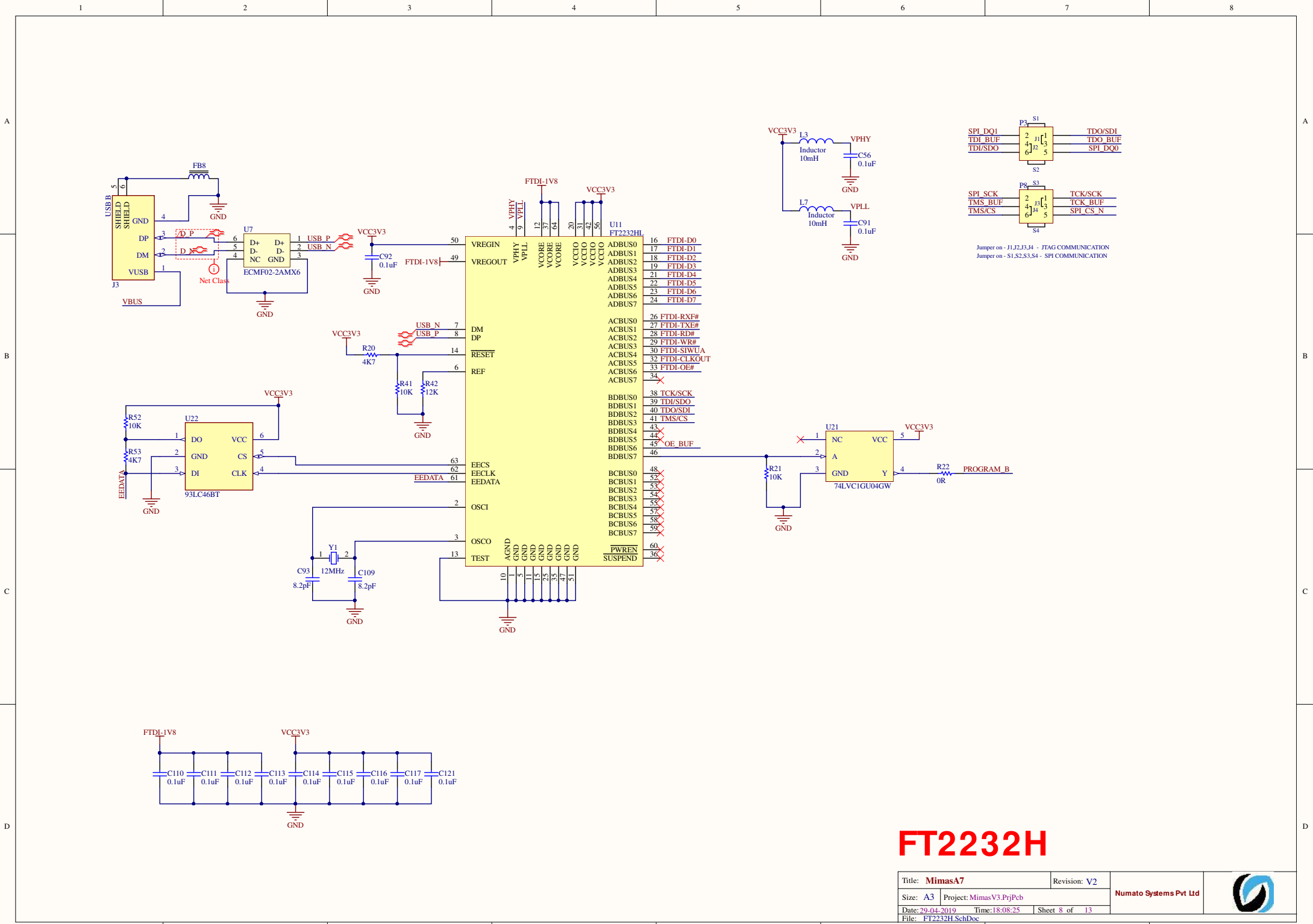


FPGA Power



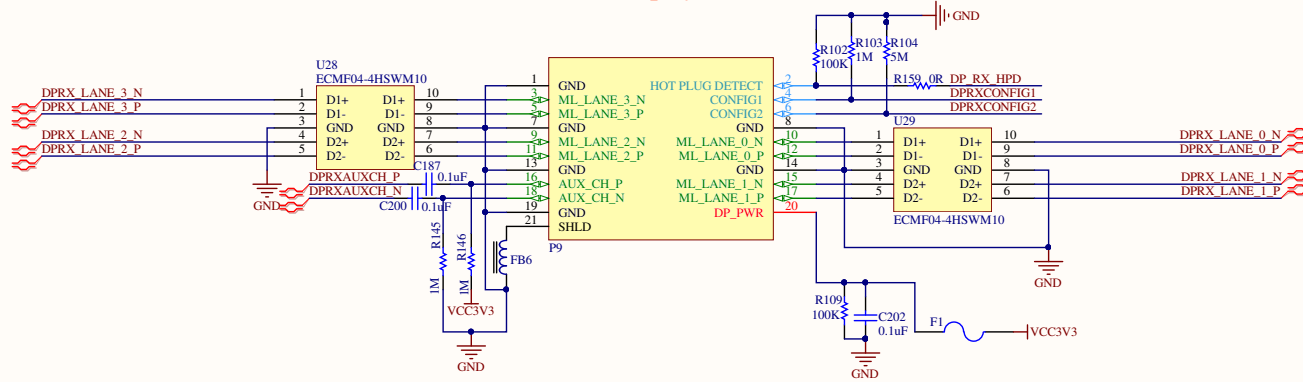
GPI O Header

Title: MimasA7		Revision: V2	Numato Systems Pvt Ltd	
Size: A3	Project: MimasV3.PrjPcb			
Date: 29.04.2019	Time: 18:08:24	Sheet 7 of 13		
File: GPI O.SchDoc				

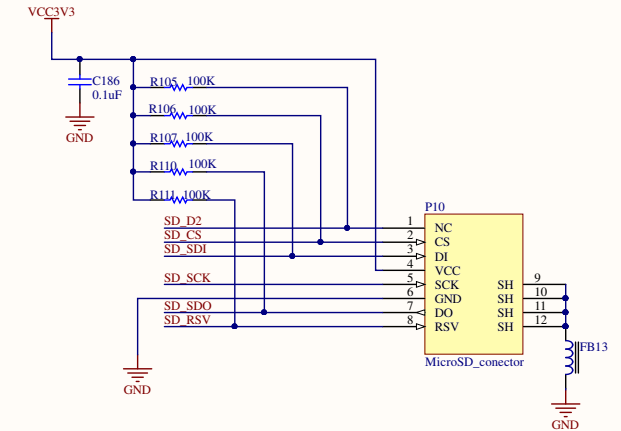
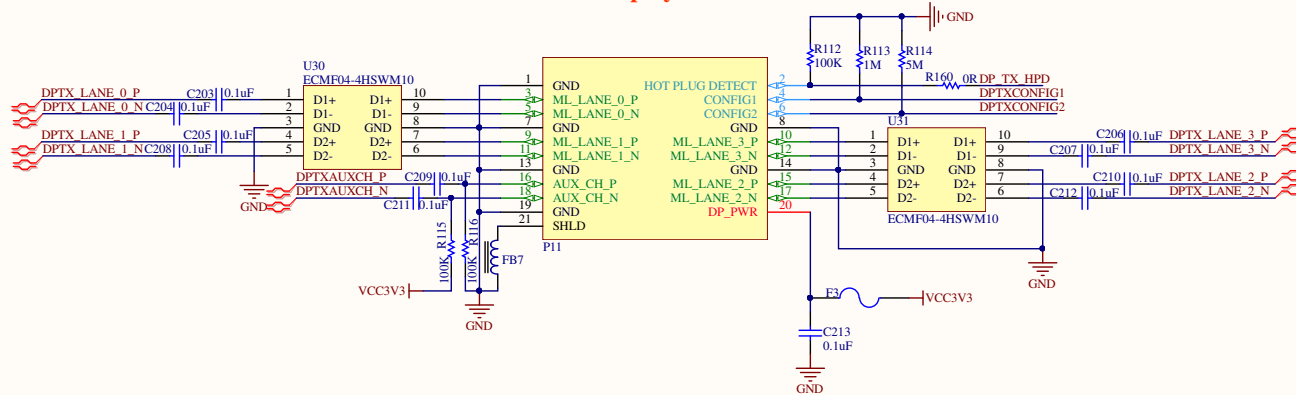


FT2232H

Mini Display Port RX

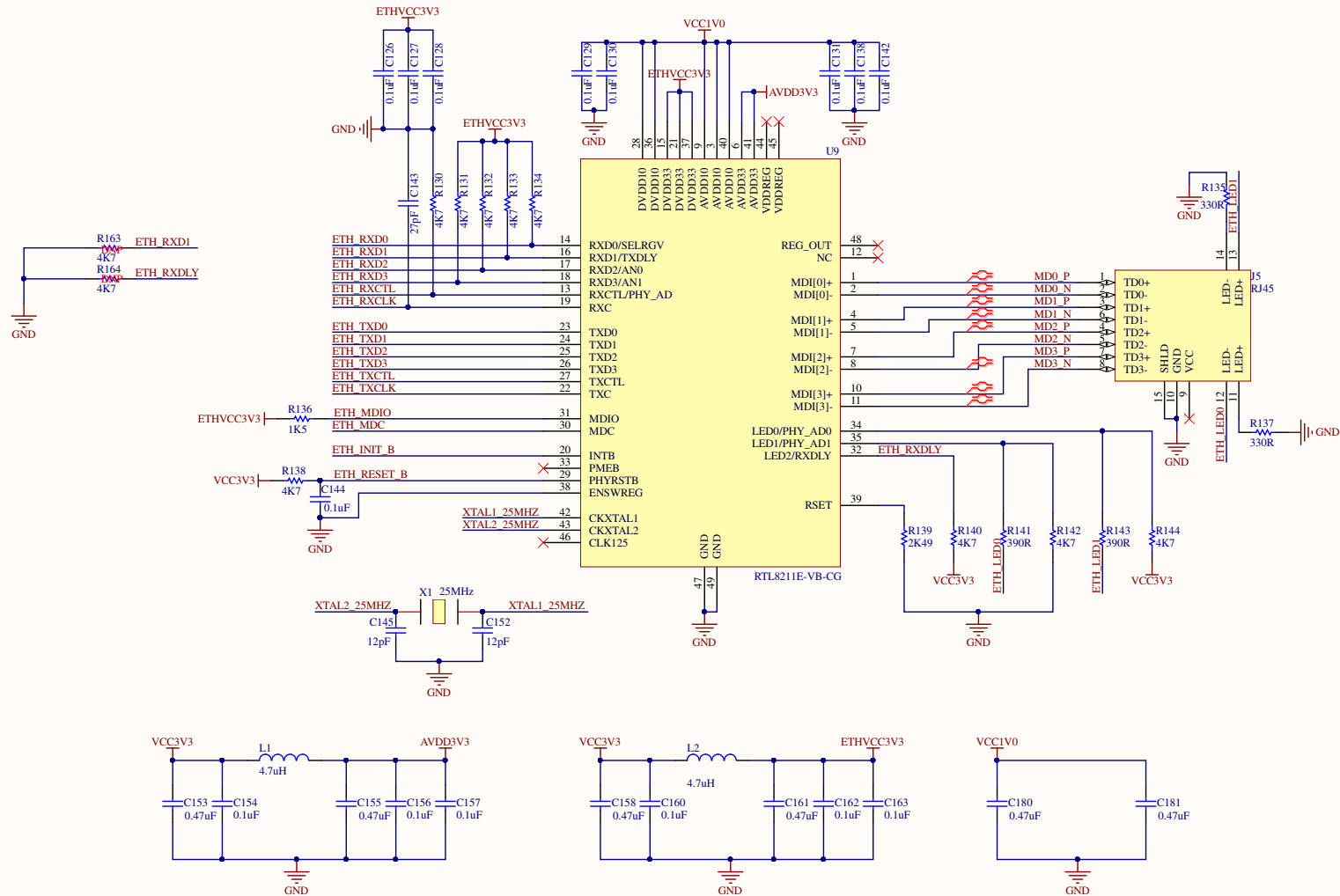


Mini Display Port TX



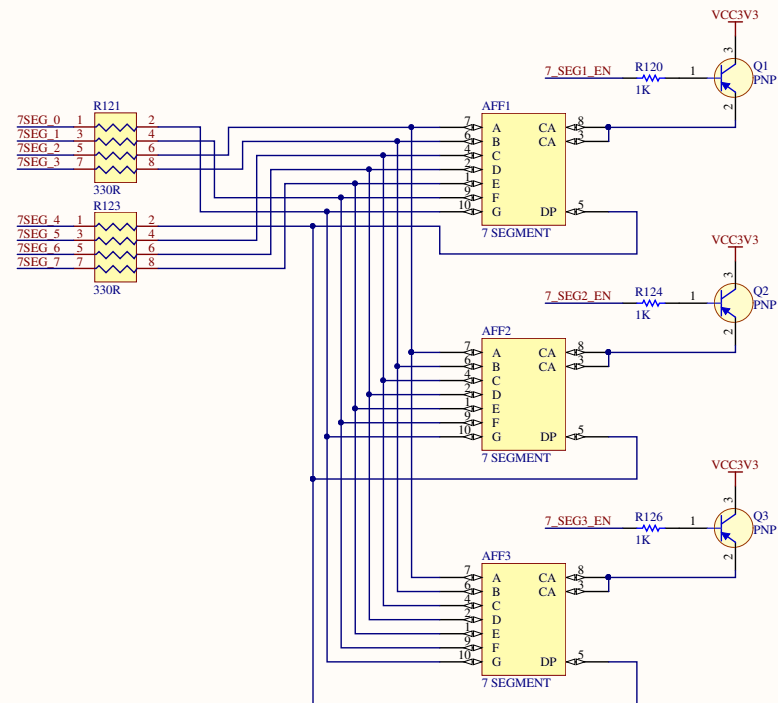
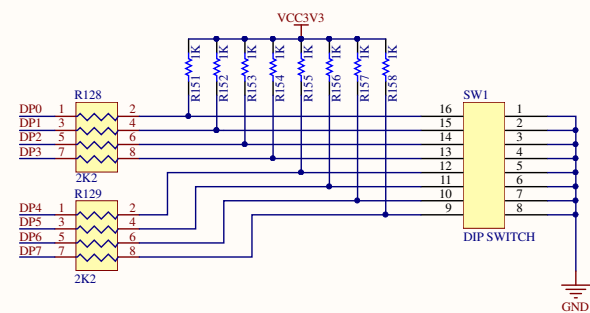
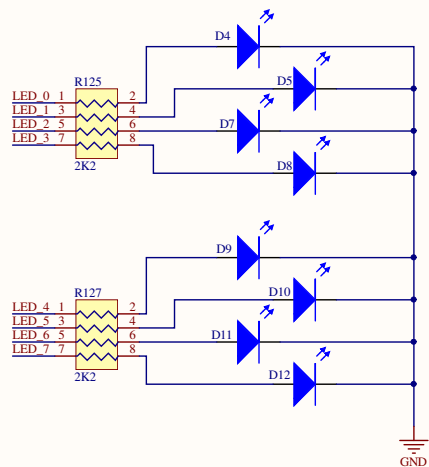
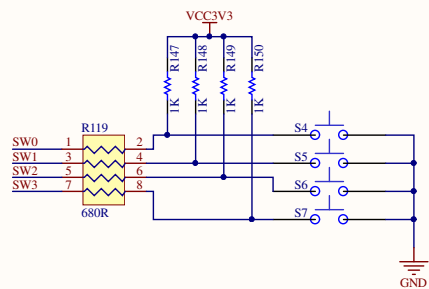
Mini Display Port & SD Card

Title: MimasA7	Revision: V2	Numato Systems Pvt Ltd	
Size: A3	Project: MimasV3.PrjPcb		
Date: 29.04.2019	Time: 18:08:25		
File: Display_Port.SchDoc	Sheet: 10 of 13		



Ethernet

Title: MimasA7	Revision: V2	Numato Systems Pvt Ltd	
Size: A3	Project: MimasV3.PrjPcb		
Date: 29.04.2019	Time: 18:08:25		
File: Ethernet.SchDoc	Sheet 11 of 13		



Push Button,DIP Switch,LEDs & 7 Segment Display

Title: MimasA7	Revision: V2	Numato Systems Pvt Ltd	
Size: A3	Project: MimasV3.PrjPcb		
Date: 29.04.2019	Time: 18:08:25	Sheet 12 of 13	
File: 7_Segment_Display.SchDoc			

