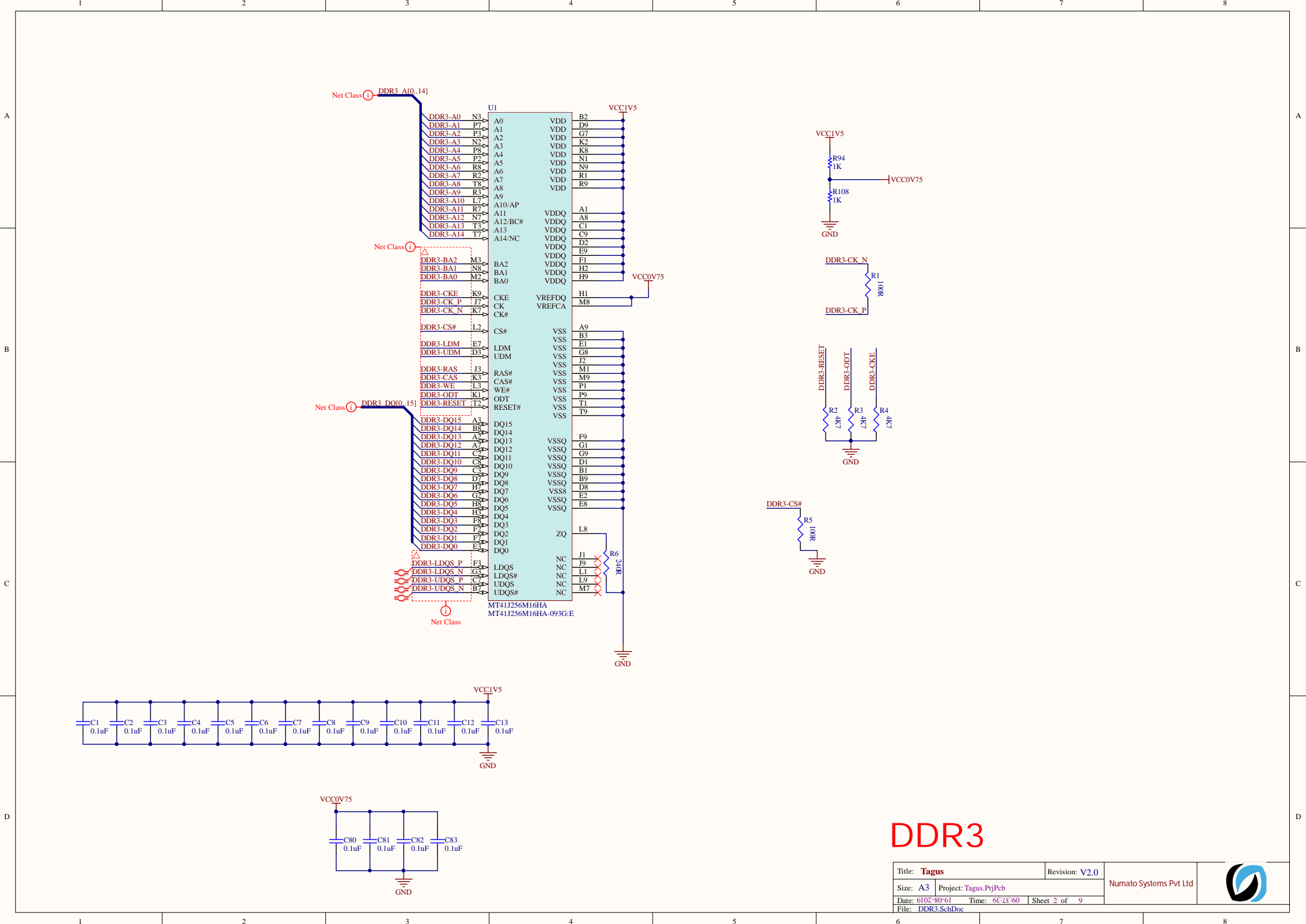


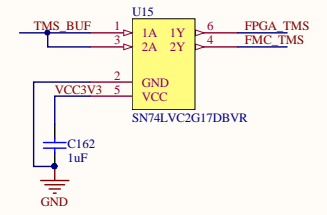
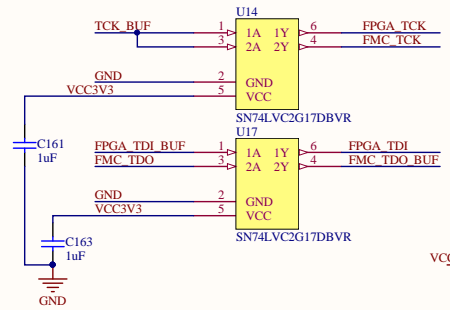
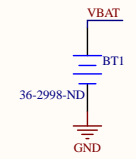
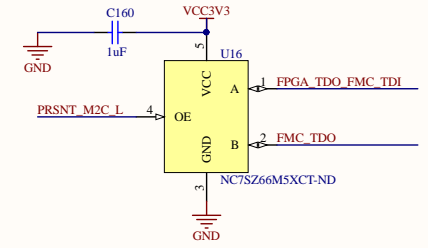
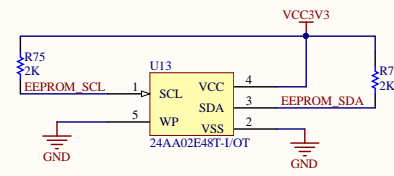
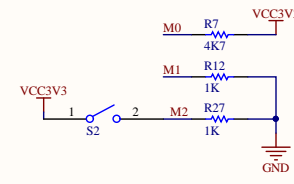
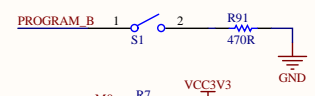
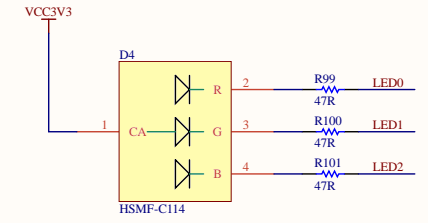
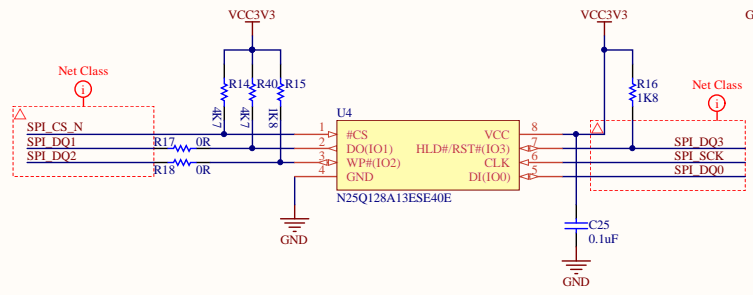
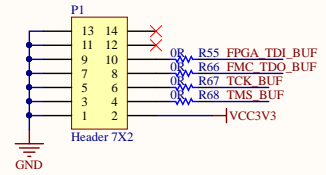
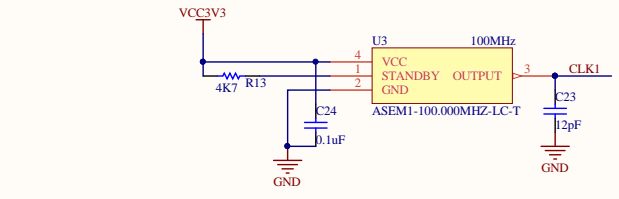
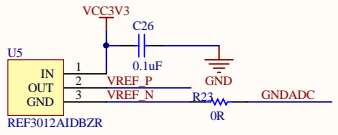
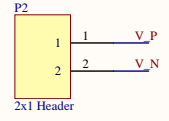
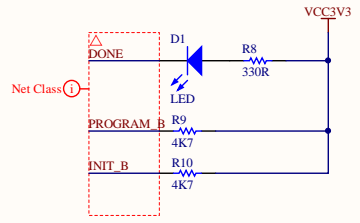
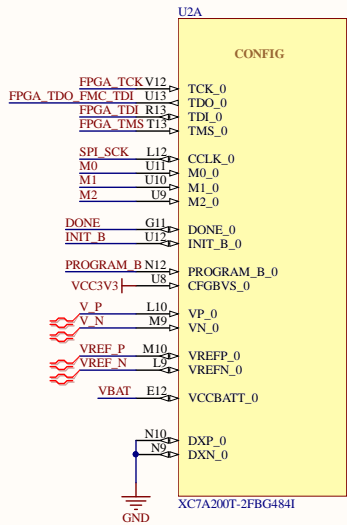
# Block Diagram



# DDR3

Title: <b>Tagus</b>	Revision: <b>V2.0</b>	Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>Tagus.PjgPcb</b>	
Date: <b>6107-8061</b>	Time: <b>6E7E60</b>	Sheet <b>2</b> of <b>9</b>
File: <b>DDR3.SchDoc</b>		





# FPGA Configuration

Title: <b>Tagus</b>	Revision: <b>V2.0</b>	Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>Tagus.PjgPcb</b>	
Date: <b>6107-80-61</b>	Time: <b>6E7E60</b>	
File: <b>FPGA_C.SchDoc</b>	Sheet <b>3 of 9</b>	



**U2B**

**\*BANK13**

IO_0_13	Y17	<del>X</del>
IO_L1P_T0_13	A16	<del>X</del>
IO_L1N_T0_13	AB16	<del>X</del>
IO_L2P_T0_13	AB17	<del>X</del>
IO_L2N_T0_13	AB18	<del>X</del>
IO_L3P_T0_DQS_13	AA18	<del>X</del>
IO_L3N_T0_DQS_13	AB18	<del>X</del>
IO_L4P_T0_13	AA18	<del>X</del>
IO_L4N_T0_13	Y13	<del>X</del>
IO_L5P_T0_13	A14	<del>X</del>
IO_L5N_T0_13	W14	<del>X</del>
IO_L6P_T0_13	Y14	<del>X</del>
IO_L6N_T0_VREF_13	AB11	<del>X</del>
IO_L7P_T1_13	AB12	<del>X</del>
IO_L7N_T1_13	AB10	<del>X</del>
IO_L8P_T1_13	AA10	<del>X</del>
IO_L8N_T1_13	AA11	<del>X</del>
IO_L9P_T1_DQS_13	W10	<del>X</del>
IO_L9N_T1_DQS_13	Y10	<del>X</del>
IO_L10P_T1_13	Y12	<del>X</del>
IO_L10N_T1_13	Y11	<del>X</del>
IO_L11P_T1_SRCC_13	Y12	<del>X</del>
IO_L11N_T1_SRCC_13	W11	<del>X</del>
IO_L12P_T1_MRCC_13	W12	<del>X</del>
IO_L12N_T1_MRCC_13	W13	<del>X</del>
IO_L13P_T2_MRCC_13	V14	<del>X</del>
IO_L13N_T2_MRCC_13	V15	<del>X</del>
IO_L14P_T2_SRCC_13	V14	<del>X</del>
IO_L14N_T2_SRCC_13	V14	<del>X</del>
IO_L15P_T2_DQS_13	V15	<del>X</del>
IO_L15N_T2_DQS_13	W15	<del>X</del>
IO_L16P_T2_13	W16	<del>X</del>
IO_L17P_T2_13	V16	<del>X</del>
IO_L17N_T2_13	V16	<del>X</del>

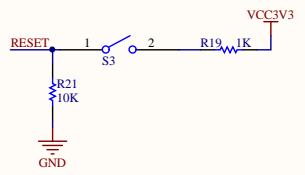
XC7A200T-2FBG4841

**U2C**

**BANK14**

IO_0_14	P20	SFP1_TX_FLT
IO_L1P_T0_D00_MOSI_14	R22	SPI_DQ0
IO_L1N_T0_D01_DIN_14	R22	SPI_DQ1
IO_L2P_T0_D02_14	P21	SPI_DQ2
IO_L2N_T0_D03_14	R21	SPI_DQ3
IO_L3P_T0_DQS_PUDCB_B_14	V22	R11_W10K
IO_L3N_T0_DQS_EMCCLK_14	V22	SFP1_TX_DSBL
IO_L4P_T0_D04_14	V21	SD_CS
IO_L4N_T0_D05_14	V21	SD_SDI
IO_L5P_T0_D06_14	P19	SD_SDO
IO_L5N_T0_D07_14	R19	SD_SCK
IO_L6P_T0_FCS_B_14	T19	SPI_CS_N
IO_L6N_T0_D08_VREF_14	T20	SFP1_MOD_ABS
IO_L7P_T1_D09_14	W21	LED0
IO_L7N_T1_D10_14	W22	LED1
IO_L8P_T1_D11_14	AA20	LED2
IO_L8N_T1_D12_14	AA21	SFP1_RS0
IO_L9P_T1_DQS_14	V22	SD_DATA1
IO_L9N_T1_DQS_D13_14	AB21	SFP1_RX_LOS
IO_L10P_T1_D14_14	AB22	SFP1_RS1
IO_L10N_T1_D15_14	U20	SFP2_TX_FLT
IO_L11P_T1_SRCC_14	V20	PG_C2M_R103_10K
IO_L11N_T1_SRCC_14	W20	PCIE_PERST
IO_L12P_T1_MRCC_14	Y18	TPM_CLK
IO_L12N_T1_MRCC_14	Y19	TPM_CS#
IO_L13P_T2_MRCC_14	V18	TPM_MOSI
IO_L13N_T2_MRCC_14	V19	TPM_MISO
IO_L14P_T2_SRCC_14	AA19	TPM_RST#
IO_L14N_T2_SRCC_14	AA20	EEPROM_SCL
IO_L15P_T2_DQS_RDWR_B_14	V17	EEPROM_SDA
IO_L15N_T2_DQS_DOUT_CSO_B_14	W17	BRSTNT_M2C_L
IO_L16P_T2_CSI_B_14	AA18	SFP1_SDA
IO_L16N_T2_A15_D31_14	AB18	SFP1_SCL
IO_L17P_T2_A14_D30_14	V17	SFP2_SDA
IO_L17N_T2_A13_D29_14	V18	SFP2_SCL
IO_L18P_T2_A12_D28_14	P14	FT234_TXD
IO_L18N_T2_A11_D27_14	R14	FT234_RXD
IO_L19P_T3_A10_D26_14	R18	FT234_RTS
IO_L19N_T3_A09_D25_VREF_14	V18	FT234_CTS
IO_L20P_T3_A08_D24_14	W17	FT234_CBUS0
IO_L20N_T3_A07_D23_14	V17	RESET
IO_L21P_T3_DQS_14	P15	SFP2_TX_DSBL
IO_L21N_T3_DQS_A06_D22_14	R16	SFP2_MOD_ABS
IO_L22P_T3_A05_D21_14	N13	FMC_SCL
IO_L22N_T3_A04_D20_14	N14	FMC_SDA
IO_L23P_T3_A03_D19_14	P16	SFP2_RS0
IO_L23N_T3_A02_D18_14	R17	SFP2_RX_LOS
IO_L24P_T3_A01_D17_14	N15	SFP2_RS1
IO_L24N_T3_A00_D16_14		
IO_25_14		

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**U2D**

**BANK15**

IO_0_15	A16	<del>X</del>
IO_L1P_T0_AD0P_15	H13	<del>X</del>
IO_L1N_T0_AD0N_15	G13	<del>X</del>
IO_L2P_T0_AD8P_15	G15	<del>X</del>
IO_L2N_T0_AD8N_15	G16	<del>X</del>
IO_L3P_T0_DQS_AD1P_15	H14	<del>X</del>
IO_L3N_T0_DQS_AD1N_15	G17	<del>X</del>
IO_L4P_T0_15	G18	<del>X</del>
IO_L4N_T0_15	V15	<del>X</del>
IO_L5P_T0_AD9P_15	H15	<del>X</del>
IO_L5N_T0_AD9N_15	H15	<del>X</del>
IO_L6P_T0_15	H17	<del>X</del>
IO_L6N_T0_VREF_15	H18	<del>X</del>
IO_L7P_T1_AD2P_15	V22	LA06_P
IO_L7N_T1_AD2N_15	V22	LA06_N
IO_L8P_T1_AD10P_15	H20	LA10_P
IO_L8N_T1_AD10N_15	H20	LA10_N
IO_L8N_T1_AD10N_15	H21	LA14_P
IO_L9P_T1_DQS_AD3P_15	H21	LA14_N
IO_L9N_T1_DQS_AD3N_15	H21	LA14_N
IO_L10P_T1_AD11P_15	M21	LA27_P
IO_L10N_T1_AD11N_15	M21	LA27_N
IO_L11P_T1_SRCC_15	M20	LA18_CC_P
IO_L11N_T1_SRCC_15	M21	LA18_CC_N
IO_L12P_T1_MRCC_15	H19	LA01_CC_N
IO_L12N_T1_MRCC_15	H18	CLK1_M2C_P
IO_L13P_T2_MRCC_15	K19	CLK1_M2C_N
IO_L13N_T2_MRCC_15	V19	LA05_P
IO_L14P_T2_SRCC_15	V20	LA05_N
IO_L14N_T2_SRCC_15	M22	LA09_P
IO_L15P_T2_DQS_15	M22	LA09_P
IO_L15N_T2_DQS_ADV_B_15	M22	LA09_N
IO_L16P_T2_A28_15	M18	LA13_P
IO_L16N_T2_A27_15	M18	LA13_N
IO_L17P_T2_A26_15	M18	LA23_P
IO_L17N_T2_A26_15	M19	LA23_N
IO_L18P_T2_A24_15	M20	LA26_P
IO_L18N_T2_A23_15	M20	LA26_N
IO_L19P_T3_A22_15	K13	<del>X</del>
IO_L19N_T3_A21_VREF_15	R14	<del>X</del>
IO_L20P_T3_A20_15	M13	<del>X</del>
IO_L20N_T3_A19_15	K13	<del>X</del>
IO_L21P_T3_DQS_15	V17	<del>X</del>
IO_L21N_T3_DQS_A18_15	V14	<del>X</del>
IO_L22P_T3_A17_15	V15	<del>X</del>
IO_L22N_T3_A16_15	V16	<del>X</del>
IO_L23P_T3_FOE_B_15	V16	<del>X</del>
IO_L23N_T3_FWE_B_15	V16	<del>X</del>
IO_L24P_T3_RS1_15	M15	<del>X</del>
IO_L24N_T3_RS0_15	M16	<del>X</del>
IO_25_15	M17	<del>X</del>

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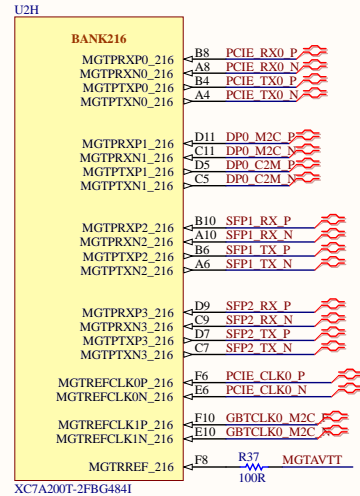
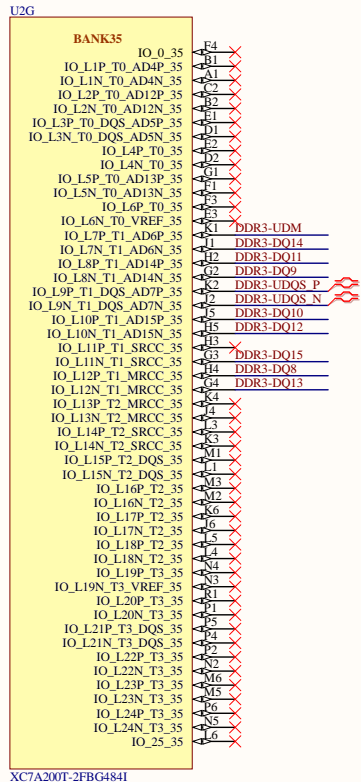
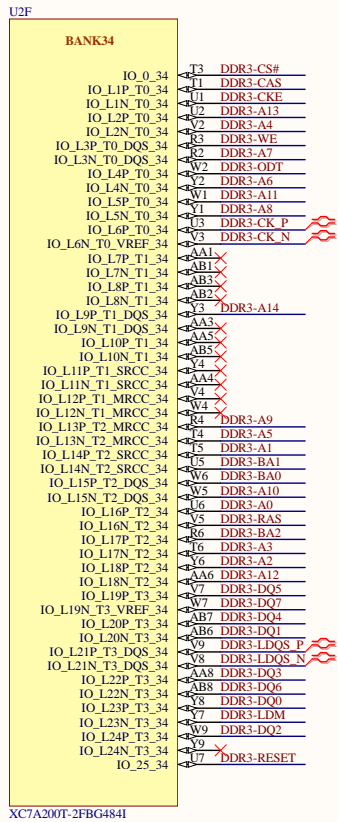
**U2E**

**BANK16**

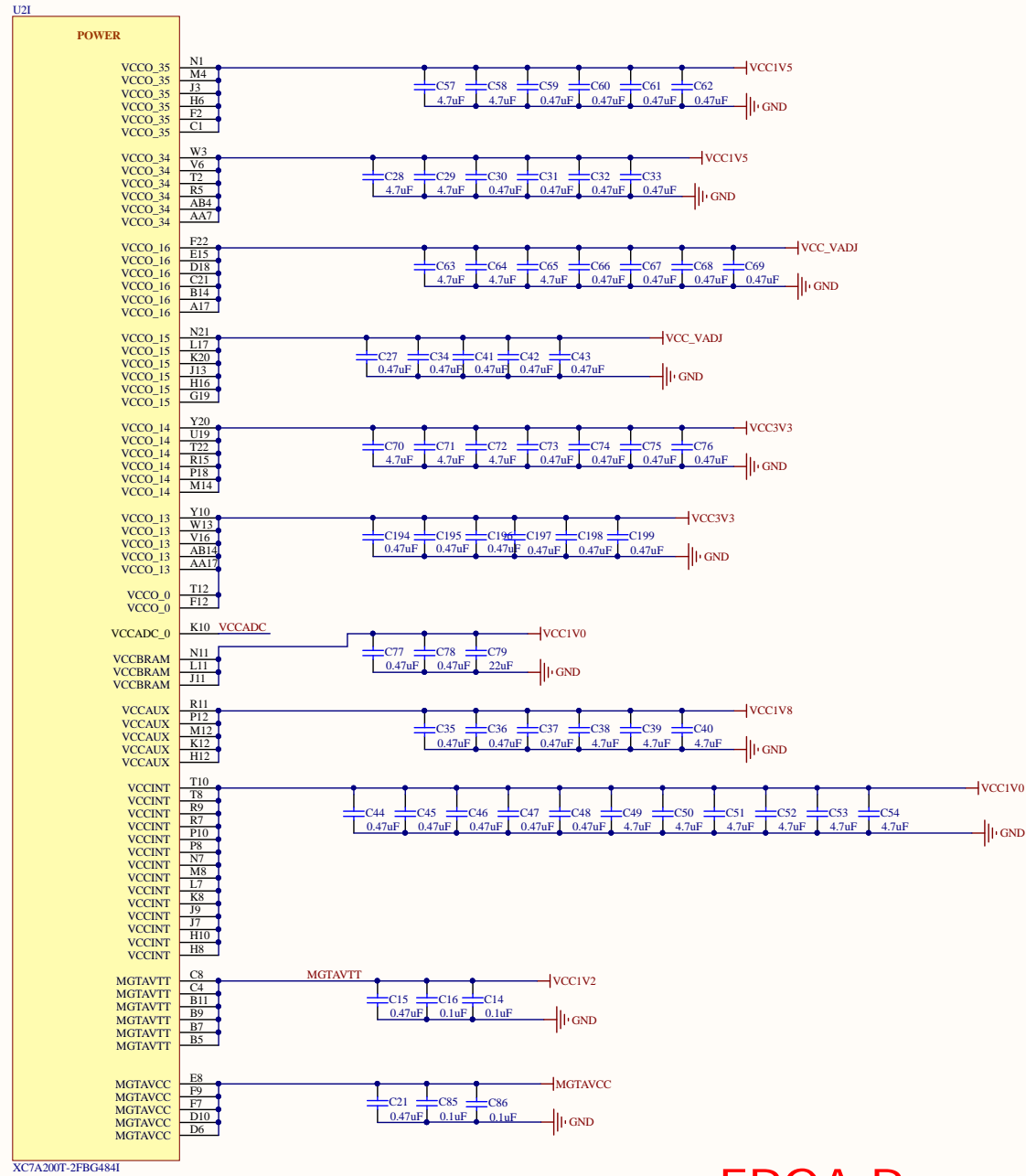
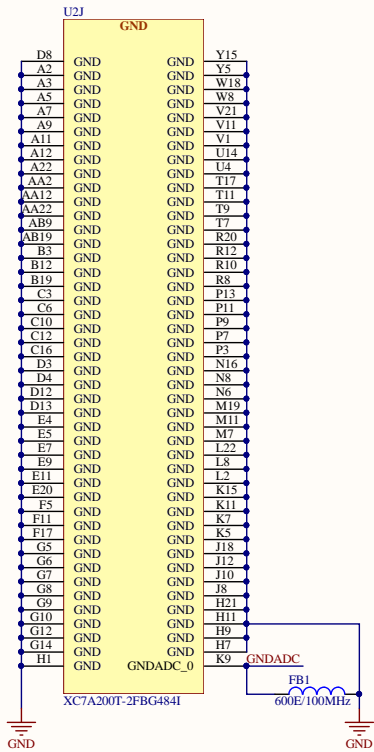
IO_0_16	F15	<del>X</del>
IO_L1P_T0_16	F13	LA03_P
IO_L1N_T0_16	F14	LA03_N
IO_L2P_T0_16	F16	LA08_P
IO_L2N_T0_16	E17	LA08_N
IO_L3P_T0_DQS_16	F14	LA12_P
IO_L3N_T0_DQS_16	F15	LA12_N
IO_L4P_T0_16	F13	LA16_P
IO_L4N_T0_16	F14	LA16_N
IO_L5P_T0_16	E16	LA20_P
IO_L5N_T0_16	F16	LA20_N
IO_L6P_T0_16	D14	LA22_P
IO_L6N_T0_VREF_16	D15	LA22_N
IO_L7P_T1_16	B15	LA25_P
IO_L7N_T1_16	B16	LA25_N
IO_L8P_T1_16	C13	LA29_P
IO_L8N_T1_16	B13	LA29_N
IO_L9P_T1_DQS_16	A15	LA31_P
IO_L9N_T1_DQS_16	A16	LA31_N
IO_L10P_T1_16	A13	LA33_P
IO_L10N_T1_16	A14	LA33_N
IO_L11P_T1_SRCC_16	B17	LA17_CC_P
IO_L11N_T1_SRCC_16	B18	LA17_CC_N
IO_L12P_T1_MRCC_16	D17	LA00_CC_P
IO_L12N_T1_MRCC_16	C17	LA00_CC_N
IO_L13P_T2_MRCC_16	C18	CLK0_M2C_P
IO_L13N_T2_MRCC_16	C19	CLK0_M2C_N
IO_L14P_T2_SRCC_16	E19	LA02_P
IO_L14N_T2_SRCC_16	D19	LA02_N
IO_L15P_T2_DQS_16	F18	LA04_P
IO_L15N_T2_DQS_16	E18	LA04_N
IO_L16P_T2_16	B20	LA07_P
IO_L16N_T2_16	A20	LA07_N
IO_L17P_T2_16	A18	LA11_P
IO_L17N_T2_16	A19	LA11_N
IO_L18P_T2_16	F19	LA15_P
IO_L18N_T2_16	F20	LA15_N
IO_L19P_T3_16	D20	LA19_P
IO_L19N_T3_VREF_16	C20	LA19_N
IO_L20P_T3_16	C22	LA21_P
IO_L20N_T3_16	B22	LA21_N
IO_L21P_T3_DQS_16	B21	LA24_P
IO_L21N_T3_DQS_16	A21	LA24_N
IO_L22P_T3_16	E22	LA28_P
IO_L22N_T3_16	D22	LA28_N
IO_L23P_T3_16	E21	LA30_P
IO_L23N_T3_16	D21	LA30_N
IO_L24P_T3_16	G21	LA32_P
IO_L24N_T3_16	G22	LA32_N
IO_25_16	F21	<del>X</del>

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# FPGA I/O Banks



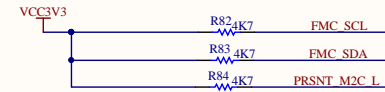
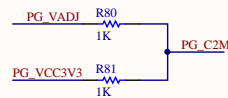
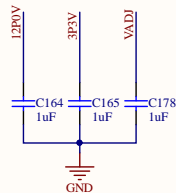
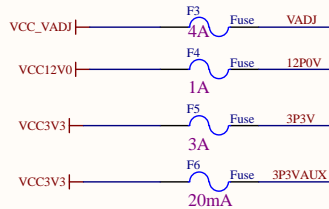
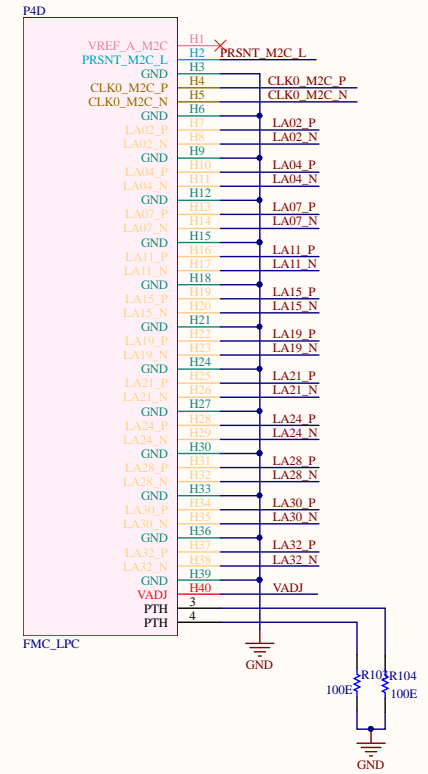
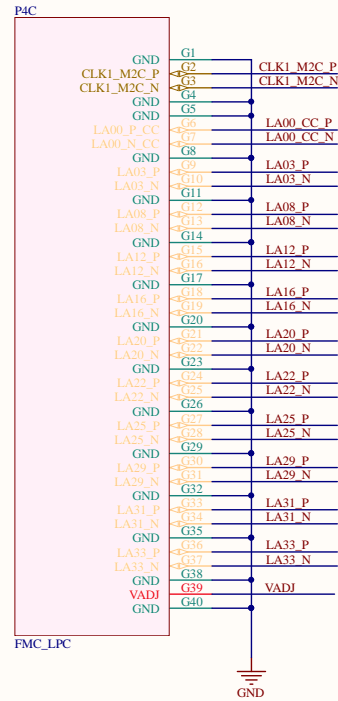
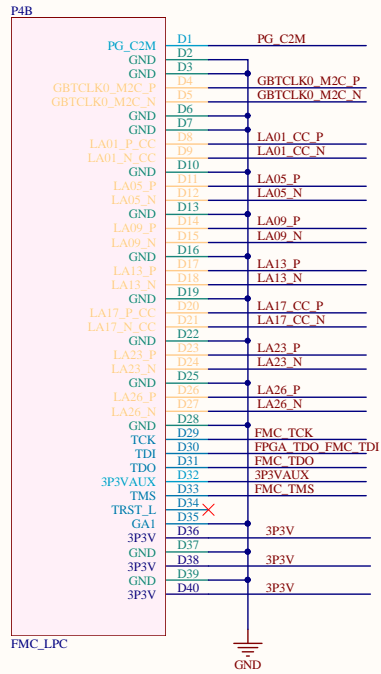
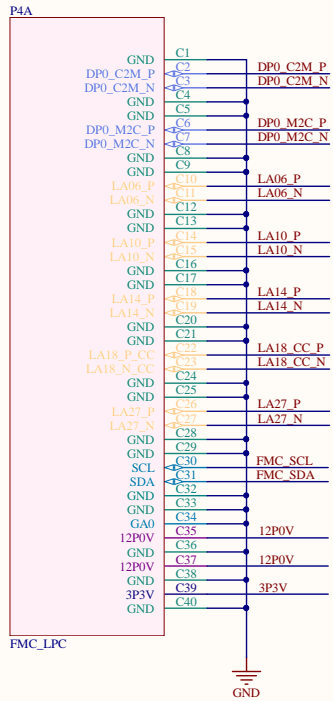
# FPGA DDR3 and PCIeX1 Banks



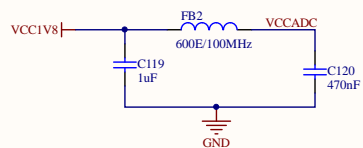
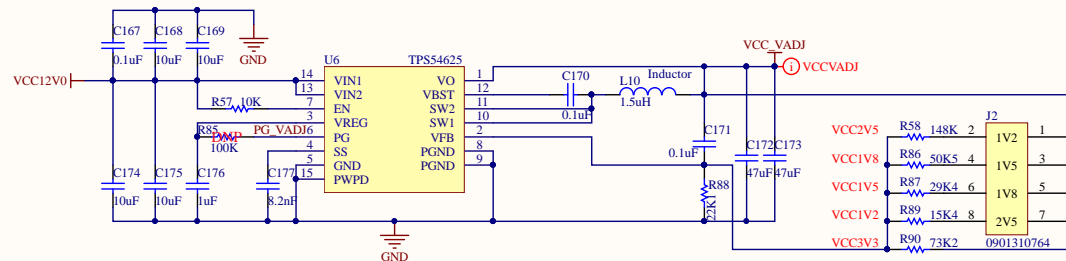
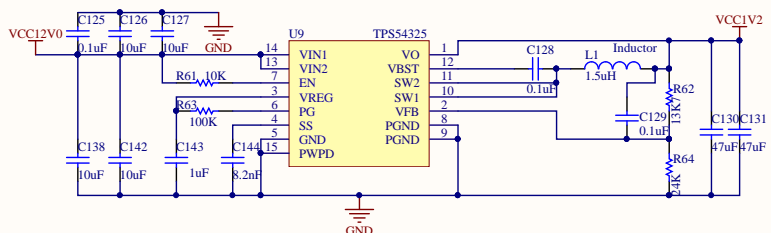
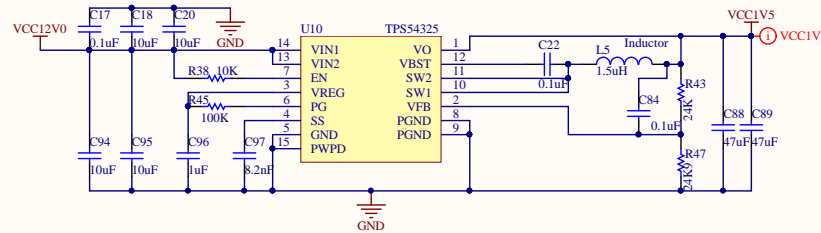
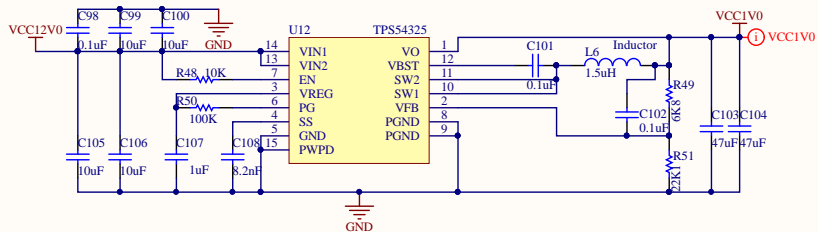
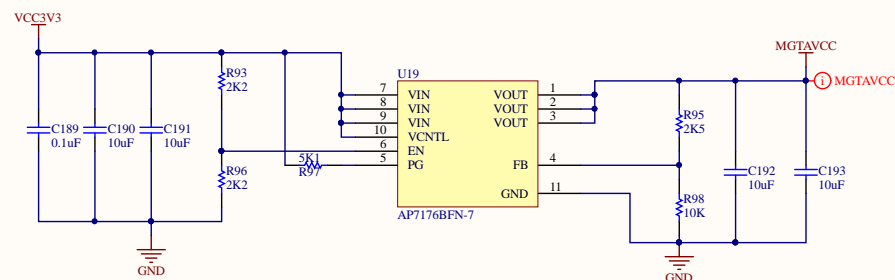
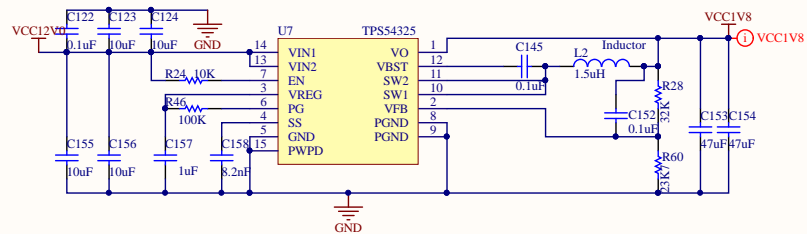
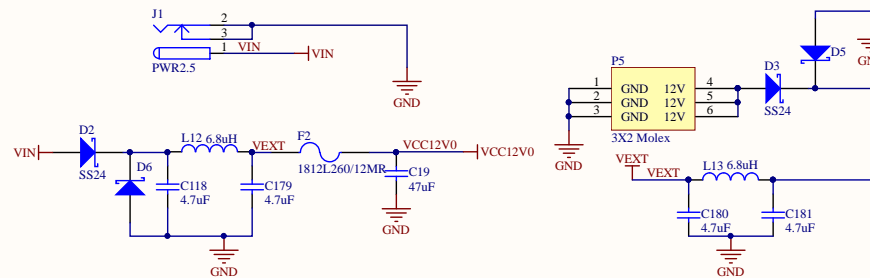
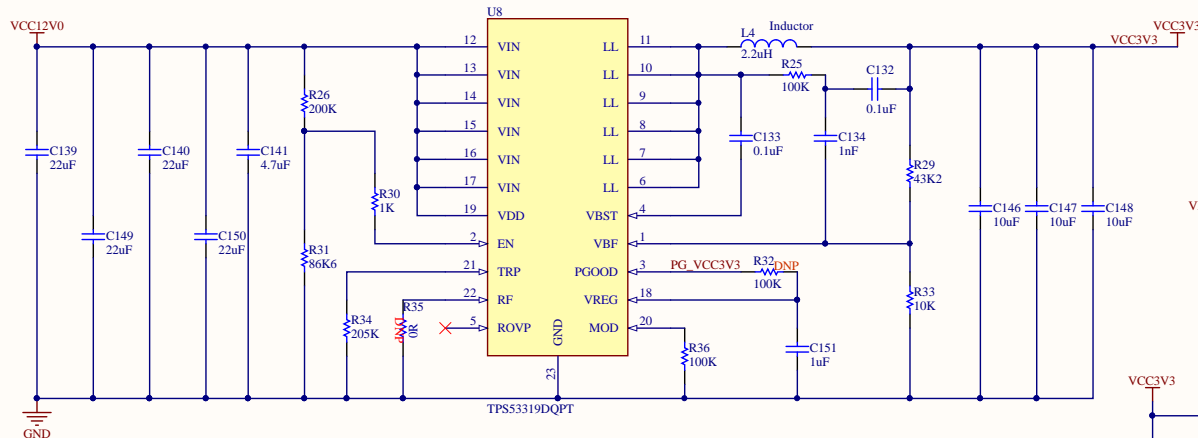
# FPGA Power

Title: <b>Tagus</b>	Revision: <b>V2.0</b>	Numato Systems Pvt Ltd	
Size: <b>A3</b>	Project: <b>Tagus.PjgPcb</b>		
Date: <b>6102-80-61</b>	Time: <b>0F:LE:60</b>		Sheet <b>6 of 9</b>
File: <b>FPGA_D.SchDoc</b>			





# FMC LPC HEADER

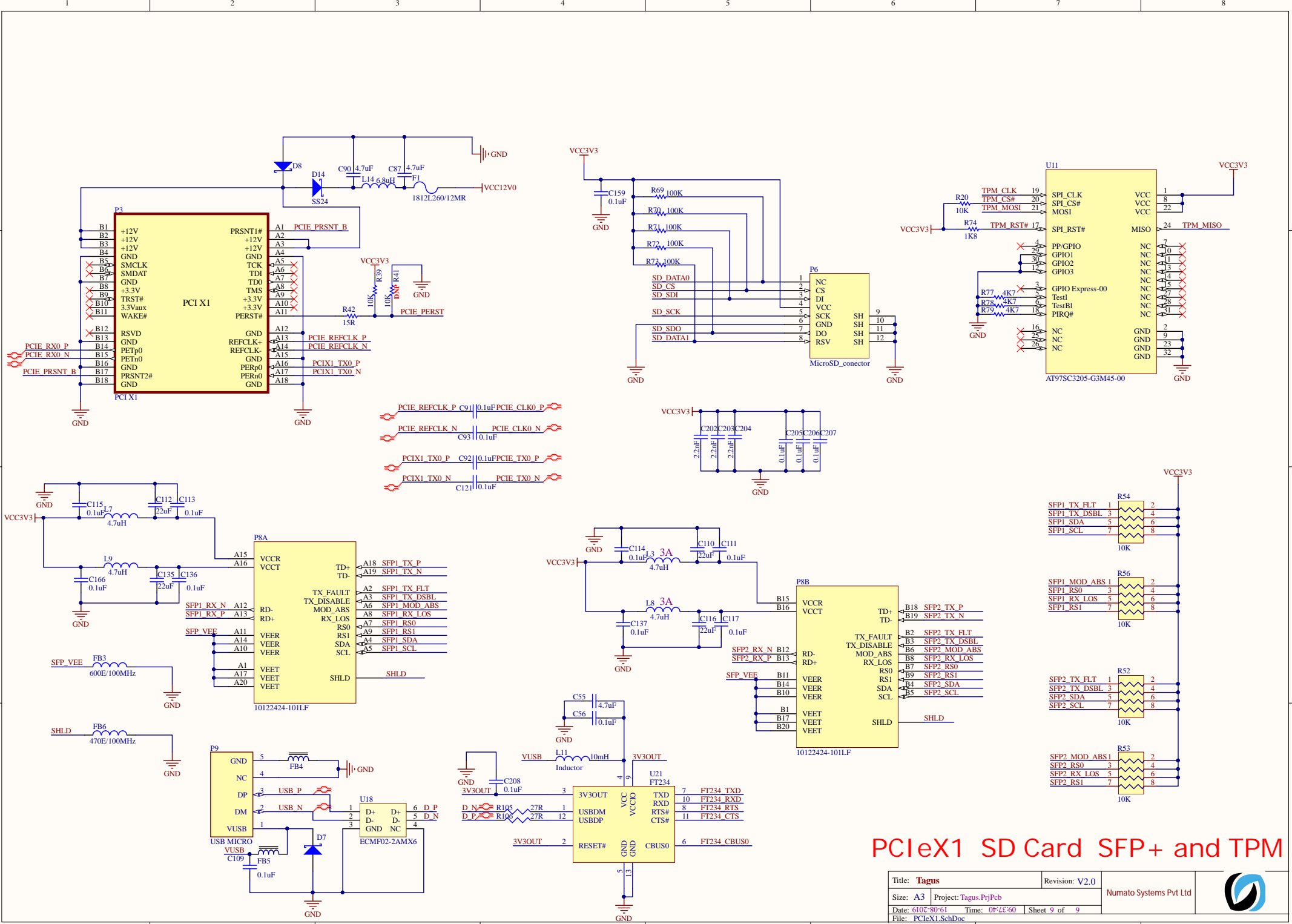


# Power Supply


Title: <b>Tagus</b>	Revision: <b>V2.0</b>	Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>Tagus.PjgPcb</b>	
Date: <b>6107-30-61</b>	Time: <b>0F:LE:60</b>	
File: <b>PowerSupply.SchDoc</b>	Sheet <b>8 of 9</b>	







# PCIeX1 SD Card SFP+ and TPM

Title: <b>Tagus</b>	Revision: <b>V2.0</b>	 <b>Numato Systems Pvt Ltd</b>
Size: <b>A3</b>	Project: <b>Tagus.PjgPcb</b>	
Date: <b>6102-80-61</b>	Time: <b>09:46:60</b>	
File: <b>PCIeX1.SchDoc</b>	Sheet <b>9</b> of <b>9</b>	