

*	Name	Node Count	Signal Length (mm)	Routed Length (mm)
False	CLK_N	5	0	17.935
False	CLK_P	5	0	17.971
False	CONN0_D1	2	47.698	47.747
False	CONN0_D2	2	47.698	47.698
False	CONN0_D3	2	47.698	47.698
False	CONN0_D4	2	47.698	47.698
False	CONN0_D5	2	47.698	47.713
False	CONN0_D6	2	47.698	47.698
False	CONN0_D7	2	47.698	47.712
False	CONN0_D8	2	47.698	47.698
False	CONN1_D1	2	75.528	75.528
False	CONN1_D2	2	75.47	75.47
False	CONN1_D3	2	75.47	75.47
False	CONN1_D4	2	75.47	75.47
False	CONN1_D5	2	75.483	75.483
False	CONN1_D6	2	75.47	75.47
False	CONN1_D7	2	75.47	75.47
False	CONN1_D8	2	75.47	75.47
False	DDR4_REFCLK_N	5	0	19.881
False	DDR4_REFCLK_P	5	0	19.881
False	DDR_CLK_N	2	5.625	5.611
False	DDR_CLK_P	2	5.589	5.579
False	FABRIC_CLK_N	2	6.689	6.689
False	FABRIC_CLK_P	2	6.705	6.713
False	NetR129_1	2	24.42	24.42
False	NetR130_1	2	24.384	24.384
False	NetR131_1	2	24.39	24.39
False	NetR132_1	2	24.39	24.39
False	NetR134_1	2	24.39	24.39
False	NetR142_1	2	24.39	24.39
False	SZG_PORTA_C2P_CLK_N	2	114.808	114.874
False	SZG_PORTA_C2P_CLK_P	2	114.808	114.874
False	SZG_PORTA_P2C_CLK_N	2	114.834	114.884

False	SZG_PORTA_P2C_CLK_P	2	114.834	114.884
False	SZG_PORTA_REFCLK_N	2	77.705	77.755
False	SZG_PORTA_REFCLK_P	2	77.705	77.755
False	SZG_PORTA_RX2_N	2	77.709	77.76
False	SZG_PORTA_RX2_P	2	77.709	77.758
False	SZG_PORTA_RX3_N	2	77.708	77.758
False	SZG_PORTA_RX3_P	2	77.707	77.76
False	SZG_PORTA_S0	2	114.824	114.827
False	SZG_PORTA_S1	2	114.824	114.822
False	SZG_PORTA_S2	2	114.545	114.558
False	SZG_PORTA_S3	2	114.824	114.824
False	SZG_PORTA_S4	2	114.798	114.799
False	SZG_PORTA_S5	2	114.824	114.838
False	SZG_PORTA_S6	2	114.808	114.808
False	SZG_PORTA_S7	2	114.824	114.828
False	SZG_PORTA_S8	2	114.808	114.809
False	SZG_PORTA_S9	2	114.873	114.891
False	SZG_PORTA_S10	2	114.82	114.811
False	SZG_PORTA_S11	2	114.824	114.827
False	SZG_PORTA_S12	2	114.824	114.825
False	SZG_PORTA_S13	2	114.824	114.826
False	SZG_PORTA_S14	2	114.824	114.834
False	SZG_PORTA_S15	2	114.808	114.81
False	SZG_PORTA_S16	2	114.828	114.836
False	SZG_PORTA_S17	2	114.824	114.824
False	SZG_PORTA_TX2_N	2	77.703	77.767
False	SZG_PORTA_TX2_P	2	77.703	77.768
False	SZG_PORTA_TX3_N	2	77.703	77.755
False	SZG_PORTA_TX3_P	2	77.703	77.753
False	SZG_PORTB_C2P_CLK_N	2	114.67	114.67
False	SZG_PORTB_C2P_CLK_P	2	114.681	114.681
False	SZG_PORTB_P2C_CLK_N	2	114.681	114.684
False	SZG_PORTB_P2C_CLK_P	2	114.681	114.683
False	SZG_PORTB_REFCLK_N	2	62.473	62.44

False	SZG_PORTB_REFCLK_P	2	62.471	62.476
False	SZG_PORTB_RX0_N	2	62.473	62.473
False	SZG_PORTB_RX0_P	2	62.473	62.473
False	SZG_PORTB_RX1_N	2	62.473	62.472
False	SZG_PORTB_RX1_P	2	62.473	62.473
False	SZG_PORTB_S0	2	114.681	114.683
False	SZG_PORTB_S1	2	114.681	114.691
False	SZG_PORTB_S2	2	114.681	114.683
False	SZG_PORTB_S3	2	114.681	114.681
False	SZG_PORTB_S4	2	114.681	114.691
False	SZG_PORTB_S5	2	114.706	114.711
False	SZG_PORTB_S6	2	114.681	114.683
False	SZG_PORTB_S7	2	114.681	114.685
False	SZG_PORTB_S8	2	114.679	114.679
False	SZG_PORTB_S9	2	114.681	114.628
False	SZG_PORTB_S10	2	114.681	114.688
False	SZG_PORTB_S11	2	114.681	114.902
False	SZG_PORTB_S12	2	114.751	114.752
False	SZG_PORTB_S13	2	114.681	114.682
False	SZG_PORTB_S14	2	114.681	114.686
False	SZG_PORTB_S15	2	114.68	114.682
False	SZG_PORTB_S16	2	114.751	114.756
False	SZG_PORTB_S17	2	114.681	114.698
False	SZG_PORTB_TX0_N	2	62.473	62.473
False	SZG_PORTB_TX0_P	2	62.473	62.473
False	SZG_PORTB_TX1_N	2	62.473	62.473
False	SZG_PORTB_TX1_P	2	62.473	62.473
False	SZG_PORTC_C2P_CLK_N	2	89.151	89.162
False	SZG_PORTC_C2P_CLK_P	2	89.157	89.164
False	SZG_PORTC_D0_N	2	89.154	89.187
False	SZG_PORTC_D0_P	2	89.154	89.176
False	SZG_PORTC_D1_N	2	89.154	89.394
False	SZG_PORTC_D1_P	2	89.154	89.155
False	SZG_PORTC_D2_N	2	89.154	89.166

False	SZG_PORTC_D2_P	2	89.154	89.155
False	SZG_PORTC_D3_N	2	89.151	89.169
False	SZG_PORTC_D3_P	2	89.157	89.172
False	SZG_PORTC_D4_N	2	89.154	89.179
False	SZG_PORTC_D4_P	2	89.154	89.169
False	SZG_PORTC_D5_N	2	89.154	89.398
False	SZG_PORTC_D5_P	2	89.154	89.175
False	SZG_PORTC_D6_N	2	89.154	89.173
False	SZG_PORTC_D6_P	2	89.154	89.162
False	SZG_PORTC_D7_N	2	89.154	89.18
False	SZG_PORTC_D7_P	2	89.154	89.175
False	SZG_PORTC_P2C_CLK_N	2	89.154	89.161
False	SZG_PORTC_P2C_CLK_P	2	89.154	89.165
False	SZG_PORTC_S16	2	89.154	89.16
False	SZG_PORTC_S17	2	89.154	88.942
False	SZG_PORTC_S18	2	89.171	89.164
False	SZG_PORTC_S19	2	89.164	89.381
False	SZG_PORTC_S20	2	89.175	89.234
False	SZG_PORTC_S21	2	89.154	89.36
False	SZG_PORTC_S22	2	89.154	91.105
False	SZG_PORTC_S23	2	89.154	89.366
False	SZG_PORTC_S24	2	89.154	89.181
False	SZG_PORTC_S25	2	89.154	89.154
False	SZG_PORTC_S26	2	89.154	89.178
False	SZG_PORTC_S27	2	89.154	89.16
False	V1_N	3	0	31.829
False	V1_P	3	0	31.829
False	VREF_N	5	0	27.869
False	VREF_P	3	0	27.869
True	DDR4-A0	3	0	48.694
True	DDR4-A1	3	0	48.675
True	DDR4-A2	3	0	48.694
True	DDR4-A3	3	0	48.694
True	DDR4-A4	3	0	48.716

True	DDR4-A5	3	0	48.716
True	DDR4-A6	3	0	48.758
True	DDR4-A7	3	0	48.694
True	DDR4-A8	3	0	48.716
True	DDR4-A9	3	0	48.694
True	DDR4-A10_AP	3	0	48.703
True	DDR4-A11	3	0	48.716
True	DDR4-A12_BC_n	3	0	48.695
True	DDR4-A13	3	0	48.716
True	DDR4-A14_WE_n	3	0	48.716
True	DDR4-A15_CAS_n	3	0	48.674
True	DDR4-A16_RAS_n	3	0	48.716
True	DDR4-ACT_n	3	0	48.716
True	DDR4-BA0	3	0	48.692
True	DDR4-BA1	3	0	48.692
True	DDR4-BG0	3	0	49.041
True	DDR4-CKE	3	0	48.722
True	DDR4-CK_N	3	0	48.692
True	DDR4-CK_P	3	0	48.692
TRUE	DDR4-CS_n	3	0	48.716
True	DDR4-DQ0	2	35.703	35.703
True	DDR4-DQ1	2	35.703	35.71
True	DDR4-DQ2	2	35.703	35.703
True	DDR4-DQ3	2	35.703	35.754
True	DDR4-DQ4	2	35.703	35.703
True	DDR4-DQ5	2	35.702	35.717
True	DDR4-DQ6	2	35.687	35.687
True	DDR4-DQ7	2	35.703	35.703
True	DDR4-DQ8	2	35.703	35.703
True	DDR4-DQ9	2	35.682	35.684
True	DDR4-DQ10	2	35.703	35.703
True	DDR4-DQ11	2	35.702	35.703
True	DDR4-DQ12	2	35.703	35.707
True	DDR4-DQ13	2	35.703	35.703

True	DDR4-DQ14	2	35.703	35.703
True	DDR4-DQ15	2	35.703	35.704
True	DDR4-ODT	3	0	48.692
True	DDR4-RESET_n	3	0	48.765
True	DDR4_DM0	2	35.71	35.719
True	DDR4_DM1	2	35.703	35.703
True	DDR4_DQS0_N	2	35.654	35.654
True	DDR4_DQS0_P	2	35.703	36.036
True	DDR4_DQS1_N	2	35.705	35.705
True	DDR4_DQS1_P	2	35.705	35.705
True	D_N	4	0	107.712
True	D_P	4	0	107.71
True	ETH_RXD0	4	0	61.279
True	ETH_RXD1	4	0	61.334
True	ETH_RXD2	3	0	61.278
True	ETH_RXD3	3	0	61.279
True	ETH_RX_CLK	4	0	61.279
True	ETH_RX_CTL	4	0	61.261
True	ETH_TXCLK	2	63.997	63.969
True	ETH_TXD0	2	64.008	63.979
True	ETH_TXD1	2	63.997	63.968
True	ETH_TXD2	2	64.008	63.98
True	ETH_TXD3	2	64.008	63.979
True	ETH_TX_CTL	2	63.997	63.969
True	FTDI-D0	2	62.737	62.737
True	FTDI-D1	2	62.736	62.737
True	FTDI-D2	2	62.737	62.737
True	FTDI-D3	2	62.736	62.737
True	FTDI-D4	2	62.737	62.737
True	FTDI-D5	2	62.737	62.737
True	FTDI-D6	2	62.737	62.737
True	FTDI-D7	2	62.737	62.737
True	FTDI-RD#	2	62.737	62.737
True	FTDI-RXF#	2	62.737	62.738

True	FTDI-SIWUA	2	62.737	62.737
True	FTDI-TXE#	2	62.737	62.742
True	FTDI-WR#	2	62.737	62.737
True	GPHY_ATXRX_N	2	13.351	13.386
True	GPHY_ATXRX_P	2	13.361	13.361
True	GPHY_BTXXRX_N	2	13.37	13.34
True	GPHY_BTXXRX_P	2	13.37	13.365
True	GPHY_CTXRX_N	2	13.344	13.382
True	GPHY_CTXRX_P	2	13.362	13.396
True	GPHY_DTXRX_N	2	13.361	13.395
True	GPHY_DTXRX_P	2	13.36	13.391
True	SPI_CS_N	3	0	39.827
True	SPI_DQ0	3	0	39.827
True	SPI_DQ1	3	0	39.827
True	SPI_DQ2	3	0	39.86
True	SPI_DQ3	3	0	39.827
True	SPI_SCK	3	0	39.836
True	V_N	2	81.148	81.148
True	V_P	2	81.148	81.148