

*	Name	Node Count	Signal Length (mm)	Routed Length (mm)
False	DDR_CLK_N	2	5.506	5.492
False	DDR_CLK_P	2	5.51	5.5
False	FABRIC_CLK_N	2	6.689	6.689
False	FABRIC_CLK_P	2	6.705	6.713
True	GPHY_CTXRX_N	2	13.344	13.382
True	GPHY_ATXRX_N	2	13.351	13.386
True	GPHY_DTXRX_P	2	13.36	13.391
True	GPHY_ATXRX_P	2	13.361	13.76
True	GPHY_DTXRX_N	2	13.361	13.395
True	GPHY_CTXRX_P	2	13.362	13.624
True	GPHY_BTXXRX_N	2	13.37	13.34
True	GPHY_BTXXRX_P	2	13.37	13.365
False	CONN0_D3	2	22.523	22.544
False	CONN0_D4	2	22.523	22.59
False	CONN0_D7	2	22.523	22.53
False	CONN0_D5	2	22.566	22.566
False	CONN0_D6	2	22.566	22.573
False	CONN0_D1	2	22.599	22.599
False	CONN0_D2	2	22.599	22.599
False	CONN0_D8	2	22.599	22.608
False	NetR129_1	2	24.39	24.39
False	NetR130_1	2	24.39	24.39
False	NetR131_1	2	24.39	24.39
False	NetR132_1	2	24.39	24.39
False	NetR134_1	2	24.39	24.39
False	NetR142_1	2	24.39	24.39
False	CONN1_D3	2	25.16	25.162
False	CONN1_D4	2	25.16	25.175
False	CONN1_D7	2	25.16	25.171
False	CONN1_D5	2	25.202	25.202
False	CONN1_D6	2	25.202	25.209
False	CONN1_D1	2	25.235	25.235
False	CONN1_D2	2	25.235	25.235
False	CONN1_D8	2	25.235	25.258
True	DDR4_DQS0_N	2	35.7	35.7
True	DDR4_DQS0_P	2	35.7	35.7
True	DDR4-DQ5	2	35.702	35.717
True	DDR4-DQ11	2	35.702	35.703
True	DDR4-DQ0	2	35.703	35.703
True	DDR4-DQ1	2	35.703	35.71
True	DDR4-DQ2	2	35.703	35.703
True	DDR4-DQ3	2	35.703	35.754
True	DDR4-DQ4	2	35.703	35.703
True	DDR4-DQ6	2	35.703	35.703
True	DDR4-DQ7	2	35.703	35.703

True	DDR4-DQ8	2	35.703	35.703
True	DDR4-DQ9	2	35.703	35.705
True	DDR4-DQ10	2	35.703	35.703
True	DDR4-DQ12	2	35.703	35.707
True	DDR4-DQ13	2	35.703	35.703
True	DDR4-DQ14	2	35.703	35.703
True	DDR4-DQ15	2	35.703	35.704
True	DDR4_DQS1_N	2	35.705	35.705
True	DDR4_DQS1_P	2	35.706	35.707
True	DDR4_DM0	2	35.71	35.719
True	DDR4_DM1	2	35.721	35.721
False	SZG_PORTA_TX0_N	2	50.257	50.257
False	SZG_PORTA_TX0_P	2	50.257	50.257
False	SZG_PORTA_TX1_N	2	50.281	50.281
False	SZG_PORTA_TX1_P	2	50.281	50.281
False	CON0_D1	2	52.151	52.151
False	CON0_D4	2	52.164	52.198
False	CON0_D2	2	52.17	52.17
False	CON0_D3	2	52.17	52.177
False	CON0_D5	2	52.17	52.17
False	CON0_D6	2	52.17	52.17
False	CON0_D7	2	52.17	52.17
False	CON0_D8	2	52.17	52.17
False	CON1_D2	2	56.163	56.163
False	CON1_D1	2	56.179	56.198
False	CON1_D3	2	56.179	56.179
False	CON1_D4	2	56.179	56.208
False	CON1_D5	2	56.179	56.179
False	CON1_D6	2	56.179	56.179
False	CON1_D7	2	56.179	56.179
False	CON1_D8	2	56.179	56.179
False	SZG_PORTA_RX0_N	2	58.703	58.703
False	SZG_PORTA_RX0_P	2	58.703	58.703
False	SZG_PORTA_RX1_N	2	58.704	58.702
False	SZG_PORTA_RX1_P	2	58.704	58.703
False	SZG_PORTB_TX2_N	2	58.984	59.047
False	SZG_PORTB_TX2_P	2	58.984	59.049
False	SZG_PORTB_TX3_N	2	58.985	59.037
False	SZG_PORTB_TX3_P	2	58.985	59.035
False	SZG_PORTB_RX3_N	2	62.094	62.143
False	SZG_PORTB_RX3_P	2	62.094	62.147
False	SZG_PORTB_RX2_N	2	62.112	62.161
False	SZG_PORTB_RX2_P	2	62.112	62.161
False	SZG_PORTA_REFCLK_P	2	62.313	62.318
False	SZG_PORTA_REFCLK_N	2	62.315	62.282
True	FTDI-D1	2	62.736	62.737
True	FTDI-D3	2	62.736	62.737

True	FTDI-D0	2	62.737	62.737
True	FTDI-D2	2	62.737	62.737
True	FTDI-D4	2	62.737	62.737
True	FTDI-D5	2	62.737	62.737
True	FTDI-D6	2	62.737	62.737
True	FTDI-D7	2	62.737	62.737
True	FTDI-RD#	2	62.737	62.737
True	FTDI-RXF#	2	62.737	62.738
True	FTDI-SIWUA	2	62.737	62.737
True	FTDI-TXE#	2	62.737	62.742
True	FTDI-WR#	2	62.737	62.737
True	ETH_TXCLK	2	63.997	63.969
True	ETH_TXD1	2	63.997	63.968
True	ETH_TX_CTL	2	63.997	63.969
True	ETH_TXD0	2	64.008	63.979
True	ETH_TXD2	2	64.008	63.98
True	ETH_TXD3	2	64.008	63.979
False	SZG_PORTA_C2P_CLK_N	2	64.147	64.147
False	SZG_PORTA_C2P_CLK_P	2	64.15	64.15
False	SZG_PORTA_P2C_CLK_N	2	70.368	70.371
False	SZG_PORTA_P2C_CLK_P	2	70.368	70.371
False	SZG_PORTC_C2P_CLK_P	2	73.606	73.613
False	SZG_PORTC_D2_P	2	73.648	73.642
False	SZG_PORTC_P2C_CLK_N	2	73.649	73.656
False	SZG_PORTC_D4_P	2	73.653	73.668
False	SZG_PORTC_D2_N	2	73.654	73.637
False	SZG_PORTC_D4_N	2	73.654	73.679
False	SZG_PORTC_D6_N	2	73.654	73.676
False	SZG_PORTC_D6_P	2	73.654	73.663
False	SZG_PORTC_P2C_CLK_P	2	73.655	73.666
False	SZG_PORTC_C2P_CLK_N	2	73.659	73.671
False	SZG_PORTC_D3_N	2	73.797	74.031
False	SZG_PORTC_D0_P	2	73.799	73.792
False	SZG_PORTC_D1_P	2	73.799	73.81
False	SZG_PORTC_D0_N	2	73.804	73.787
False	SZG_PORTC_D1_N	2	73.804	73.584
False	SZG_PORTC_D5_P	2	73.804	73.797
False	SZG_PORTC_D7_N	2	73.804	74.027
False	SZG_PORTC_D7_P	2	73.804	74.021
False	SZG_PORTC_D3_P	2	73.805	74.034
False	SZG_PORTC_D5_N	2	73.81	73.585
False	SZG_PORTB_REFCLK_P	2	76.016	76.065
False	SZG_PORTB_REFCLK_N	2	76.035	76.084
False	SZG_PORTC_S20	2	76.628	76.634
False	SZG_PORTC_S22	2	76.628	76.651
False	SZG_PORTC_S24	2	76.628	76.646
False	SZG_PORTC_S26	2	76.628	76.64

False	SZG_PORTC_S19	2	76.77	76.987
False	SZG_PORTC_S16	2	76.778	76.848
False	SZG_PORTC_S17	2	76.778	76.566
False	SZG_PORTC_S18	2	76.778	76.772
False	SZG_PORTC_S21	2	76.778	76.984
False	SZG_PORTC_S23	2	76.778	76.99
False	SZG_PORTC_S25	2	76.778	76.992
False	SZG_PORTC_S27	2	76.778	76.984
True	V_N	2	86.175	86.175
True	V_P	2	86.175	86.175
False	SZG_PORTB_C2P_CLK_N	2	98.944	99.009
False	SZG_PORTB_C2P_CLK_P	2	98.944	99.01
False	SZG_PORTB_P2C_CLK_N	2	104.757	104.807
False	SZG_PORTB_P2C_CLK_P	2	104.757	104.806
False	SZG_PORTB_S0	2	114.824	114.827
False	SZG_PORTB_S1	2	114.824	114.822
False	SZG_PORTB_S3	2	114.824	114.824
False	SZG_PORTB_S4	2	114.824	114.825
False	SZG_PORTB_S5	2	114.824	114.838
False	SZG_PORTB_S7	2	114.824	114.828
False	SZG_PORTB_S9	2	114.824	114.827
False	SZG_PORTB_S10	2	114.824	114.815
False	SZG_PORTB_S11	2	114.824	114.827
False	SZG_PORTB_S12	2	114.824	114.825
False	SZG_PORTB_S13	2	114.824	114.826
False	SZG_PORTB_S14	2	114.824	114.834
False	SZG_PORTB_S15	2	114.824	114.827
False	SZG_PORTB_S16	2	114.824	114.832
False	SZG_PORTB_S17	2	114.824	114.824
False	SZG_PORTB_S2	2	114.975	114.988
False	SZG_PORTB_S6	2	114.975	114.976
False	SZG_PORTB_S8	2	114.975	114.975
False	SZG_PORTA_S2	2	115.888	115.89
False	SZG_PORTA_S3	2	115.927	115.927
False	SZG_PORTA_S4	2	115.927	115.937
False	SZG_PORTA_S11	2	115.927	115.983
False	SZG_PORTA_S12	2	115.927	115.927
False	SZG_PORTA_S15	2	115.927	115.929
False	SZG_PORTA_S16	2	115.927	115.932
False	SZG_PORTA_S0	2	116.077	116.079
False	SZG_PORTA_S1	2	116.077	116.087
False	SZG_PORTA_S5	2	116.077	116.083
False	SZG_PORTA_S6	2	116.077	116.079
False	SZG_PORTA_S7	2	116.077	116.081
False	SZG_PORTA_S8	2	116.077	116.077
False	SZG_PORTA_S9	2	116.077	116.025
False	SZG_PORTA_S10	2	116.077	116.084

False	SZG_PORTA_S13	2	116.077	116.078
False	SZG_PORTA_S14	2	116.077	116.082
False	SZG_PORTA_S17	2	116.077	116.094
False	CLK_N	5	n/a	19.505
False	CLK_P	5	n/a	19.557
True	DDR4-A0	3	n/a	48.694
True	DDR4-A1	3	n/a	48.72
True	DDR4-A2	3	n/a	48.694
True	DDR4-A3	3	n/a	48.716
True	DDR4-A4	3	n/a	48.716
True	DDR4-A5	3	n/a	48.694
True	DDR4-A6	3	n/a	48.716
True	DDR4-A7	3	n/a	48.694
True	DDR4-A8	3	n/a	48.716
True	DDR4-A9	3	n/a	48.694
True	DDR4-A10_AP	3	n/a	48.703
True	DDR4-A11	3	n/a	48.716
True	DDR4-A12_BC_n	3	n/a	48.695
True	DDR4-A13	3	n/a	48.716
True	DDR4-A14_WE_n	3	n/a	48.694
True	DDR4-A15_CAS_n	3	n/a	48.694
True	DDR4-A16_RAS_n	3	n/a	48.716
True	DDR4-ACT_n	3	n/a	48.716
True	DDR4-BA0	3	n/a	48.716
True	DDR4-BA1	3	n/a	48.697
True	DDR4-BG0	3	n/a	48.974
True	DDR4-CKE	3	n/a	48.714
True	DDR4-CK_N	3	n/a	48.659
True	DDR4-CK_P	3	n/a	48.659
True	DDR4-CS_n	3	n/a	48.716
True	DDR4-ODT	3	n/a	48.692
True	DDR4-RESET_n	3	n/a	48.725
False	DDR4_REFCLK_N	5	n/a	19.881
False	DDR4_REFCLK_P	5	n/a	19.881
True	D_N	4	n/a	107.712
True	D_P	4	n/a	107.71
False	ETH_MDIO	3	n/a	65.326
True	ETH_RXD0	4	n/a	61.165
True	ETH_RXD1	4	n/a	61.186
True	ETH_RXD2	3	n/a	61.188
True	ETH_RXD3	3	n/a	61.188
True	ETH_RX_CLK	4	n/a	61.145
True	ETH_RX_CTL	4	n/a	61.188
True	SPI_CS_N	3	n/a	39.878
True	SPI_DQ0	3	n/a	39.878
True	SPI_DQ1	3	n/a	39.878
True	SPI_DQ2	3	n/a	39.88

True	SPI_DQ3	3	n/a	39.878
True	SPI_SCK	3	n/a	39.898
False	SYZYG_PORT_SCL	7	n/a	276.964
False	SYZYG_PORT_SDA	7	n/a	285.436
False	V1_N	3	n/a	31.666
False	V1_P	3	n/a	31.666
False	VREF_N	5	n/a	27.834
False	VREF_P	3	n/a	27.834